Specifications for

Blanview TFT-LCD Monitor

Version 1.0
(Please be sure to check the specifications latest version.)

MODEL COM57H5M	87KTC
Customer's Approval	
Signature:	
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Section:	
Title:	
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1. Application

This Specification is applicable to 14.4cm (5.7 inch) Blanview TFT-LCD monitor for non-military use.

- ORTUS TECHNOLOGY makes no warranty or assume no liability that use of this Product and/or any information including drawings in this Specification by Purchaser is not infringing any patent or other intellectual property rights owned by third parties, and ORTUS TECHNOLOGY shall not grant to Purchaser any right to use any patent or other intellectual property rights owned by third parties. Since this Specification contains ORTUS TECHNOLOGY's confidential information and copy right, Purchaser shall use them with high degree of care to prevent any unauthorized use, disclosure, duplication, publication or dissemination of ORTUS TECHNOLOGY'S confidential information and copy right.
- O If Purchaser intends to use this Products for an application which requires higher level of reliability and/or safety in functionality and/or accuracy such as transport equipment (aircraft, train, automobile, etc.), disaster-prevention/security equipment or various safety equipment, Purchaser shall consult ORTUS TECHNOLOGY on such use in advance.
- This Product shall not be used for application which requires extremely higher level of reliability and/or safety such as aerospace equipment, telecommunication equipment for trunk lines, control equipment for nuclear facilities or life-support medical equipment.
- ① It must be noted as an mechaniacl design manner, especial attention in housing design to prevent arcuation/flexureor caused by stress to the LCD module shall be considered.
- ORTUS TECHNOLOGY assumes no liability for any damage resulting from misuse, abuse, and/or miss-operation of the Product deviating from the operating conditions and precautions described in the Specification.
- ORTUS TECHNOLOGY is not responsible for any nonconformities and defects that are not specified in this specifications.
- Olf any issue arises as to information provided in this Specification or any other information, ORTUS TECHNOLOGY and Purchaser shall discuss them in good faith and seek solution.
- ORTUS TECHNOLOGY assumes no liability for defects such as electrostatic discharge failure occurred during peeling off the protective film or Purchaser's assembly process.

This Product is compatible for RoHS directive.

Object substance	Maximum content [ppm]
Cadmium and its compound	100
Hexavalent Chromium Compound	1000
Lead & Lead compound	1000
Mercury & Mercury compound	1000
Polybrominated biphenyl series (PBB series)	1000
Polybrominated biphenyl ether series (PBDE series)	1000

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2. Outline Specifications

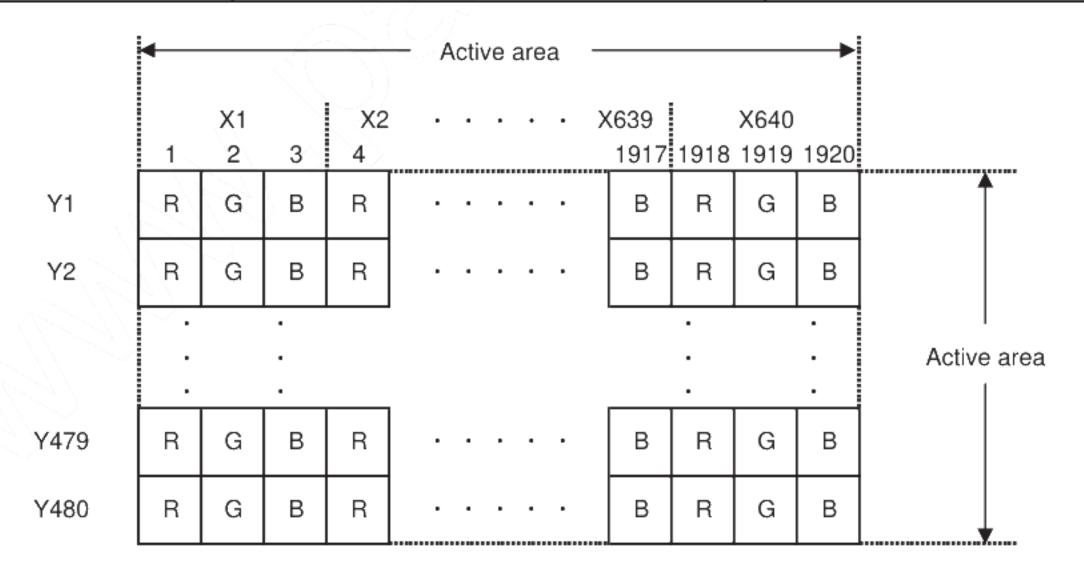
2.1 Features of the Product

- 5.7 inch diagonal display, 1,920 [H] x 480 [V] dots.
- 6-bit 262,144 color display capability.
- Built in Timing generator (TG).
- Long life & High bright white LED back-light.
- Blanview TFT-LCD, improved outdoor readability.

	Ind	oor	Outdoor			
	Readability	Readability	Power Efficiency (Battery Life)			
Transmissive	Good	Good	Fair	Poor		
Transflective	Fair	Poor	Good	Good		
Blanview	Good	Good	Good	Good		

2.2 Display Method

Items	Specifications	Remarks
Display type	TN type 262,144 colors.	
	Blanview, Normally white.	
Driving method	a-Si TFT Active matrix.	
	Line-scanning, Non-interlace.	
Dot arrangement	RGB stripe arrangement.	Refer to "Dot arrangement".
Signal input method	6-bit RGB, parallel input.	
Backlight type	Long life & High bright white LED.	
Touch panel	Resistance type,transmissive analog tablet	Surface finishing:Clear



Dot arrangement (FPC cable placed down)

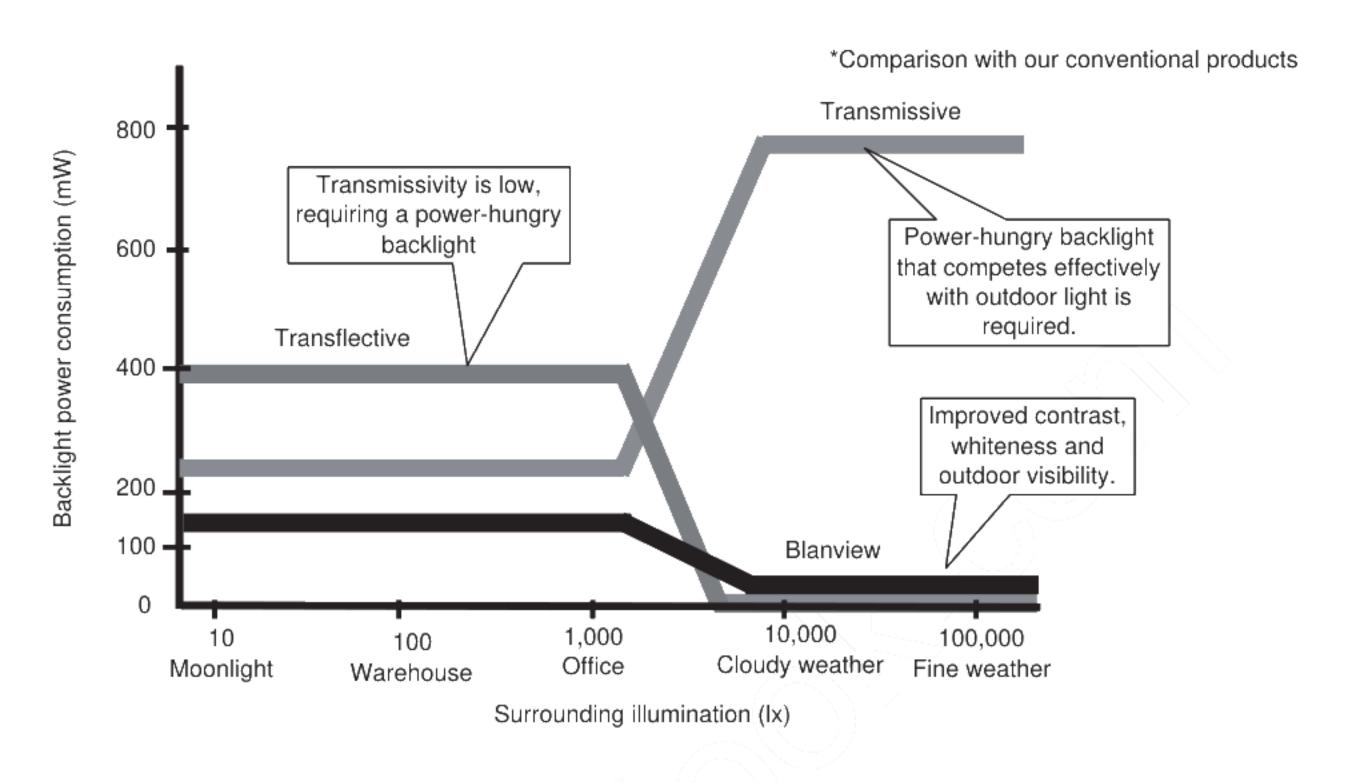
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<Features of Blanview>

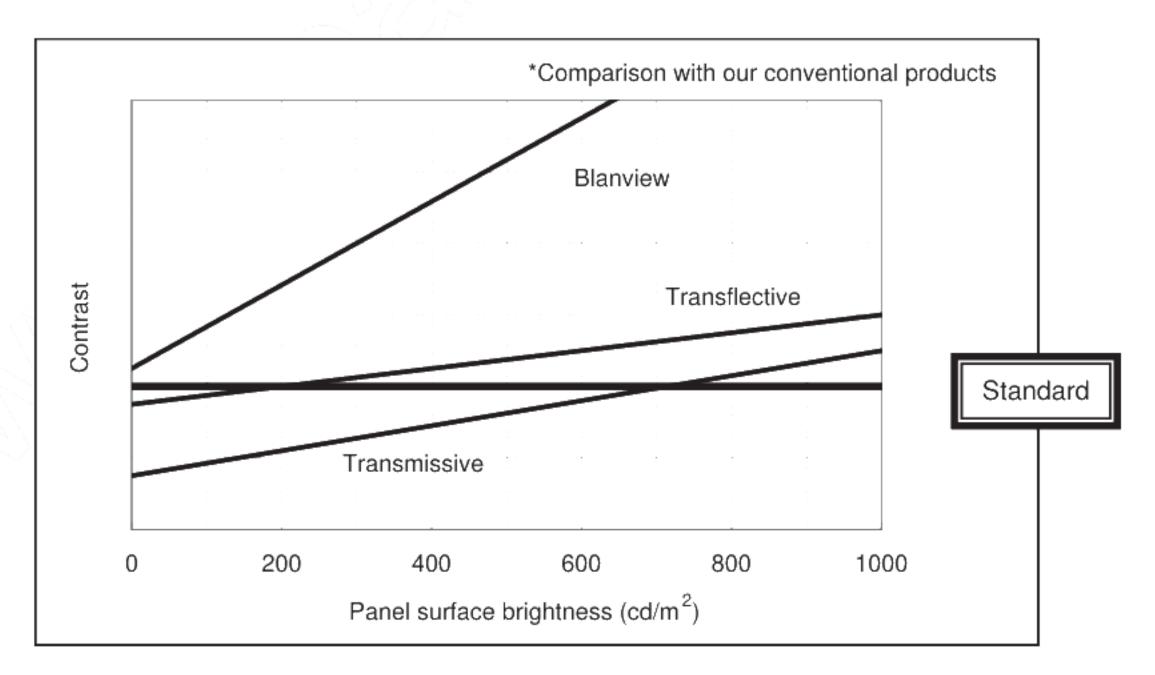
- Backlight power consumption required to assure visibility. (equivalent to 3.5"QVGA)



- Contrast characteristics under 100,000lx. (same condition as direct sunlight.)

With better contrast (higher contrast ratio), Blanview TFT-LCD has the best outdoor readability in three different types of TFT-LCD.

Below chart shows contrast value against panel surface brightness. (Horizontal: Panel surface brightness/ Vertical: Contrast value) LCD panel has enough outdoor readability above our Standard line. (ORTUS TECHNOLOGY criteria)



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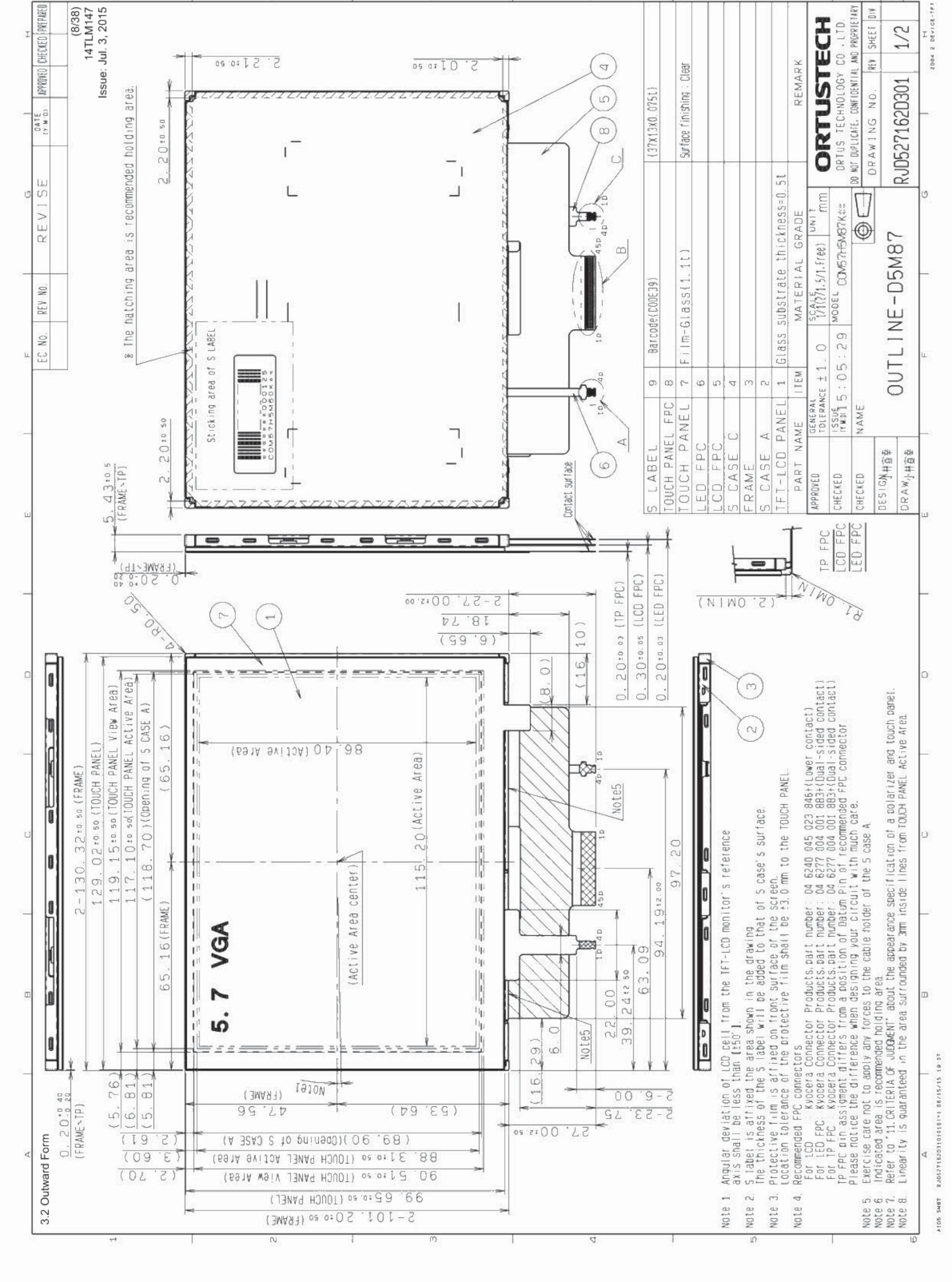
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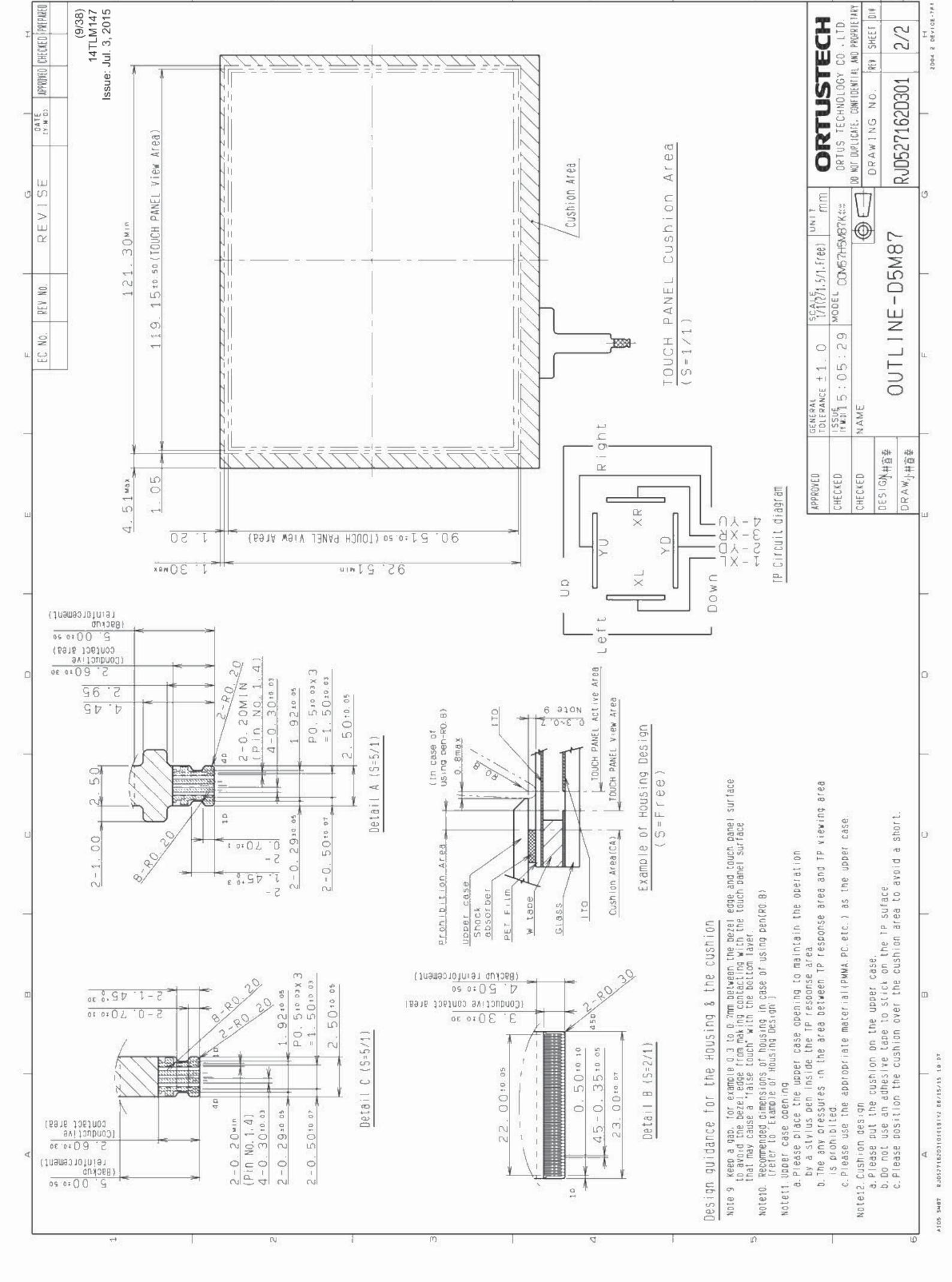
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3. Dimensions and Shape

3.1 Dimensions

Items	Specifications	Unit	Remarks
Outline dimensions	130.32[H] × 101.20[V] × 5.43[D]	mm	Exclude FPC cable.
Active area	115.20[H] × 86.40[V]	mm	14.4cm diagonal.
Number of dots	1,920[H] × 480[V]	dot	
Dot pitch	60.00[H] × 180.00[V]	μm	
Hardness of	3	Н	Load:4.9N,Angle:45°
Touch Panel surface			Reference judgment standard:JIS-K5600
Weight	125	g	Include FPC cable.





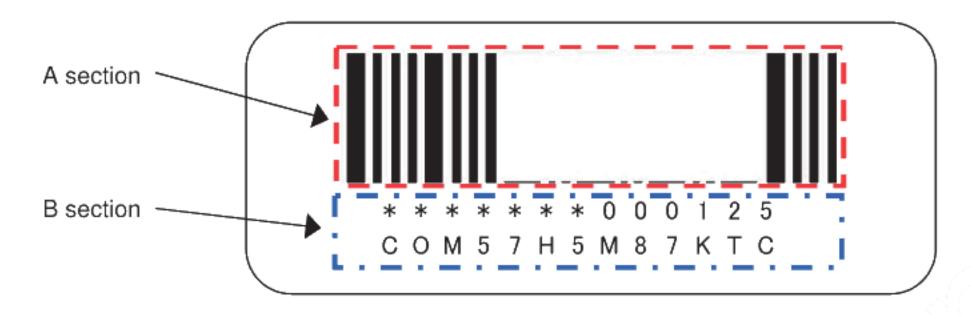
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3.3 SERIAL LABEL (S-LABEL)

1) Display Items

A section : Bar code

B section: Combination of a character



Details of B section

Upper culumn: It indicates The least significant digit of manufacture year (1 digit), manufacture month with below alphabet (1letter), model code (5characters), serial number (6digits).

* * ***** a b c d

_										
	Contents of display									
а	The least significant	The least significant digit of manufacture year								
b	Manufacture month	Jan-A	Mar-C	May-E	Jul-G	Sep-I	Nov-K			
		Feb-B	Apr-D	Jun-F	Aug-H	Oct-J	Dec-L			
С	Model code	57FAC	57FAC (Made in Japan)							
		57FBC	(Made in Mal	aysia)						
				ΔΫ́						
d	Serial number			7						

- * Example of indication of Serial label (S-label)
- Made in Japan

6A57FAC000125

means "manufactured in Jan. 2015, 5.7" FA type, C specifications, serial number 000125"

·Made in Malaysia

6A57FBC000125

means "manufactured in Jan. 2015, 5.7" FB type, C specifications, serial number 000125"

Lower culumn: Model (13characters)

- Location of Serial Label (S-label)Refer to 3.2 "Outward Form".
- 3) Others

Bar code readablity is excluded from quality assurance coverage.

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4. Pin Assignment

4.1 Display Module Part

No.	Symbol	Function
1	VSS	GND.
2	VGL	Negative voltage for gate driver.
3	VDD	Power supply for logic circuit.
4	VGH	Positive voltage for gate driver.
5	AVDD	Power supply for analog circuit.
6	V10	Source driver output level voltage. (negative case)
7	V6	Source driver output level voltage. (negative case)
8	V5	Source driver output level voltage. (positive case)
9	V1	Source driver output level voltage. (positive case)
10	POCB	Power on clear. (Low: Active)
11	DISP	Display on/off control signal.(Lo:display off, Hi:display on)
12	RL	Horizontally Flipped (right/left) signal. (Lo: Horizontally Flipped Display, Hi: Normal display)
13	UD	Vertically Flipped (up/down) signal. (Lo: Normal display,Hi: Vertically Flipped Display)
14	VSS	GND.
15	VDD	Power supply for logic circuit.
16	DE	Input data effective signal. (It is effective for the period of "Hi")
17	HSYNC	Horizontal sync signal. (Low active)
18	VSYNC	Vertical sync signal. (Low active)
19	CLK	Clock signal.Latching data at the rising edge.
20	TEST5	Short to VSS.
21	TEST6	Short to VSS.
22	D00	Display data(R).
23	D01	00h: Black
24	D02	D00:LSB D05:MSB
25	D03	Deliver has interest actions a services
26	D04	Driver has internal gamma conversion.
27	D05	Short to VCC
28 29	TEST3	Short to VSS. Short to VSS.
30	D10	
31	D10	Display data(G). 00h: Black
32	D12	D10:LSB D15:MSB
33	D12	DIG.LOD DIG.MOD
34	D14	Driver has internal gamma conversion.
35	D15	
36	TEST1	Short to VSS.
37	TEST2	Short to VSS.
38	D20	Display data(B).
39	D21	00h: Black
40	D22	D20:LSB D25:MSB
41	D23	
42	D24	Driver has internal gamma conversion.
43	D25	
44	VCOM	Input signal for common electrode.
45	VSS	GND.

- Recommended connector: KYOCERA connector products, 6240 series (04 6240 045 023 846+)
- Please make sure to check a consistency between pin assignment in "3.2 Outward Form" and your connector pin assignment when designing your circuit.
 Inconsistency in input signal assignment may cause a malfunction.
- Since FPC cable has gold plated terminals, gilt finish contact shoe connector is recommended.

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4.2 Backlight Part

No.	Symbol	Function
1	BLH1	Backlight drive 1 (anode side).
2	BLH2	Backlight drive 2 (anode side).
3	BLL2	Backlight drive 2 (cathode side).
4	BLL1	Backlight drive 1 (cathode side).

- Recommended connector: KYOCERA connector products, 6277 series [04 6277 004 001 883+]
- Please make sure to check a consistency between pin assignment in "3.2 Outward Form" and your connector pin assignment when designing your circuit. Inconsistency in input signal assignment may cause a malfunction.
- Since FPC cable has gold plated terminals, gilt finish contact shoe connector is recommended.

4.3 Touch Panel Part

No.	Symbol	Function
1	XL	X-axis left terminal
2	YD	Y-axis down terminal
3	XR	X-axis right terminal
4	YU	Y-axis up terminal

- Recommended connector: KYOCERA connector products 6277 series [04 6277 004 001 883+]
- Please make sure to check a consistency between pin assignment in "3.2 Outward Form" and your connector pin assignment when designing your circuit. Inconsistency in input signal assignment may cause a malfunction.
- Since FPC cable has gold plated terminals, gilt finish contact shoe connector is recommended.

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5. Absolute Maximum Rating

VSS-0V

						V22=UV	
Item	Symbol	Condition	Rating		Unit	Applicable terminal	
	-		MIN MAX				
Supply voltage for logic	VDD	Ta=25°C	-0.3	7.0	V	VDD	
Supply voltage for analog1	AVDD	Note 1	-0.3	13.5	V	AVDD	
Supply voltage for analog2	VGH]	-0.3	27.0	V	VGH	
Supply voltage for analog3	VGL]	VGH-27.0	0.3	V	VGL	
Supply voltage for analog4	Vγ]	-0.3	AVDD-0.1	V	V1,V5,V6,V10	
Note 2	-						
Input voltage for logic	VI]	-0.3	VDD+0.3	V	CLK,VSYNC,HSYNC,DE	
						D[25:20],D[15:10],D[05:00],RL,	
						UD,TEST1~6,DISP,POCB	
Common electrode voltage	VCOM	1	-0.3	10.0	V	VCOM	
LED direction current	IL	Ta=25°C		35	mA	BLH1 - BLL1,BLH2 - BLL2	
of order		Ta=70°C		15			
Touch Panel input voltage	VIT			7.0	V	XR,XL,YU,YD	
Storage temperature range Tstg			-30	80	°C		
Storage humidity range Hstg		Non condensing in an environmental					
	moisture at or less than 40°C90%RH.						
lated. Disease refer to the IID areas On IOH Company of this decrement							

Note1: Please refer to the "Power On/Off Sequence" section of this document.

Note2: AVDD>V1>V5>V6>V10>VSS.

6. Recommended Operating Conditions

VSS=0V

Item	Symbol	Condition	Rating			Unit	Applicable terminal
TO THE	Cymbol	Condition	MIN	TYP	MAX	011110	Applicable terminal
Supply voltage for logic	VDD	Ta=25°C	3.0	3.3	3.6	V	VDD
Supply voltage for analog1	AVDD	1	11.0	12.0	13.0	V	AVDD
Supply voltage for analog2	VGH]	20.0	21.0	22.0	V	VGH
Supply voltage for analog3	VGL]	-8.0	-7.0	-6.0	V	VGL
Common electrode voltage	VCOM	1	4.2	4.7	5.2	V	VCOM
Note 1))			
	V1]	10.3	10.6	10.9	V	V1
Contrast range	V5] //	6.9	7.2	7.5	V	V5
	V6] ((5.2	5.5	5.8	V	V6
	V10	J ()	0.7	0.8	0.9	V	V10
Input voltage for logic	VI		0		VDD	V	CLK,VSYNC,HSYNC,
			b				DE,D[25:20],D[15:10],
							D[05:00],RL,UD,DISP,
	//	\Box A\\\\					POCB
Operating temperature	Top	Note 2,3	-20	25	70	°C	Panel surface
range		22					temperature
Operating humidity range		Ta≦30°C	20		80	%	
Нор		Ta>30°C	Non condensing in				
\wedge			an environmental moisture at or				
Latert This was a fault at a star				0°C80%RH.			

Note1: This range indicates the most probable range for the optimal setting for VCOM.

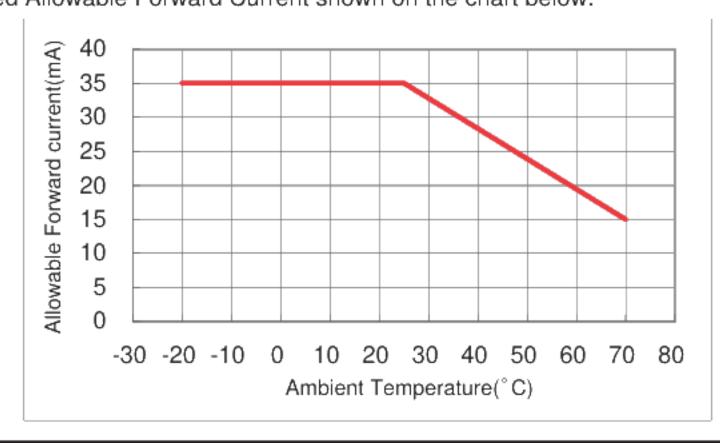
It does not mean that the optimal settings for VCOM for all monitors will be in this range.

VCOM should be optimized by viewing/using the monitor.

Note2: This monitor is operatable in this temperature range. With regard to optical characteristics,

refer to Item "10. CHARACTERISTICS".

Note3: Acceptable Forward Current to LED is up to 15mA, when Ta=+70°C. Do not exceed Allowable Forward Current shown on the chart below.



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7. Characteristics

7.1 DC Characteristics

7.1.1 Display Module

(Unless otherwise noted, Ta=25°C, VDD=3.3V, VSS=0V)

Itom	Symbol	Condition	· · · · · · · · · · · · · · · · · · ·			Unit	Applicable terminal
Item	Syllibol	Condition		Rating		Offic	Applicable terminal
			MIN	TYP	MAX		
Input voltage	VIH		0.7×VDD		VDD	V	CLK,VSYNC,HSYNC,
for logic							DE,D[25:20],D[15:10],
	VIL		0		0.3×VDD	V	D[05:00],RL,UD,DISP,
							POCB_
Pull up	Rpu		300	450	600	kΩ	DISP,POCB
resister value							
Pull down	Rpd		300	450	600	kΩ	DE,D[25:20],D[15:10],
resister value						/	D[05:00],TEST1~6,
Current	IDD	fCLK=25MHz		7.0	14.0	mA	VDD
consumption		Color bar display			/	5/Vc.	<i>1</i> 2
	IAVDD	VDD=3.3V		14.0	28.0	mA	AVDD
		AVDD=12.0V				IJŻ	
	IGH	VGH=21.0V		120	240	μA	VGH
		VGL=-7.0V		//	L		
	IGL	1	-240	-120		μA	VGL

7.1.2 Backlight

Item	Symbol	Condition		Rating		Unit	Applicable terminal
			MIN	TYP	MAX		
Forward current	IL25	Ta=25°C	Z = 1	10.2	35.0	mA	BLH1 - BLL1
	IL70	Ta=70°C			15.0	mA	BLH2 - BLL2
Forward voltage	VL	Ta=25°C,IL=10.2mA		24.5	25.7	V	
Estimated Life	LL	Ta=25°C,IL=10.2mA		(50,000)		hr	
of LED		Note					

Note: - The lifetime of the LED is defined as a period till the brightness of the LED decreases to the half of its initial value.

- This figure is given as a reference purpose only, and not as a guarantee.
- This figure is estimated for an LED operating alone.
 As the performance of an LED may differ when assembled as a monitor together with a TFT panel due to different environmental temperature.
- Estimated lifetime could vary on a different temperature and usually higher temperature could reduce the life significantly.

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7.1.3 Touch Panel

Ta=25" C

Item	Symbol	Condition		Rating		Unit	Applicable terminal
			MIN	TYP	MAX		
Linearity	LE	Note	-1.5		1.5	%	
Insulation	RI	DC 25V	20			МΩ	XR,XL-YU,YD
resistance							
Terminal		X	300		1000	Ω	XR,XL
resistance		Υ	100		600		YU,YD
Rated voltage		DC		5.0	7.0	V	XR,XL,YU,YD
on/off chattering		R0.8mm Polyacetal pen.			10	ms	

Note: -Please refer to "3.2 Outward Form" for the range of the guarantee.

-Linearity Measurement:Refer to the APPENDIX of "Reference Method for Measuring Optical Characteristics". Load:2.45N

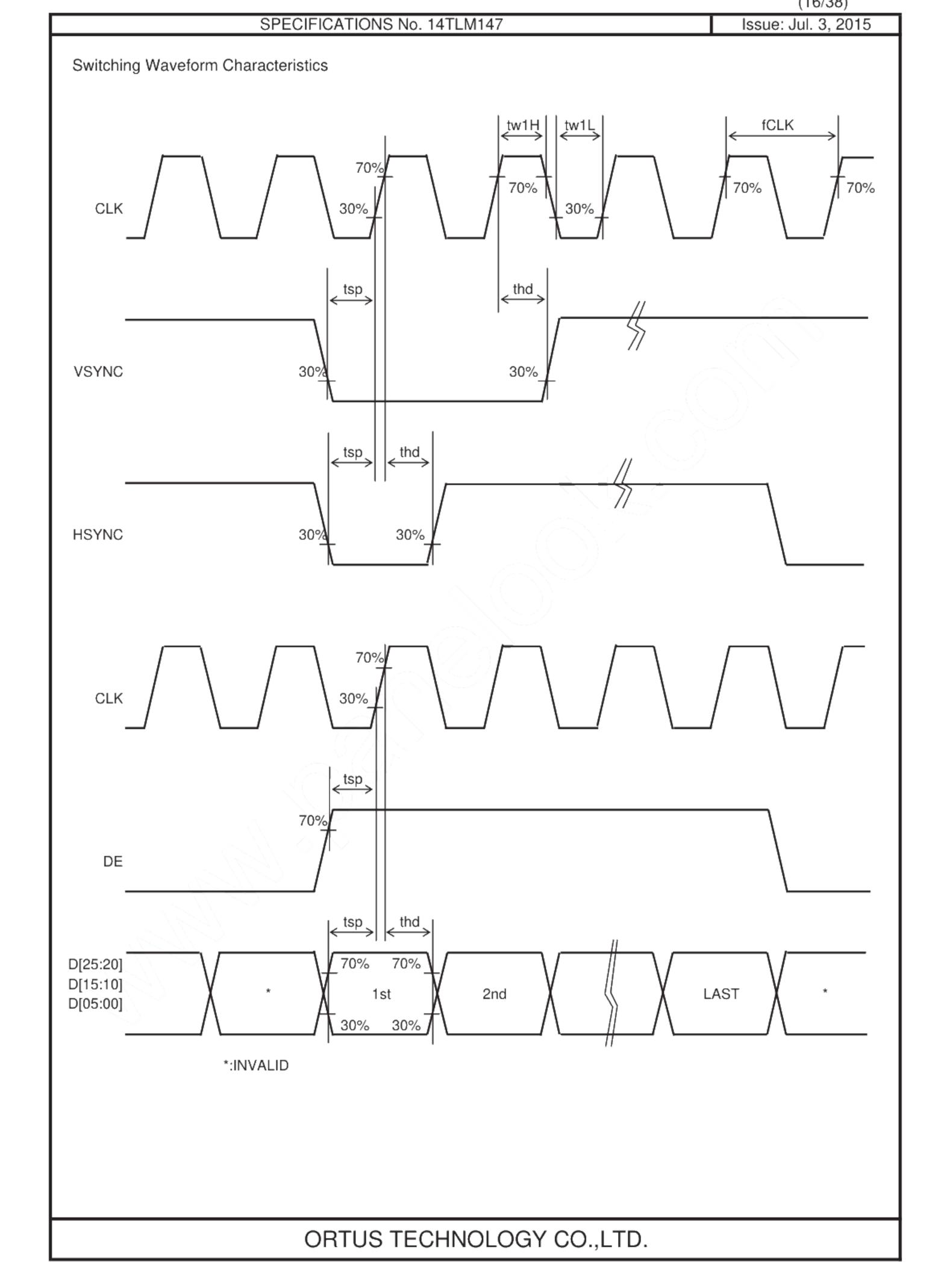
Mechanical Characteristics

Item		Rating		Unit	Remark
	MIN	TYP	MAX		
Detectable activation force	0.05		1.20	N	R0.8mm Polyacetal pen or finger.
					Resistance between X and Y axis must be
				,,,,	equal or lower than 2KΩ.
Keystroke durability				177	key the same part by silicon rubber.
	1,000,000			times	(Touch panel Active area only)
			(/~		-Rubber tip part: R8mm
					-Load: 2.45N
					-speed: 2times/second

7.2 AC Characteristics

(Unless otherwise noted, Ta=25 °C,VDD=3.3V,VSS=0V)

Item	Symbol	Condition		Rating		Unit	Applicable terminal
			MIN	TYP	MAX		
CLK frequency	fCLK			25	27	MHz	CLK
CLK Low period	tw1L	0.3×VDD or less.	14.8			ns	CLK
CLK High period	tw1H	0.7×VDD or more.	14.8			ns	CLK
Setup time	tsp		10			ns	CLK,DE,D[25:20],
Hold time	thd	\Diamond \Diamond	10			ns	D[15:10],D[05:00],
	10011						HSYNC,VSYNC



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7.3 Input Timing Characteristics

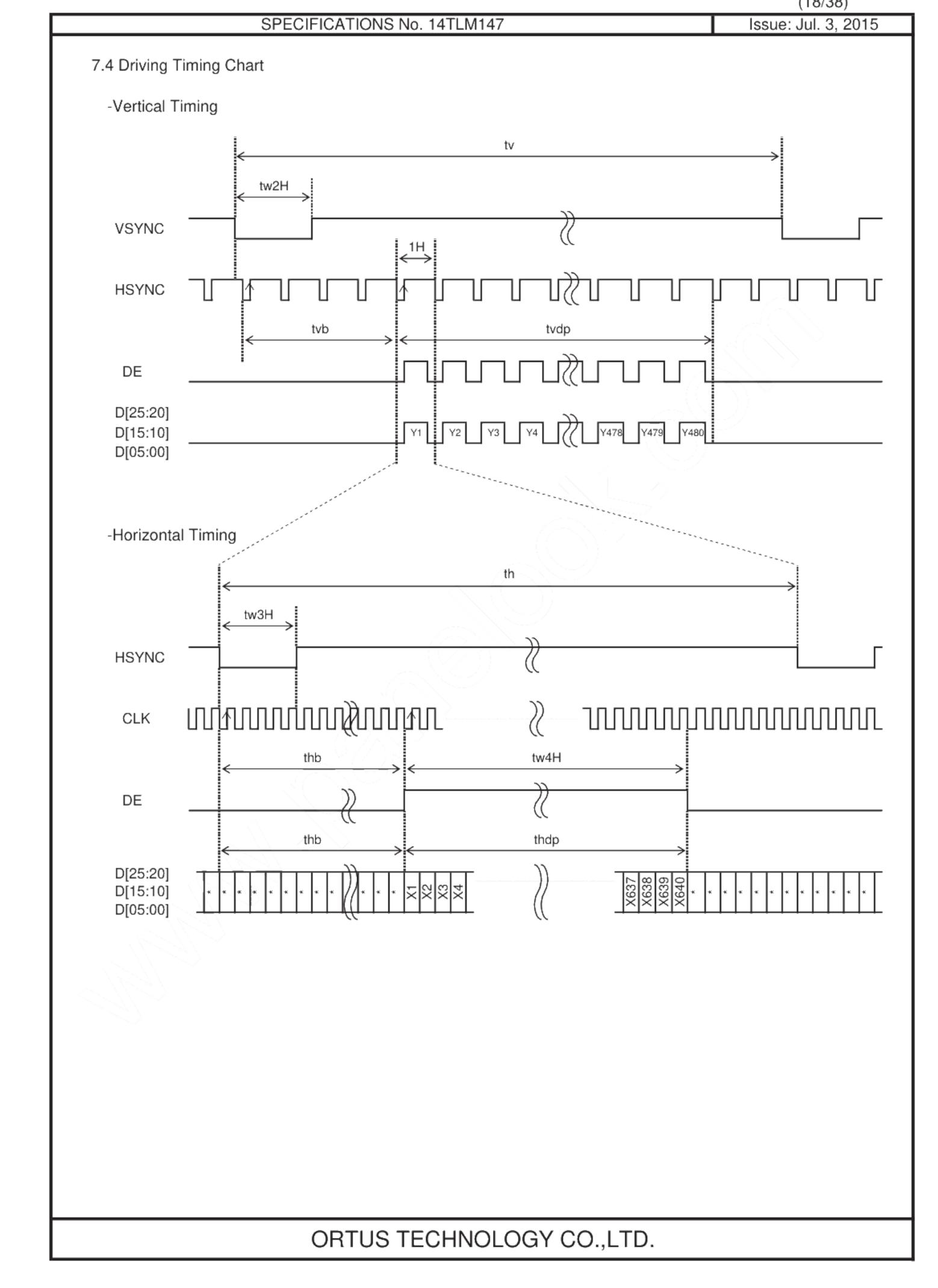
Item	Symbol		Rating		Unit	Applicable terminal
		MIN	TYP	MAX		
CLK frequency	fCLK		25	27	MHz	CLK
VSYNC signal cycle time	tv		525		Н	VSYNC,HSYNC
VSYNC frequency Note1	fVSYNC	54	60	66	Hz	VSYNC
VSYNC pulse width	tw2H	1	3	5	Н	VSYNC,HSYNC
Vertical back porch	tvb		35		Н	VSYNC,HSYNC,DE,D[25:20],
Vertical display period	tvdp		480		Н	D[15:10],D[05:00]
HSYNC signal cycle time	th		800		CLK	HSYNC,CLK
HSYNC pulse width	tw3H	5	30		CLK	
Horizontal back porch	thb	112		144	CLK	HSYNC,CLK,DE,D[25:20],
				Note 2		D[15:10],D[05:00]
Horizontal display period	thdp		640		CLK	
DE pulse width	tw4H		640		CLK	DE,CLK

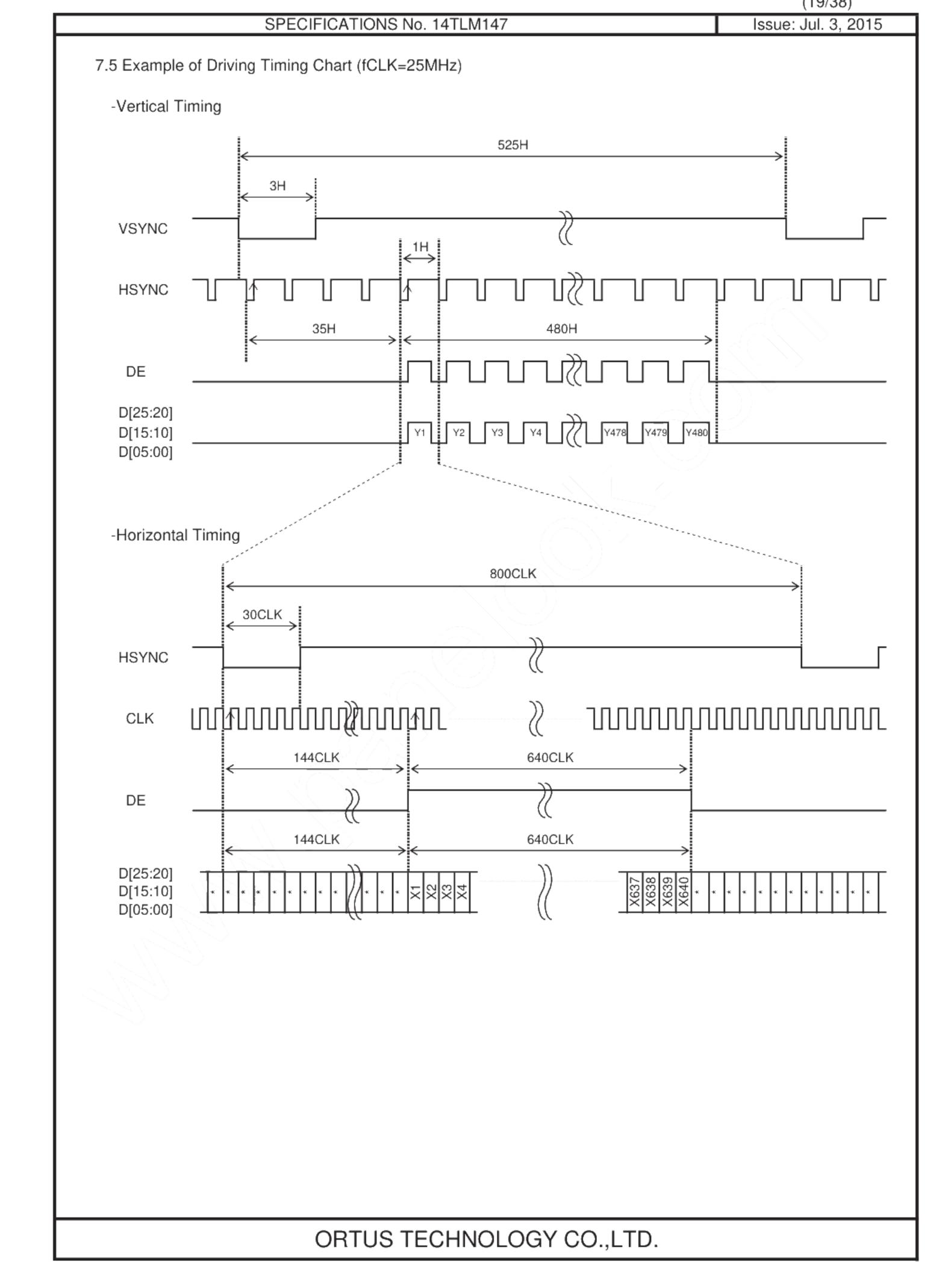
Note1: The characteristic of this item is recommended standard.

Please use it after it confirms it enough like the display fineness etc.

When it comes off from this characteristic and it is used.

Note2: When "DE" keeps "Lo" for 144CLK or longer, start capturing data automatically from 144CLK.





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8. Description of Operation

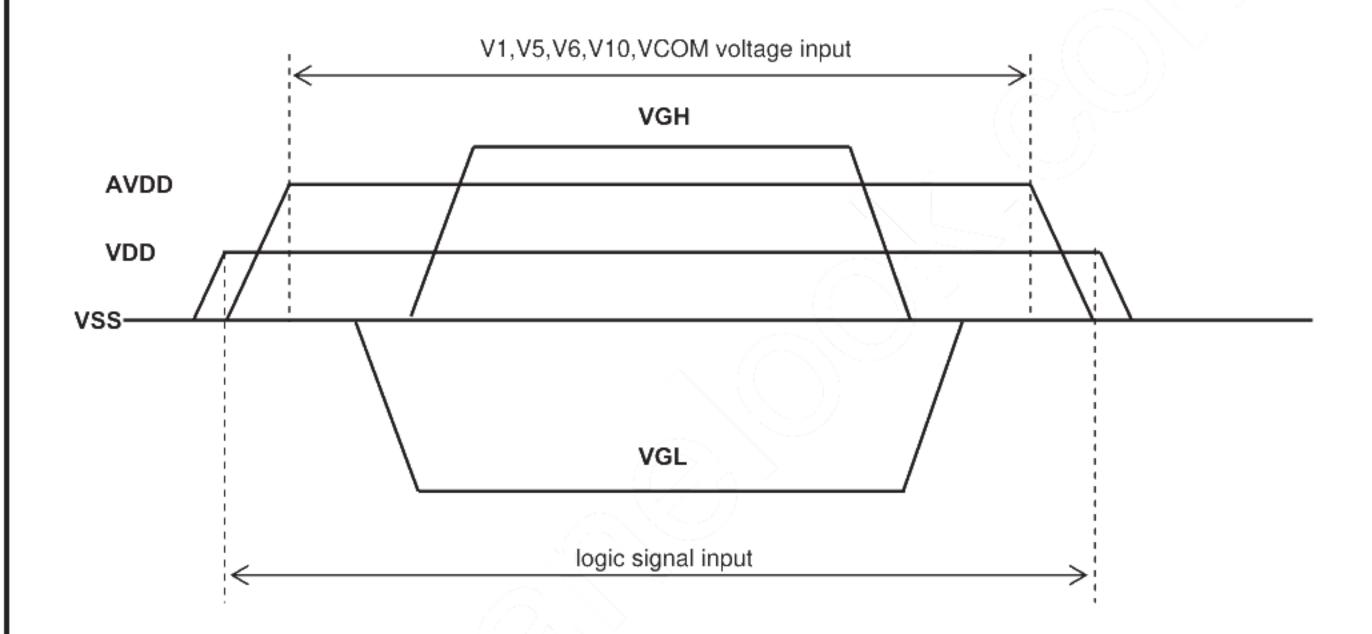
8.1 Power On/Off Sequence

The sequence of the Power On/Off and the signal input must defend the following conditions.

- Please input the logic signal after turning on VDD.
- Please input AVDD after turning on VDD or at the same time.
- Please input V1, V5, V6, V10 and VCOM voltage after turning on AVDD.
- Please input VGL after turning on VDD.
- Please input VGH after turning on VGL.

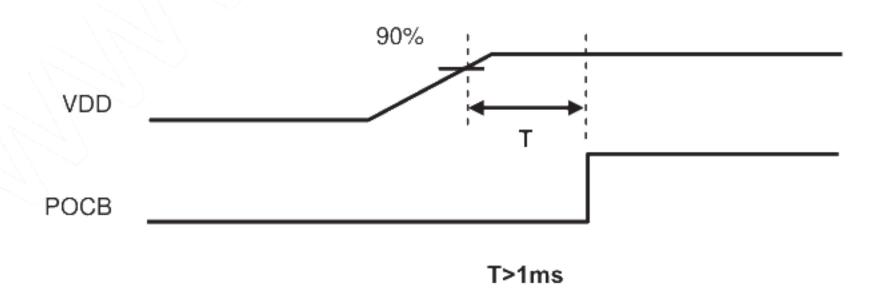
Power Off sequence is assumed to be opposite of the above mentioned sequence.

Please refer Power On/Off recommended sequence is shown in the figure below.



8.2 Power On Clear

There is a limitation between Power On and POCB (power on clear) . Please defend the following conditions.



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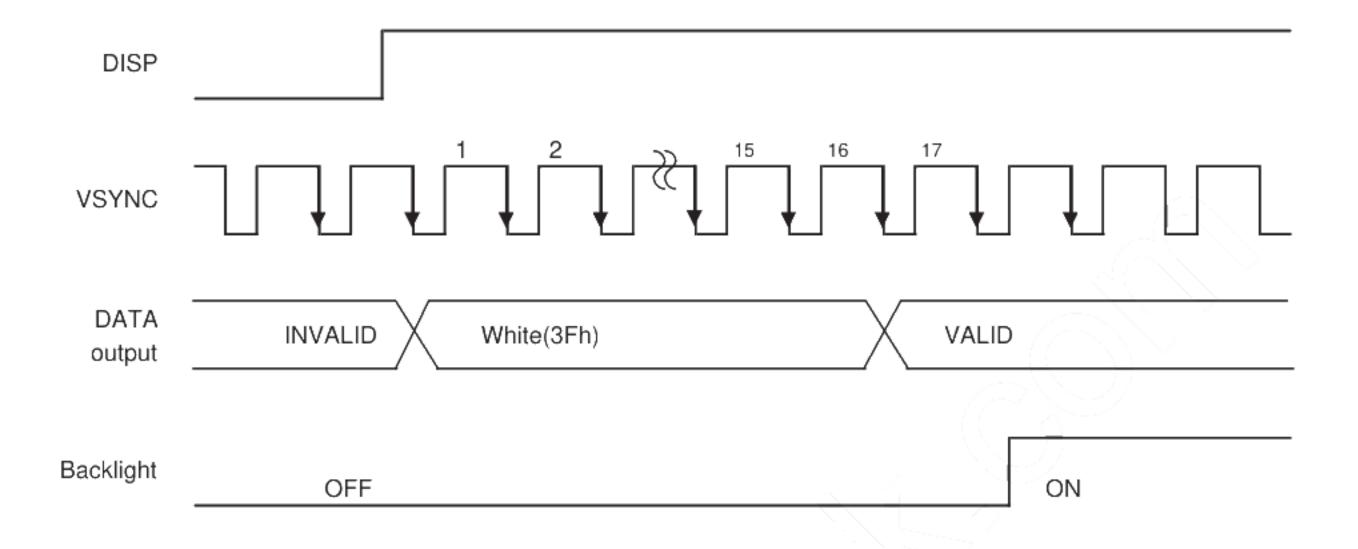
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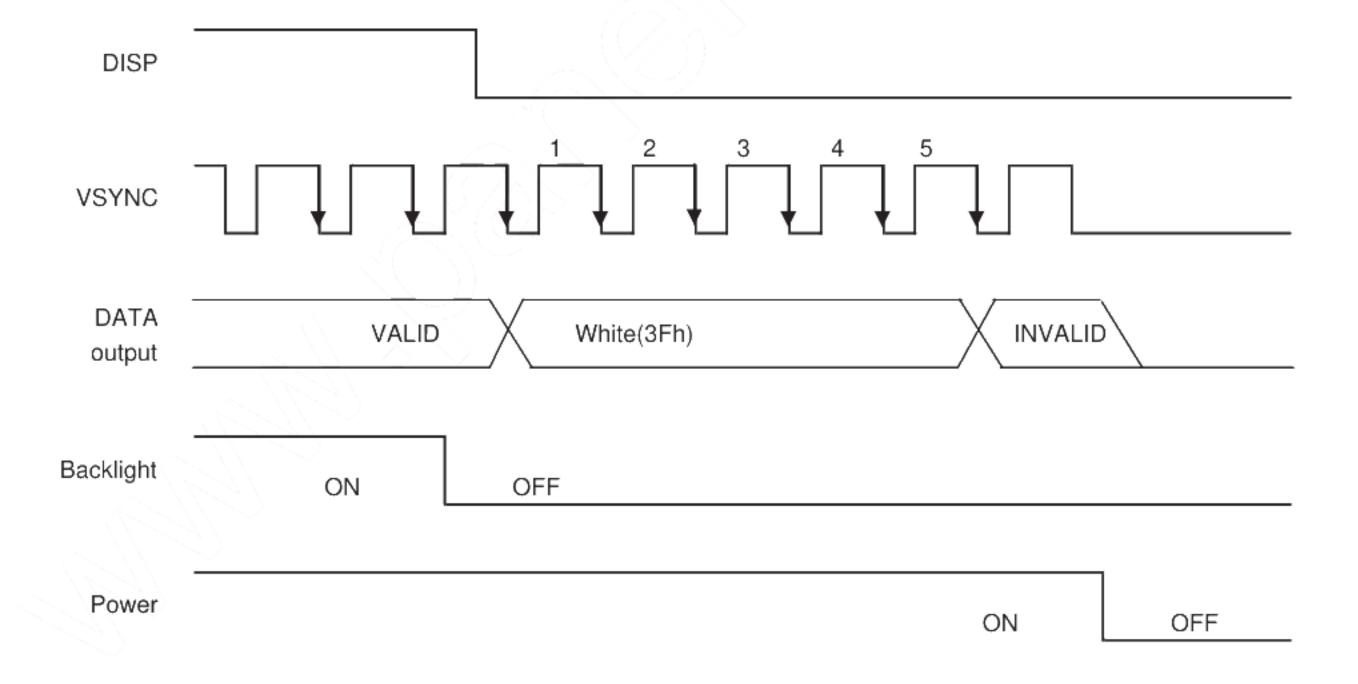
It explains the Display on/off sequence.

8.3 "DISP" On/Off Sequence

After Display on,"White" data is outputted for 16-Frames first, from the falling edge of the following VSYNC signal.



After Display off,"White" data is outputted for 5-Frames first, from the falling edge of the following VSYNC signal. Please turn off the power supply promptly after OFF of "DISP".



(22/38) SPECIFICATIONS No. 14TLM147 Issue: Jul. 3, 2015 9. Circuit 9.1 Driving Circuit Example (AVDD = 12.0V) VSS 1 VSS **AVDD** VSS, VGL 2 VGL 1uF25V VDD 0.1uF25V VDD VDD 3 **VDD** 10uF6.3V VSS VSS **VGH** 4 VGH 1uF50V AVDD 0.1uF25V AVDD F28kΩ **AVDD** 5 **AVDD** 10uF25Vi OP AMP VGH VGH 6 V10 VGL VGL 7 ٧6 F2kΩ 8 V5 1uF16V CLK 9 CLK V1 0.1uF25V √F10kΩ **HSYNC POCB** 10 HSYNC **POCB** OP AMP DISP VSYNC 11 **VSYNC** DISP DE RL12 DE RLF9kΩ D[25:20] UD 13 UD D[25:20] 1uF16V D[15:10] VSS 14 D[15:10] VSS 0.1uF25V D[05:00] **VDD** F9kΩ 15 VDD D[05:00] OP AMP RL DE 16 DE RLUD **HSYNC** 17 UD **HSYNC** F12kΩ **VSYNC** 18 VSYNC POCB 1uF16V POCB CLK 19 CLK DISP 0.1uF25V DISP $F3k\Omega$ VSS 20:21 TEST5,6 OP AMP D[05:00] 22:27 D[05:00] VSS 28:29 TEST3,4 F23kΩ D[15:10] 30:35 D[15:10] 1uF16V VSS 36:37 TEST1,2 D[25:20] 38:43 F20kΩ D[25:20] OP AMP $4.7k\Omega$ 44 **VCOM** VSS 45 VSS $F12k\Omega\,$ 1uF16V 0.1uF25V Example of OP AMP:NJM2742M

TFT-LCM Reference Circuit

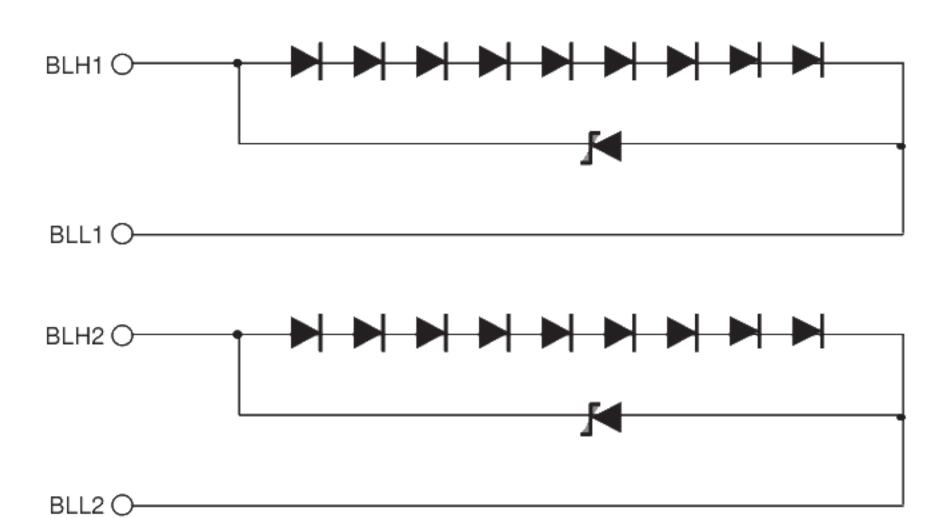
This circuit design is for reference purposes only. Appropriate component values may be different. Please evaluate on your side.

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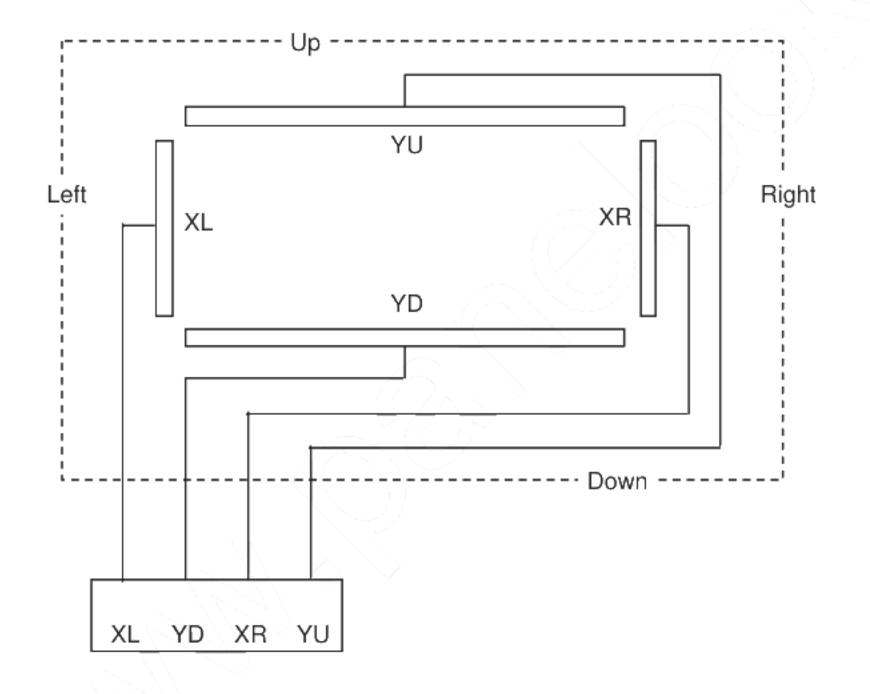
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9.2 LED Circuit



9.3 Touch Panel Circuit



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10. Characteristics

10.1 Optical Characteristics

< Measurement Condition >

Measuring instruments: CS1000 (KONICA MINOLTA), LCD7200(OTSUKA ELECTRONICS),

EZcontrast160D (ELDIM)

Driving condition: Typical Rating of "6. Recommended Operating Conditions".

Optimized VCOMDC

Backlight: IL=10.2mA
Measured temperature: Ta=25 C

	ltem	Symbol	Condition	MIN	TYP	MAX	Unit	Note No.	Remark
Response time	Rise time	TON	[Data]= 3Fh→00h	_		40	ms	1	*
Respon	Fall time	TOFF	[Data]= 00h→3Fh	_		60	ms		
Contrast ratio	Backlight ON	CR	[Data]= 3Fh/00h	360	600			2	
Con	Backlight OFF			_	2.3				
	Left	θL	[Data]=	_	80	_	deg	3	*
riewing angle	Right	θR	3Fh/00h	_	80		deg	77	
Viewing angle	Up	φU	CR≧10	_	60	- /	deg	1 /	
	Down	φD		_	65	_ \	deg		
\/\/hite	Chromaticity	Х	[Data]=3Fh	White ch	romaticit	y range		4	
VVIII(C	Oniomaticity	У) /		
	Burn-in			No noticeable burn-in image shall be observed after 0.5 hours of window pattern display.			ours of	5	
Cente	er brightness		[Data]=3Fh	300	450		cd/m ²	6	
Brigh	tness distributio	on	[Data]=3Fh	70		_	%	7	

^{*} Note number 1 to 7: Refer to the APPENDIX of "Reference Method for Measuring Optical Characteristics".

^{*} Measured in the form of LCD module.

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0.42 0.40 0.38 0.36 0.34 0.32 0.30 0.28 0.26 0.24 0.22 0.24 0.26 0.28 0.30 0.32 0.34 0.36 0.38 0.40

[White Chromaticity Range]

Х	у
0.26	0.34
0.26	0.28
0.27	0.26
0.33	0.26
0.36	0.31
0.36	0.38
0.35	0.39
0.29	0.39

White Chromaticity Range

10.2 Temperature Characteristics

< Measurement Condition >

Measuring instruments: CS1000 (KONICA MINOLTA) , LCD7200(OTSUKA ELECTRONICS)

Driving condition: Typical Rating of "6. Recommended Operating Conditions".

Optimized VCOMDC

Backlight: IL=10.2mA

10	Item		Specif	ication	Remark	
	tem		Ta=-20°C	Ta=70°C	nemark	
Contr	ast ratio	CR	40 or more	40 or more	Backlight ON	
Response time	Rise time	TON	200 msec or less	30 msec or less	*	
nesponse time	Response time Fall time TC		300 msec or less 50 msec or less		*	
Display Quality			No noticeable display defect or ununiformity should be observed.		Use the criteria for judgment specified in the section 11.	

^{*} Measured in the form of LCD module.

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11. Criteria of Judgment

11.1 Defective Display and Screen Quality

Test Condition: Observed TFT-LCD monitor from front during operation with the following conditions

Driving Signal Raster Patter (RGB, white, black)
Signal condition [Data]: 3Fh, 18h, 00h (3 steps)

Observation distance 30 cm
Illuminance 200 to 350 lx
Backlight IL=10.2mA

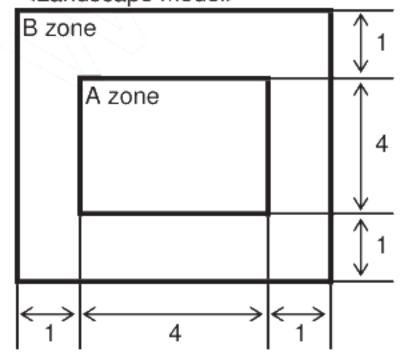
De	efect item		Defect conten	t	Criteria		
	Line defect	Black, white or color	r line, 3 or more neig	hboring defective dots	Not exists		
		Uneven brightness	on dot-by-dot base d	ue to defective	Refer to table 1		
Quality		TFT or CF, or dust i	s counted as dot def	ect			
2		(brighter dot, darker	dot)				
<u>a</u>	Dot defect	High bright dot: Visil	ble through 2% ND f	ilter at [Data]=00h			
Display		Low bright dot: Visi	ble through 5% ND f	ilter at [Data]=00h			
_		Dark dot: Appear da	rk through white dis	play at [Data]=18h			
		Invisible through 5%	ND filter at [Data]=0	00h	ignored		
	Dirt	Uneven brightness (white stain, black st	ain etc)	Invisible through 1% ND filter		
		Point-like	0.25mm< φ		N=0		
	Foreign		0.20mm< φ ≦0.2	25mm	N≦2		
	Foreign particle		φ ≦0.2	20mm	Ignored		
<u> </u>	Particle	Liner	3.0mm <length and<="" td=""><td>0.08mm<width< td=""><td>N=0</td></width<></td></length>	0.08mm <width< td=""><td>N=0</td></width<>	N=0		
Quality			length≦3.0mm or v	vidth ≦0.08mm	Ignored		
en C		Flaw on the surface	0.05mm <w< td=""><td></td><td>Conform to the criteria of point-</td></w<>		Conform to the criteria of point-		
ree		of the Touch panel			like foreign particles.		
Scre	Flaw		0.03 <w≦0.05mm 2<l≦5mm<="" td=""><td>N≦5</td></w≦0.05mm>		N≦5		
			((L≦2mm	Ignored		
			W≦0.03mm		Ignored		
	Others		(1)		Use boundary sample		
	Others				for judgment when necessary		

 ϕ (mm): Average diameter = (major axis + minor axis)/2 Permissible number: N

Table 1

1000			2.5		1 CHINGGISTO HAMISOT. 14
Area	High bright dot	Low bright dot	Dark dot	Total	Criteria
А	0	2	2	3	Permissible distance between same color bright dots (includes neighboring dots): 3 mm or more
В	2	4	4	6	Permissible distance between same color high bright dots (includes neighboring dots): 5 mm or more
Total	2	4	4	7	

<Landscape model>



Division of A and B areas

B area: Active area

Dimensional ratio between A and B areas: 1: 4: 1 (Refer to the left figure)

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11.2 Screen and Other Appearance

Testing conditions

Observation distance

Illuminance

30cm

1200~2000 lx

	Item	Criteria	Remark
Polarizer	Flaw Stain Bubble Dust Dent	Ignore invisible defect when the backlight is on.	Applicable area: Active area only (Refer to the section 3.2 "Outward form")
	S-case	No functional defect occurs	
	FPC cable	No functional defect occurs	

Item		Appearance	Criteria
Touch Panel	Glass chipping	Others Progressive crack Concentric interference fringe (Test method) Observe the Panel surface from 60 degrees angle to the surface under white fluorescent lamp (Triple wavelength lamp)	Unit:mm a ≤ 3 b ≤ 3 c ≤ t (t: glass thickness) a,b ≤ 0.5 is ignored n ≤ 2 Unit:mm a ≤ 5 b ≤ 1 c ≤ t (t:glass thickness) a,b ≤ 0.5 is ignored Maximum permissible number of chipping off on a side is 5. None Size: 1/3 or less of Active area. Darkness: comply with the boundary sample.
	Fisheye Film surface		$D \le \varphi 0.2 mm$ Ignored $\varphi 0.2 < D \le \varphi 0.6 mm$ $N \le 2$ $\varphi 0.6 mm < D$ $N=0$
	Puffiness	(D: Average diameter of valley part) 0.4mm Touch Panel	H≦0.4mm is acceptable.

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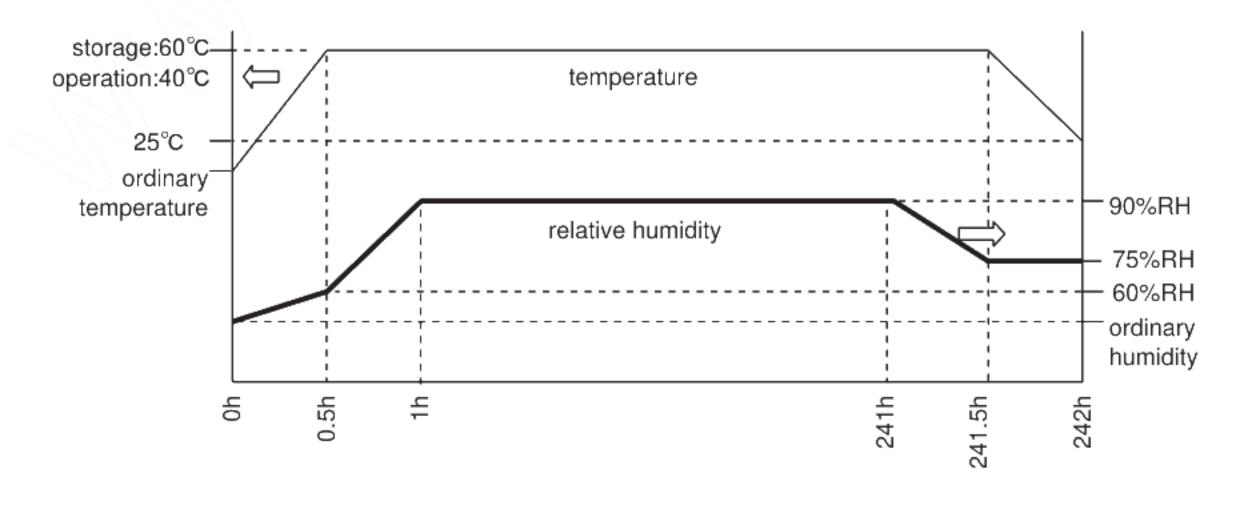
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12. Reliability Test

z. nei	ability rest		
Test item		Test condition	number of failures
		10000011011011	/number of examinations
Durability test	High temperature storage	Ta=80°C 240hr	0/3
	Low temperature storage	Ta=-30°C 240hr	0/3
	High temperature & high	Ta=60°C, RH=90% 240hr	0/3
	humidity storage	non condensing **	
	High temperature operation	Tp=70°C 240hr	0/3
	Low temperature operation	Tp=-20°C 240hr	0/3
ļδ	High temp & humid operation	Tp=40°C, RH=90% 240hr	0/3
	Trigit terrip & flutilid operation	non condensing **	
	Thermal shock storage	-30←→80°C(30min/30min) 100 cycles	0/3
		Confirms to EIAJ ED-4701/300	0/3
	Electrostatic discharge test	C=200pF,R=0Ω,V=±200V	
	(Non operation)	Each 3 times of discharge on and power supply	(4)
		and other terminals.	
	Surface discharge test (Non operation)	C=250pF, R=100Ω, V=±12kV	0/3
est		Each 5 times of discharge in both polarities	been the second of the second
1		on the center of screen with the case grounded.	
- July	EDO La calla de la	Pull the FPC with the force of 3N for 10 sec.	0/3
l ŭ	FPC tension test (FPC of LCD only)	in the direction +/- 90-degree to its	
iror	(FFC of LCD offly)	original direction.	
environmental	EDO Is and I see	Pull the FPC with the force of 3N for 10 sec.	0/3
	FPC bend test	in the direction +/-180-degree to its	
Mechanical	(FPC of LCD only)	original direction. Reciprocate it 3 times.	
Ç) Claustian to at	Total amplitude 1.5mm, f=10~55Hz, X,Y,Z	0/3
Me	Vibration test	directions for each 2 hours	
		Use ORTUS TECHNOLOGY original jig	0/3
		(see next page)and make an impact with	
	Impact test	peak acceleration of 1000m/s2 for 6 msec with	
		half sine-curve at 3 times to each X, Y, Z directions	
		in conformance with JIS C 60068-2-27-2011.	
acking test		Acceleration of 19.6m/s ² with frequency of	0/1 Packing
	Packing vibration-proof test	10→55→10Hz, X,Y, Zdirection for each	
		30 minutes	
	Dealder description	Drop from 75cm high.	0/1 Packing
<u> </u>	Packing drop test	1 time to each 6 surfaces, 3 edges, 1 corner	
1			

Note:Ta=ambient temperature

Tp=Panel temperature



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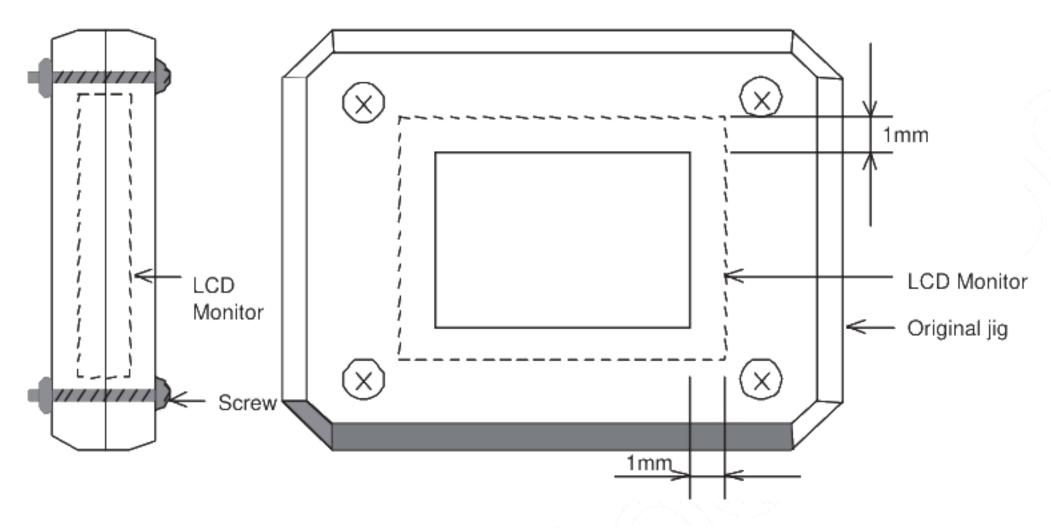
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Table2.Reliability Criteria

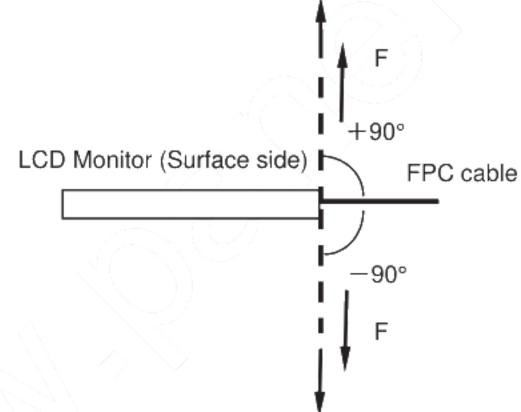
Measure the parameters after leaving the monitor at the ordinary temperature for 24 hours or more after the test completion.

item	Standard	Remarks
Display quality	No visible abnormality shall be seen.	
Contrast ratio	40 or more	Backlight ON

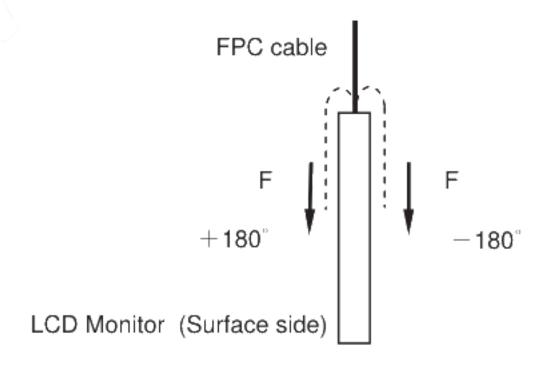
ORTUS TECHNOLOGY Original Jig



Tension Test Method for FPC cable



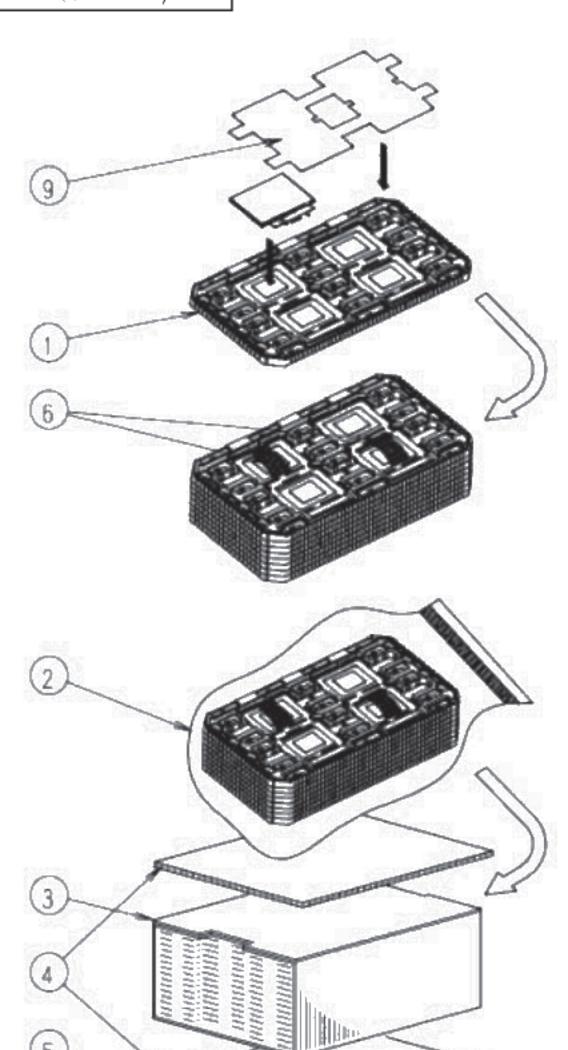
Bend Test Method for FPC cable



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13. Packing Specifications

Packing specification (S=FREE)



Step 1. Each product is to be placed in one of the cut-outs of the tray with the display surface facing upward.

(4 products per tray)

Step 2. Each tray is to be piled up in same orientation and the trays be in a stack of 7.

Step 3. 2 packs of moisture absobers are to be placed on the top tray as shown in the drawing.

One empty tray is to be put on the top of stack of 7 trays.

Put piled trays into a sealing bag.

Vacuum and seal the sealing bag with the vacuum sealing machine.

- Step 4. The stack of trays in the plastic back is to be inserted into a inner carton.
- Step 5. A corrugated board is to be placed on the top and on the bottom of the inner carton. The two corrugated boards and the inner carton is to be inserted into an outer carton.
- Step 6. The outer carton needs to sealed with packing tape as shown in the drawing.

The model number, quantity of products, and shipping date are to be printed on the outer carton.

If necessary, shipping labels or impression markings are to be put on the outer carton.

Step 7. The outer carton is to be inserted into a extra outer carton with same direction.

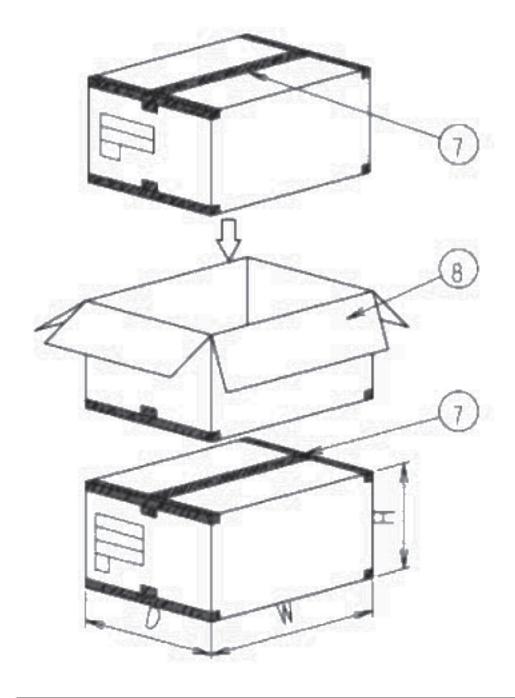
The extra outer carton needs to sealed with packing tape as shown in the drawing.

Step 8. The model number, quantity of products, and shipping date are to be printed on the extra outer carton.

If necessary, shipping labels or impression markings are to be put on the extra outer carton.



	Packing item name	Specs., Material			
1	TRAY	A-PET			
2	SEALING BAG				
3	INNER CARTON	Corrugated cardboard			
4	INNER BOARD	Corrugated cardboard			
5	OUTER CARTON	Corrugated cardboard			
6	Drier	Moisture absorber			
7	Packing tape				
8	EXTRA OUTER CARTON	Corrugated cardboard			
9	FOAM SHEET	Anti-static polyethilene			



Dimension of extra outer carton				
D : Approx.	(338mm)			
W : Approx.	(549mm)			
H : Approx.	(198mm)			
Quantity of products in one carton: 4pcsx7=28pcs				
Gross weight:	Approx. 7.1Kg			

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14. Handling Instruction

14.1 Cautions for Handling LCD panels



Caution

- (1) Do not make an impact on the LCD panel glass because it may break and you may get injured from it.
- (2) If the glass breaks, do not touch it with bare hands.
 (Fragment of broken glass may stick you or you cut yourself on it.
- (3) If you get injured, receive adequate first aid and consult a medial doctor.
- (4) Do not let liquid crystal get into your mouth.
 (If the LCD panel glass breaks, try not let liquid crystal get into your mouth even toxic property of liquid crystal has not been confirmed.
- (5) If liquid crystal adheres, rinse it out thoroughly.
 (If liquid crystal adheres to your cloth or skin, wipe it off with rubbing alcohol or wash it thoroughly with soap. If liquid crystal gets into eyes, rinse it with clean water for at least 15 minutes and consult an eye doctor.
- (6) If you scrap this products, follow a disposal standard of industrial waste that is legally valid in the community, country or territory where you reside.
- (7) Do not connect or disconnect this product while its application products is powered on.
- (8) Do not attempt to disassemble or modify this product as it is precision component.
- (9) If a part of soldering part has been exposed, and avoid contact (short-circuit) with a metallic part of the case etc. about FPC of this model, please. Please insulate it with the insulating tape etc. if necessary. The defective operation is caused, and there is a possibility to generation of heat and the ignition.
- (10) Since excess current protection circuit is not built in this TFT module, there is the possibility that LCD module or peripheral circuit become feverish and burned in case abnormal operation is generated. We recommend you to add excess current protection circuit to power supply.
- (11) The end part of glass and film of touch panel has conductivity, and avoid contact (short-circuit) with electroconductive case etc.. There is a possibility of setting up a defective touch panel, and insulate it for the case suppression (cushion etc.) if necessary, please.



Caution

This mark is used to indicate a precaution or an instruction which, if not correctly observed, may result in bodily injury, or material damages alone.

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14.2 Precautions for Handling

 Wear finger tips at incoming inspection and for handling the TFT monitors to keep display quality and keep the working area clean.
 Do not touch the surface of the monitor as it is easily scratched.

- Wear grounded wrist-straps and use electrostatic neutralization blowers to prevent static charge and discharge when handling the TFT monitors as the LED in this TFT monitors is damageable to electrostatic discharge. Designate an appropriate operating area, and set equipment, tools, and machines properly when handling this product.
- 3) Avoid strong mechanical shock including knocking, hitting or dropping to the TFT monitors for protecting their glass parts. Do not use the TFT monitors that have been experienced dropping or strong mechanical shock.
- 4) Do not use or storage the TFT monitors at high temperature and high humidity environment. Particularly, never use or storage the TFT monitors at a location where condensation builds up.
- 5) Avoid using and storing TFT monitors at a location where they are exposed to direct sunlight or ultraviolet rays to prevent the LCD panels from deterioration by ultraviolet rays.
- 6) Do not stain or damage the contacts of the FPC cable.
 FPC cable needs to be inserted until it can reach to the end of connector slot.
 During insertion, make sure to keep the cable in a horizontal position to avoid an oblique insertion.
 Otherwise, it may cause poor contact or deteriorate reliability of the FPC cable.
- 7) Do not bend or pull the FPC cable or carry the TFT monitor by holding the FPC cable.
- Peel off the protective film on the TFT monitors during mounting process. Refer to the section 14.5 on how to peel off the protective film. We are not responsible for electrostatic discharge failures or other defects occur when peeling off the protective film.

14.3 Precautions for Operation

- Since this TFT monitors are not equipped with light shielding for the driver IC,
 do not expose the driver IC to strong lights during operation as it may cause functional failures.
- In case of powering up or powering off this LCD module, be sure to comply the sequence as instructed in this specification.
- Optimize VCOMDC within recommended operating conditions.

 *When VCOMDC is not an entired value flictor and image sticking will be as
 - * When VCOMDC is not an optimal value, flicker and image sticking will be occurred.
- 4) Do not plug in or out the FPC cable while power supply is switch on. Plug the FPC cable in and out while power supply is switched off.
- 5) Do not operate the TFT monitors in the strong magnetic field. It may break the TFT monitors.
- 6) Do not display a fixed image on the screen for a long time. Use a screen-saver or other measures to avoid a fixed image displayed on the screen for a long time. Otherwise, it may cause burn-in image on the screen due the characteristics of liquid crystal.

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14.4 Storage Condition for Shipping Cartons

Storage environment

Temperature 0 to 40° C
 Humidity 60%RH or less

No-condensing occurs under low temperature with high humidity condition.

Atmosphere No poisonous gas that can erode electronic components and/or wiring

materials should be detected.

Time period 3 months

Unpacking To prevent damages caused by static electricity, anti-static precautionary measures

(e.g. earthing, anti-static mat) should be implemented.

Maximum piling up 7 cartons

14.5 Precautions for Peeling off the Protective film

The followings work environment and work method are recommended to prevent the TFT monitors from static damage or adhesion of dust when peeling off the protective films.

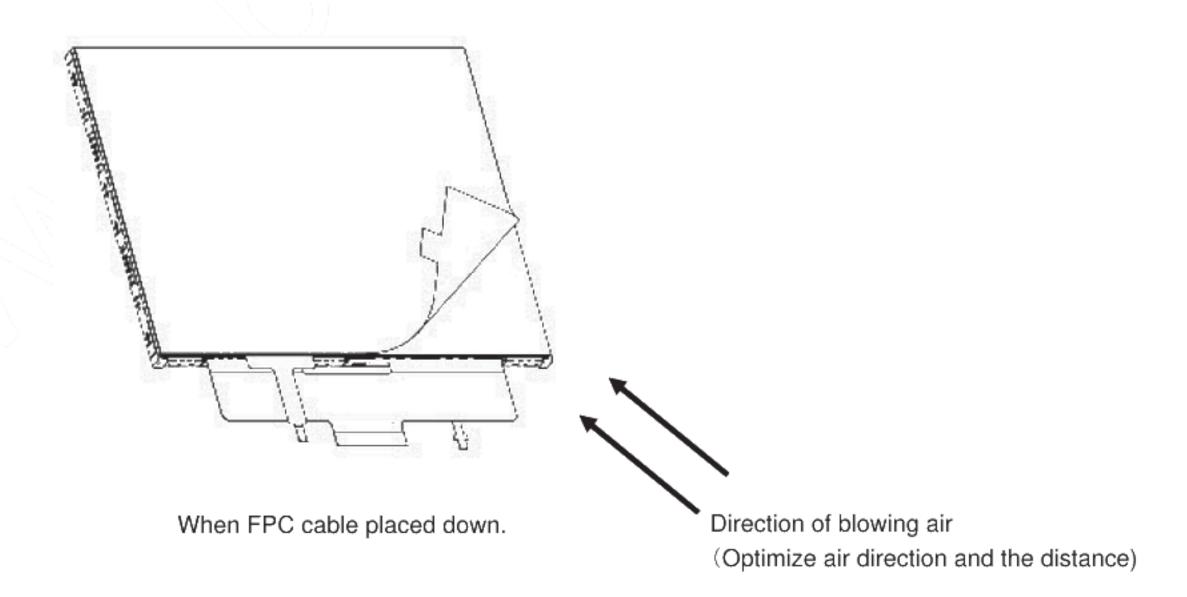
A) Work Environment

- a) Humidity: 50 to 70 %RH, Temperature15 to 27 °C
- b) Operators should wear conductive shoes, conductive clothes, conductive finger tips and grounded wrist-straps. Anti-static treatment should be implemented to work area's floor.
- Use a room shielded against outside dust with sticky floor mat laid at the entrance to eliminate dirt.

B) Work Method

The following procedures should taken to prevent the driver ICs from charging and discharging.

- a) Use an electrostatic neutralization blower to blow air on the TFT monitors to its lower right when FPC cable placed down.
 - Optimize direction of the blowing air and the distance between the TFT monitors and the electrostatic neutralization blower.
- b) Peel off the tab slowly (spending more than 2 secs to complete) by pulling it to opposite direction.



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APPENDIX

Reference Method for Measuring Optical Characteristics and Performance

1. Measurement Condition (Backlight ON)

CS1000 (KONICA MINOLTA) , LCD7200(OTSUKA ELECTRONICS) ,EZcontrast160D (ELDIM) Measuring instruments:

Driving condition: Refer to the section "Optical Characteristics"

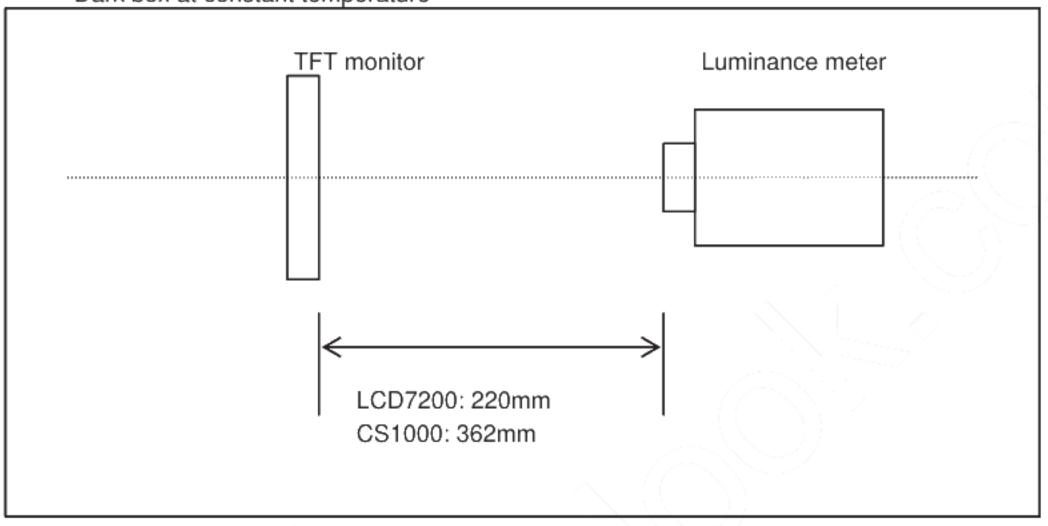
25°C unless specified Measured temperature:

See the chart below. The luminance meter is placed on the normal line of Measurement system:

measurement system.

At the center of the screen unless otherwise specified Measurement point:

Dark box at constant temperature

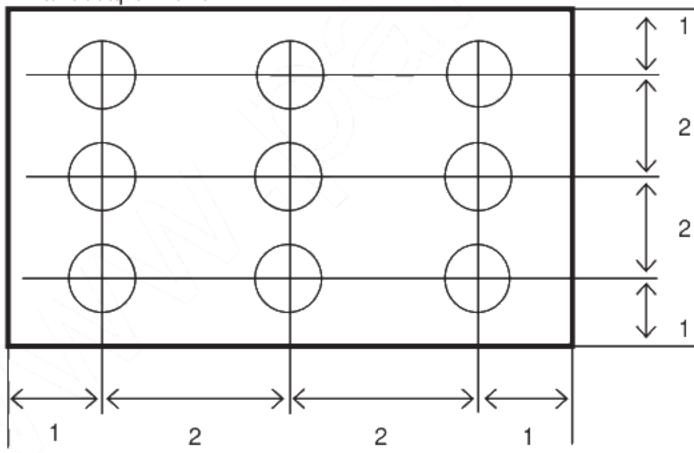


Measurement is made after 30 minutes of lighting of the backlight.

Measurement point: At the center point of the screen

Brightness distribution: 9 points shown in the following drawing.

<Landscape model>



Dimensional ratio of active area

Backlight IL=10.2mA

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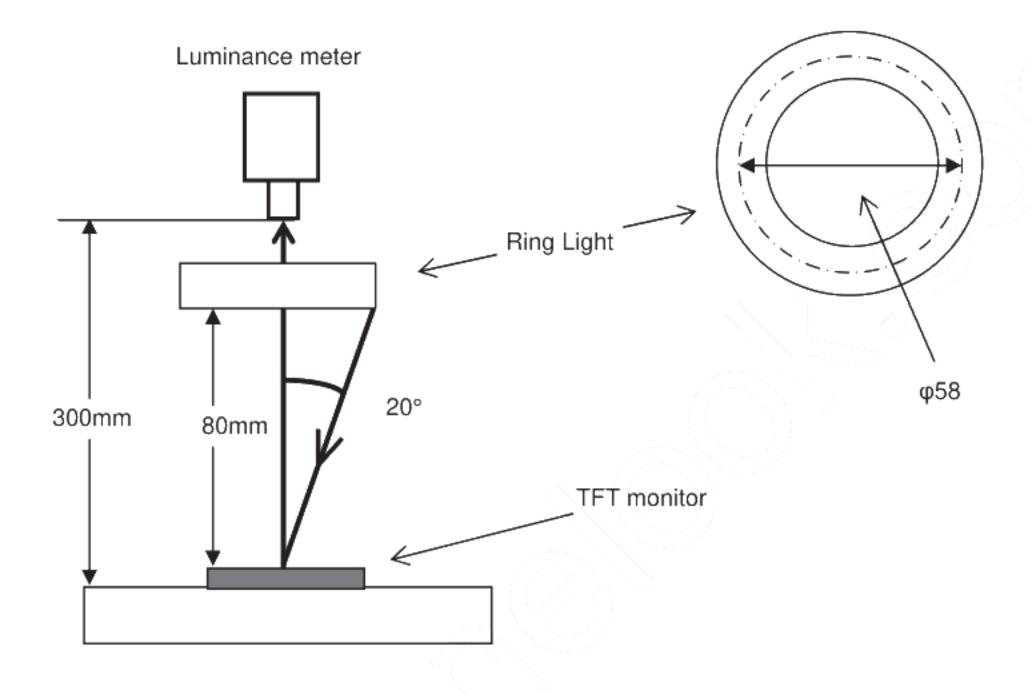
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Measurement Condition (Contrast ratio Backlight OFF only)

Measuring instruments: LCD7200(OTSUKA ELECTRONICS), Ring Light(40,000 Ix, φ58)

Driving condition: Refer to the section "Optical Characteristics"

Measured temperature: 25°C unless specified
Measurement system: See the chart below.
Measurement point: At the center of the screen.



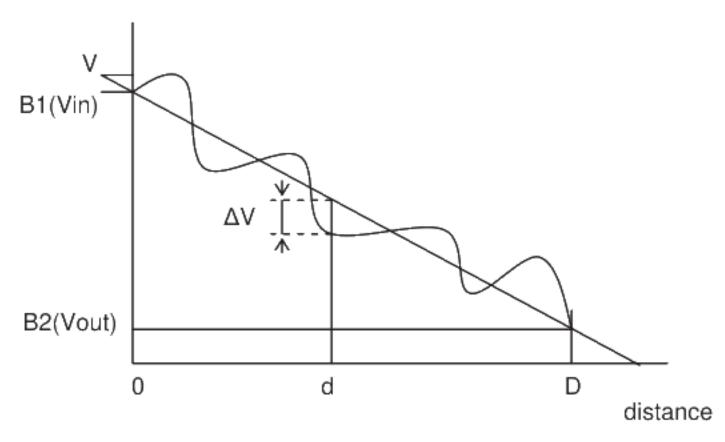
(30/30) SPECIFICATIONS No. 14TLM147 Issue: Jul. 3, 2015 2. Test Method Test method Item Measuring Remark Notice instrument Measure output signal waveform by the luminance Black display LCD7200 Response meter when raster of window pattern is changed from time [Data]=00h white to black and from black to white. White display [Data]=3Fh TON Rise time White White Black TOFF Fall time White 100% 90% 10% 0% Black TON TOFF Contrast ratio Measure maximum luminance Y1([Data]=3Fh) and 2 CS1000 Backlight ON minimum luminance Y2([Data]=00h) at the center of LCD7200 Backlight OFF the screen by displaying raster or window pattern. Then calculate the ratio between these two values. Contrast ratio = Y1/Y2 Diameter of measuring point: 8mmφ(CS1000) Diameter of measuring point: 3mmφ(LCD7200) Viewing Move the luminance meter from right to left and up EZcontrast160D angle and down and determine the angles where Horizontal₀ contrast ratio is 10. Verticalφ Measure chromaticity coordinates x and y of CIE1931 White CS1000 5 colorimetric system at [Data] = 3Fh chromaticity Color matching faction: 2°view Burn-in Visually check burn-in image on the screen after 6 0.5 hours of "window display" ([Data]=3Fh/00h). 7 Measure the brightness at the center of the screen. Center CS1000 brightness 8 Brightness (Brightness distribution) = 100 x B/A % CS1000 A: max. brightness of the 9 points distribution

B: min. brightness of the 9 points

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* Linearity Measurement of Touch Panel



 $LE(\%) = \Delta V/(Vin\text{-}Vout) \times 100$

 $LEmax(\%) = \Delta Vmax/(Vin-Vout) \times 100$

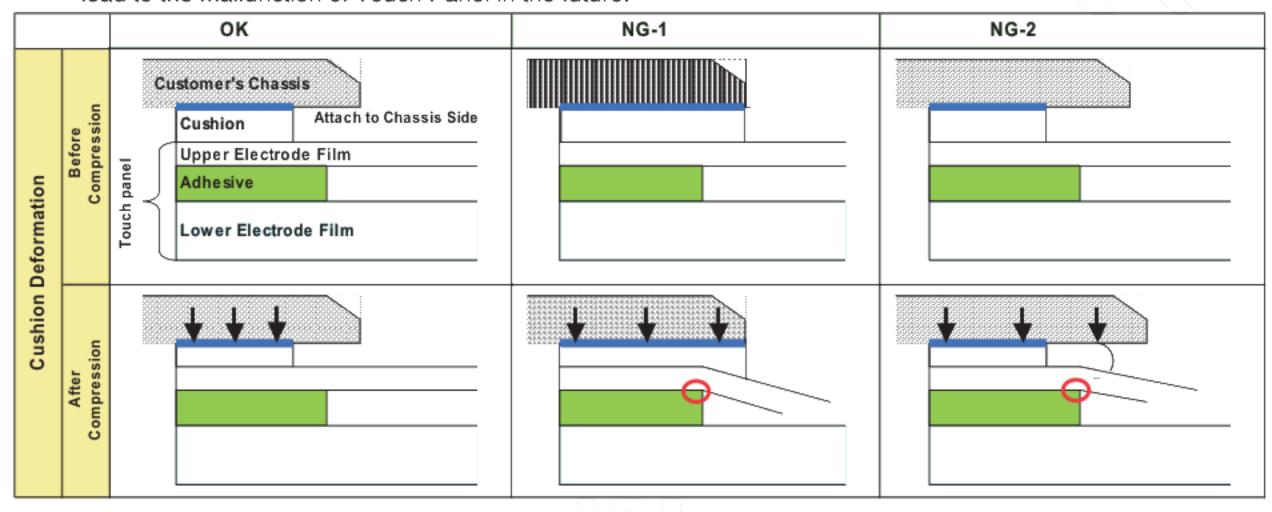
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SPECIFICATIONS No. 14TLM147

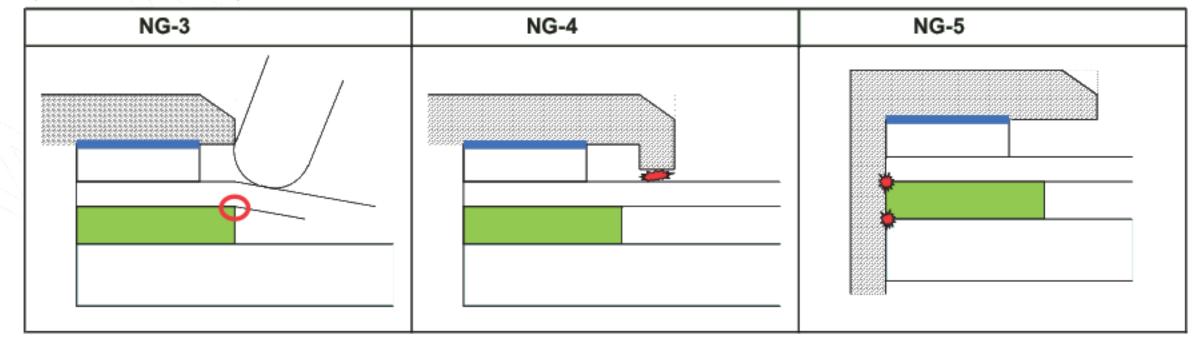
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- Cautionary instruction to handle a Touch-panel
 - Cushion (between Touch Panel Chassis) Design
 - 1) A cushion is required to be placed between Touch Panel and customer's chassis and there is a designated area to attach it. Attachment at area inside Input Prohibition Area must be forbidden.

 If cushion was located inside Input Prohibition Area, Upper Electrode may be push constantly and which may cause the electrode breakage at the position falling on the edge of adhesive; it eventually results in Touch Panel malfunction in the future. (Please see "NG-1")
 - 2) Be attention to the cushion material you use. In the case that too soft cushion was used, the cushion may protrude into Prohibition Area by being push strongly; which may result in the electrode breakage. Eventually there is a chance that the electrode breakage leads to the malfunction of Touch Panel in the future. (Please see "NG-2")
 - 3) Cushion is required to be attached at the side of Customer's chassis.
 Attaching a cushion at the side of Upper Electrode Film has a chance to deform the film and lead to the malfunction of Touch Panel in the future.



- Design Guidance of Chassis (Front Part)
 - 4) Be attention to stay Input Prohibition Area away from touching and/or drawing by a stylus pens in order to avoid the electrode breakage and potential malfunction of Touch Panel. (Please see "NG-3") We recommend customers to design chassis (front case) being able to protect Input Prohibition Area.
 - 5) Clearance between customer's chassis and Touch Panel surface is certainly required in order to avoid erroneous input caused by a collision of the edge of chassis. (Please see "NG-4") A clearance of 0.3 to 0.7mm is recommended.
- Design Guidance of Chassis (Side Part)
 - 6) Upper Electrode and Lower Electrode fall on the edge of Touch Panel outline. Redundant design having enough clearance to avoid electric short with chassis is highly recommended. (Please see "NG-5")



- Example of Recommended Chassis Design
 - Refer to "3.2 Outward Form".
- As a terminal resistance has individual specificity, calibration to align the displaying and the sensing position one each is mandatory before use.