

### (i) Preliminary Specifications

### (v) Final Specifications

|                   |                         |
|-------------------|-------------------------|
| <b>Module</b>     | 10.1 Inch Color TFT-LCD |
| <b>Model Name</b> | G101EVN01.3             |

|                                  |             |                                                                 |                   |
|----------------------------------|-------------|-----------------------------------------------------------------|-------------------|
| <b>Customer</b>                  | <b>Date</b> | <b>Approved by</b>                                              | <b>Date</b>       |
|                                  |             | <u>Grace Hung</u>                                               | <u>2016/04/14</u> |
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|                                  |             | General Display Business Division /<br>AU Optronics corporation |                   |



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## Record of Revision

# Product Specification

AU OPTRONICS CORPORATION

G101EVN01.3

| Version and Date | Page | Old description           | New Description |
|------------------|------|---------------------------|-----------------|
| 1.0 2016/04/14   | All  | First draft specification | -               |





## 1. Operating Precautions

- 1) Since front polarizer is easily damaged, please be cautious and not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or soft cloth.
- 5) Since the panel is made of glass, it may be broken or cracked if dropped or bumped on hard surface.
- 6) To avoid ESD (Electro Static Discharge) damage, be sure to ground yourself before handling TFT-LCD Module.
- 7) Do not open nor modify the module assembly.
- 8) Do not press the reflector sheet at the back of the module to any direction.
- 9) In case if a module has to be put back into the packing container slot after it was taken out from the container, do not press the center of the LED light bar edge. Instead, press at the far ends of the LED light bar edge softly. Otherwise the TFT Module may be damaged.
- 10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 11) TFT-LCD Module is not allowed to be twisted & bent even force is added on module in a very short time. Please design your display product well to avoid external force applying to module by end-user directly.
- 12) Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.
- 13) Severe temperature condition may result in different luminance, response time and lamp ignition voltage.
- 14) Continuous operating TFT-LCD display under low temperature environment may accelerate lamp exhaustion and reduce luminance dramatically.
- 15) The data on this specification sheet is applicable when LCD module is placed in landscape position.
- 16) Continuous displaying fixed pattern may induce image sticking. It's recommended to use screen saver or shuffle content periodically if fixed pattern is displayed on the screen.



## 2. General Description

G101EVN01.3 is a Color Active Matrix Liquid Crystal Display composed of a TFT LCD panel, a driver circuit, and LED backlight system. The screen format is intended to support the 16:10 WXGA, 1280(H) x 800(V) screen and 16.2M colors (RGB 6-bits + 2-bits dithering) with LED backlight driving circuit. All input signals are LVDS interface compatible.

### 2.1 Display Characteristics

The following items are characteristics summary under 25 °C condition:

| Items                                                     | Unit         | Specifications                                             |
|-----------------------------------------------------------|--------------|------------------------------------------------------------|
| Screen Diagonal                                           | [inch]       | 10.1"                                                      |
| Active Area                                               | [mm]         | 216.96(H) x 135.6(V)                                       |
| Pixels H x V                                              |              | 1280 x 3(RGB) x 800                                        |
| Pixel Pitch                                               | [mm]         | 0.1695 X 0.1695                                            |
| Pixel Arrangement                                         |              | R.G.B. Vertical Stripe                                     |
| Display Mode                                              |              | MVA, Normally Black                                        |
| Nominal Input Voltage VDD                                 | [Volt]       | 3.3 (Typical)                                              |
| Power Consumption                                         | [Watt]       | 6.65 max. (Include Logic and Blu power)                    |
| Weight                                                    | [Grams]      | 350 g max.                                                 |
| Physical Size                                             | [mm]         | 227.91(H) x 148(V)x7.6 (T) (without connector)             |
| Electrical Interface                                      |              | LVDS                                                       |
| Surface Treatment                                         |              | Anti-Reflection $\leq 1.5\%$ , Hardness 3H<br>Anti- Static |
| Support Color                                             |              | 16.2M (8bits) / 262K colors (6bits)                        |
| Temperature Range<br>Operating<br>Storage (Non-Operating) | [°C]<br>[°C] | -20 to +70 (Central)<br>-30 to +80                         |
| RoHS Compliance                                           |              | RoHS Compliance                                            |



## 2.2 Optical Characteristics

The optical characteristics are measured under stable conditions at 25°C (Room Temperature):

| Item                                        | Unit                 | Conditions                              | Min.  | Typ.  | Max.  | Note |
|---------------------------------------------|----------------------|-----------------------------------------|-------|-------|-------|------|
| White Luminance                             | [cd/m <sup>2</sup> ] | I <sub>LED</sub> = 45mA<br>(5p average) | 400   | 500   |       |      |
| Uniformity                                  | %                    | 5 points                                | 70%   |       |       |      |
| Contrast Ratio                              |                      |                                         | 1000  | 1300  | -     |      |
| Response Time                               | [msec]               | Rising + Falling                        | ---   | 25    | 35    |      |
| Viewing Angle                               | [degree]             | Horizontal (Right)                      | 80    | 85    | ---   |      |
|                                             | [degree]             | CR = 10 (Left)                          | 80    | 85    | ---   |      |
|                                             | [degree]             | Vertical (Upper)                        | 80    | 85    | ---   |      |
|                                             | [degree]             | CR = 10 (Lower)                         | 80    | 85    | ---   |      |
| Color / Chromaticity Coordinates (CIE 1931) |                      | Red x                                   | 0.549 | 0.579 | 0.609 |      |
|                                             |                      | Red y                                   | 0.308 | 0.338 | 0.368 |      |
|                                             |                      | Green x                                 | 0.295 | 0.325 | 0.355 |      |
|                                             |                      | Green y                                 | 0.530 | 0.56  | 0.590 |      |
|                                             |                      | Blue x                                  | 0.122 | 0.152 | 0.182 |      |
|                                             |                      | Blue y                                  | 0.095 | 0.125 | 0.155 |      |
|                                             |                      | White x                                 | 0.263 | 0.313 | 0.363 |      |
|                                             |                      | White y                                 | 0.279 | 0.329 | 0.379 |      |
| Color Gamut                                 | %                    |                                         | -     | 45    | -     |      |

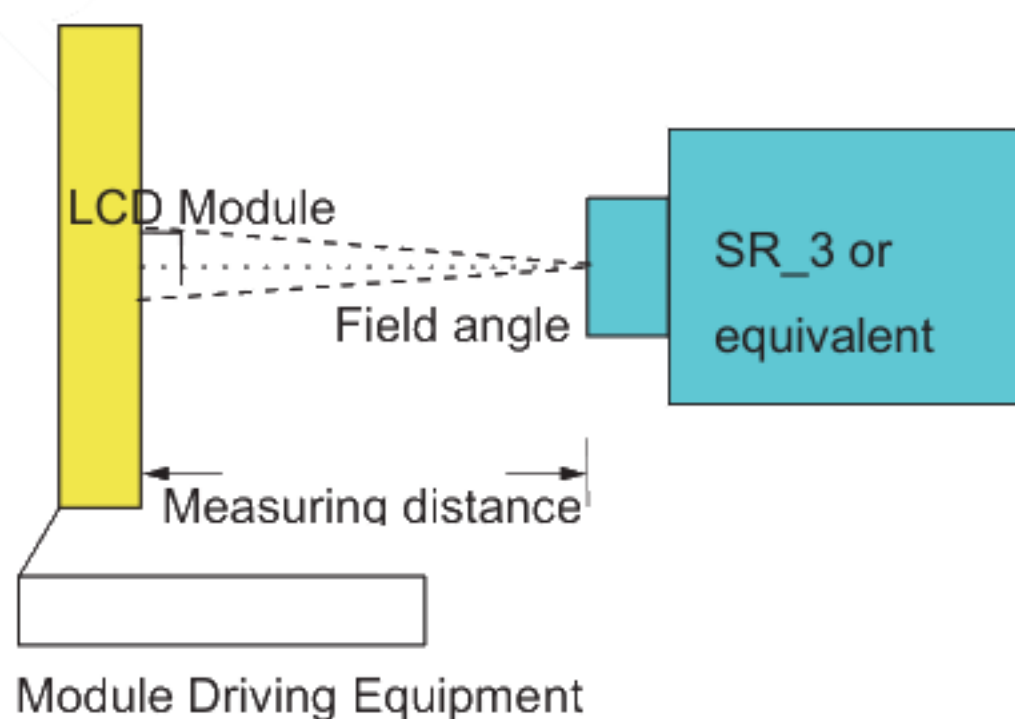
Note 1: Measurement method

Equipment Pattern Generator, Power Supply, Digital Voltmeter, Luminance meter (SR\_3 or equivalent)

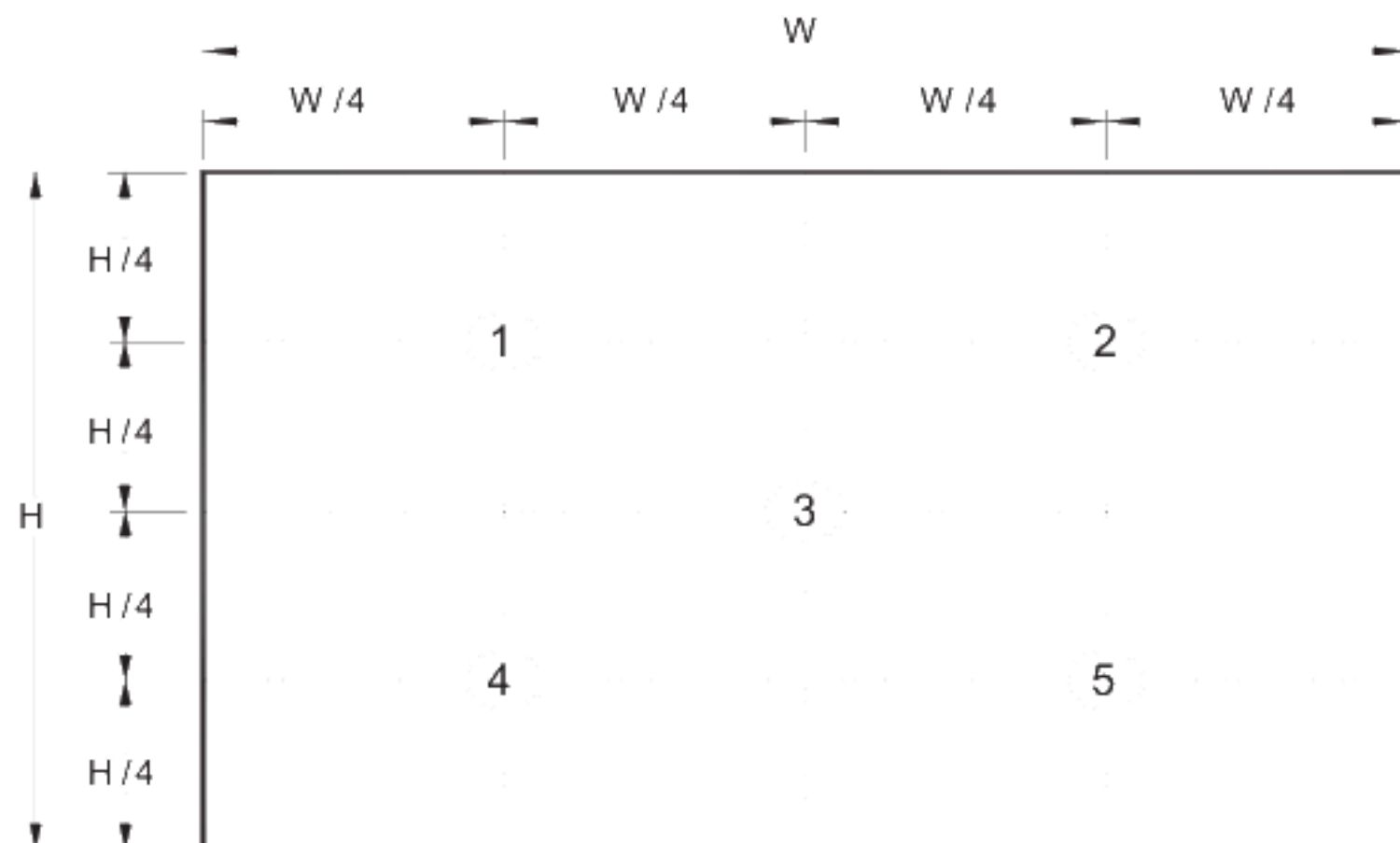
Aperture Field angle 2\_ with 50cm measuring distance

Test Point Follow Note 2 position

Environment < 1 lux



Note 2: Definition of 5 points position



Note 3: The luminance uniformity of 5 points is defined by dividing the minimum luminance values by the maximum test point luminance

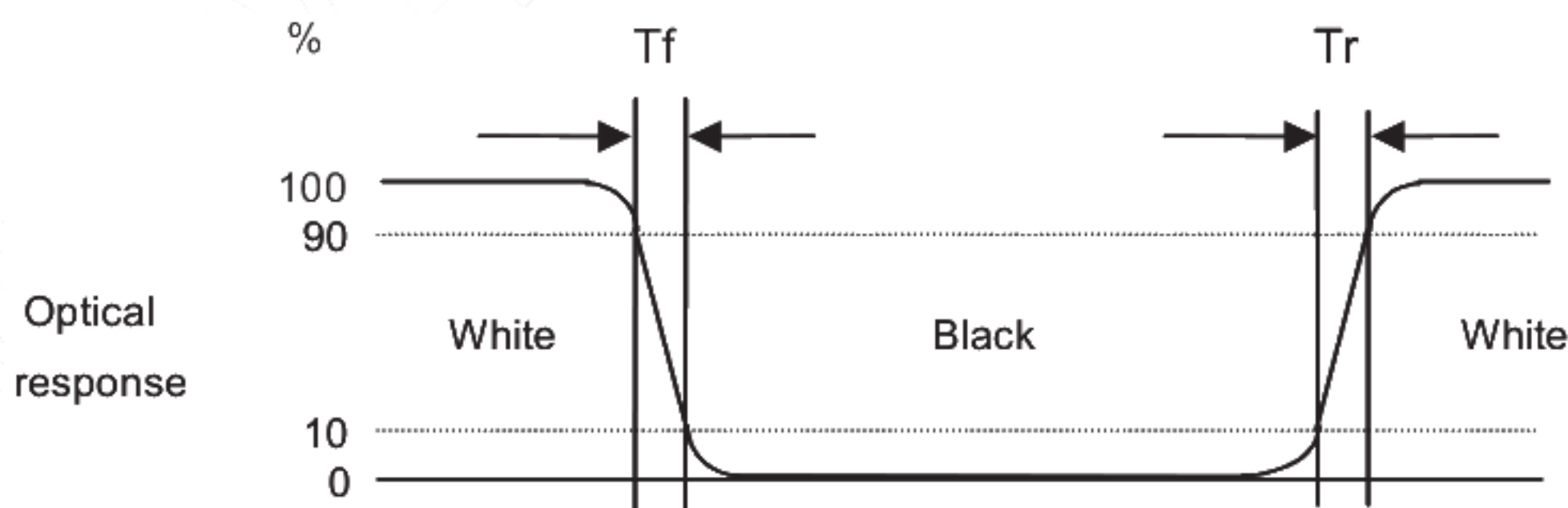
$$\delta_{W9} = \frac{\text{Minimum Brightness of five points}}{\text{Maximum Brightness of five points}}$$

Note 4: Definition of contrast ratio (CR):

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness on the "White" state}}{\text{Brightness on the "Black" state}}$$

Note 5: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "White" to "Black" (falling time) and from "Black" to "White" (rising time), respectively. The response time interval is between 10% and 90% of amplitudes. Please refer to the figure as below.

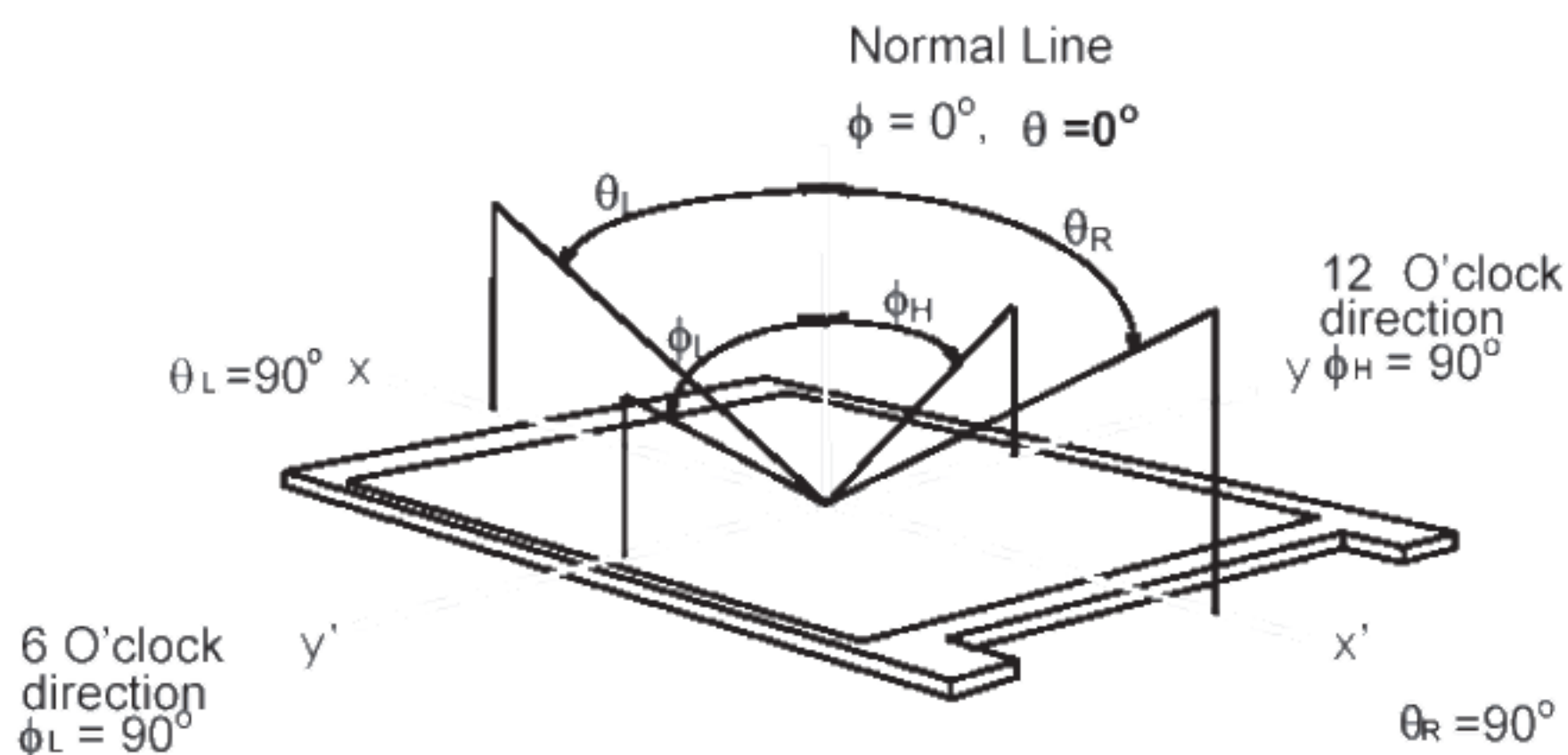






## Note 6: Definition of viewing angle

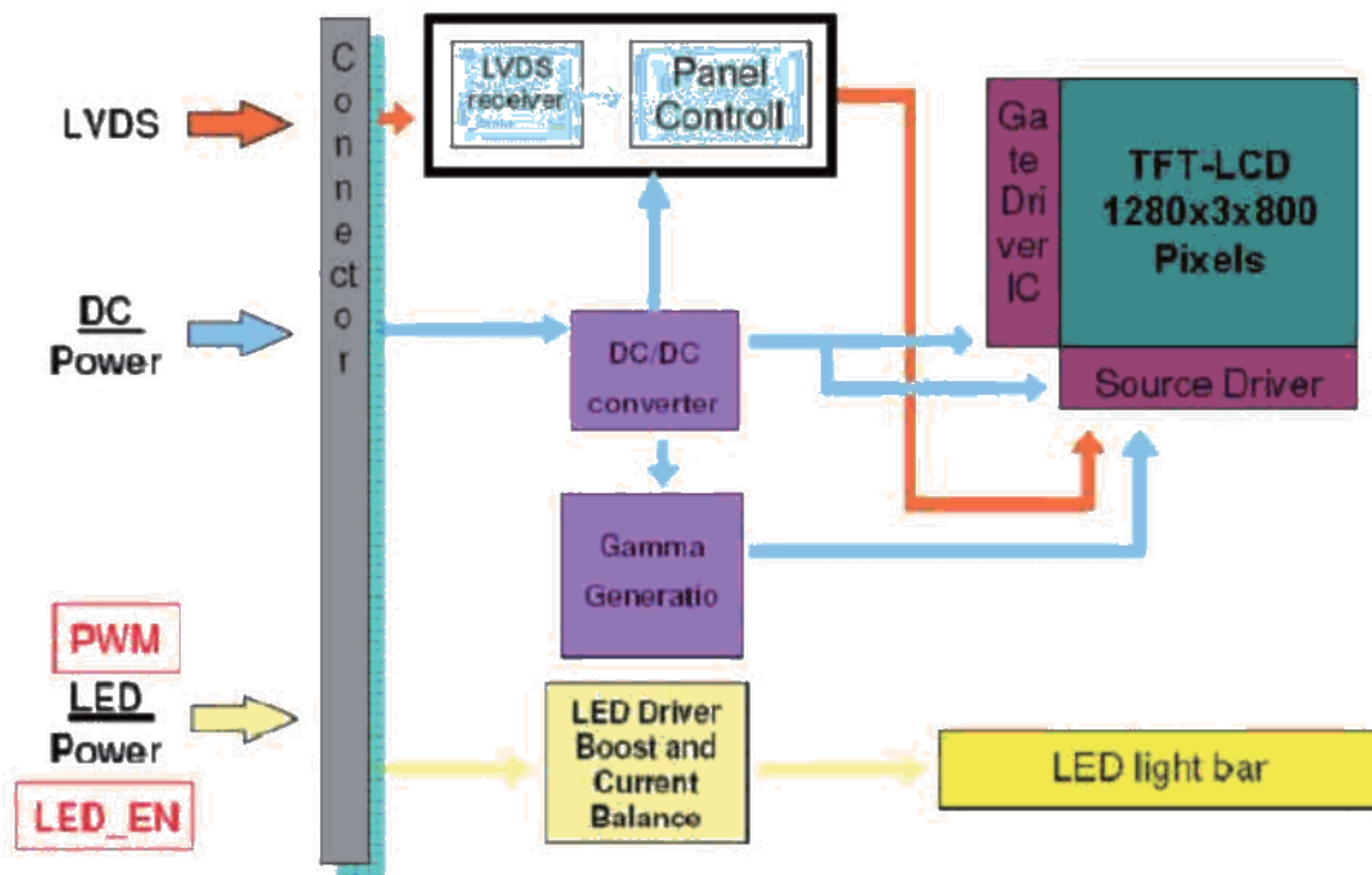
Viewing angle is the measurement of contrast ratio  $\geq 10$ , at the screen center, over a  $180^\circ$  horizontal and  $180^\circ$  vertical range (off-normal viewing angles). The  $180^\circ$  viewing angle range is broken down as below:  $90^\circ$  ( $\theta$ ) horizontal left and right, and  $90^\circ$  ( $\phi$ ) vertical high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated to its center to develop the desired measurement viewing angle.





## 3. Functional Block Diagram

The following diagram shows the functional block of the 10.1 inch color TFT/LCD module:



## 4. Absolute Maximum Ratings

### 4.1 Absolute Ratings of TFT LCD Module

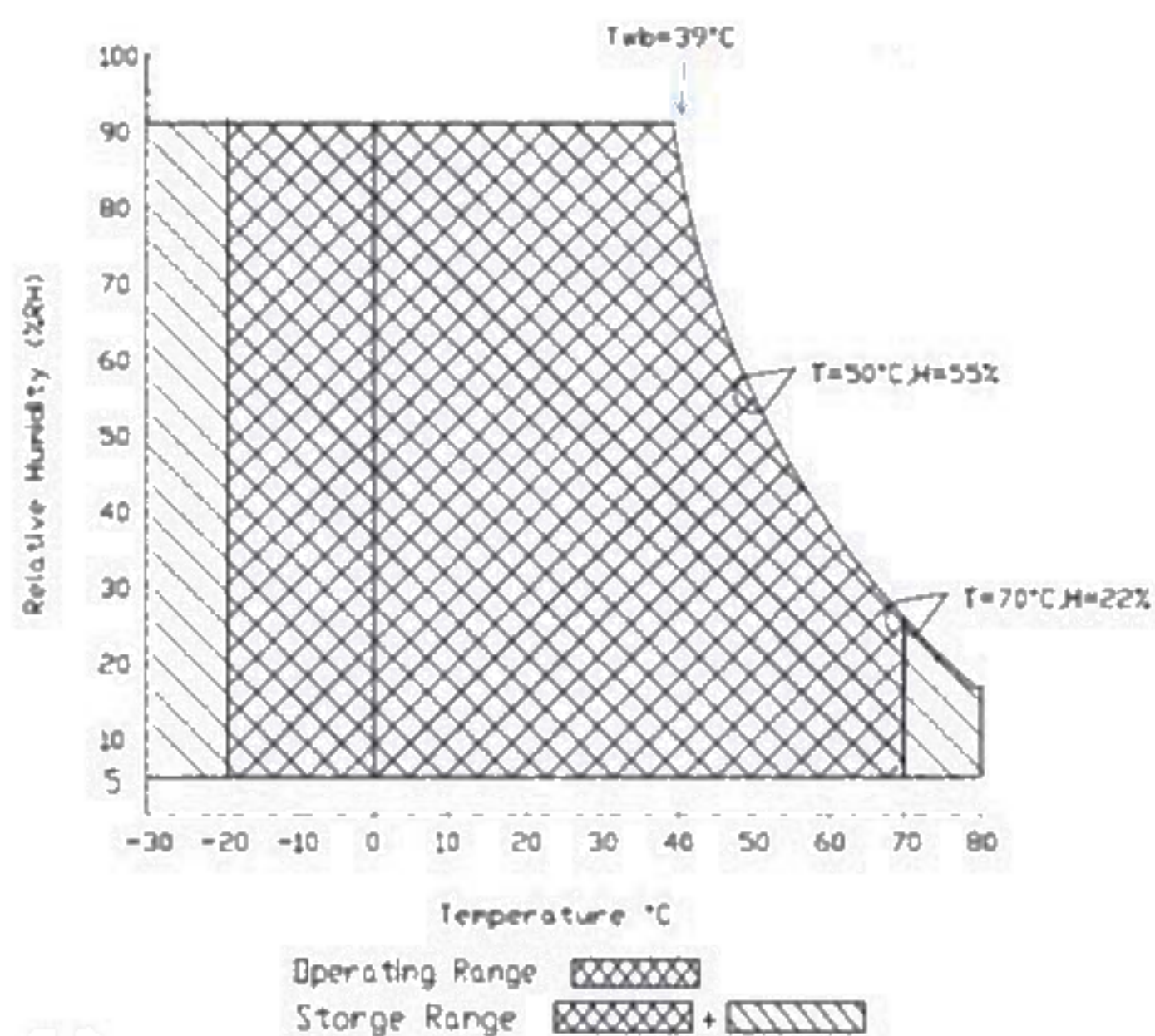
| Item                      | Symbol              | Min  | Max     | Unit   |
|---------------------------|---------------------|------|---------|--------|
| Logic/LCD drive Voltage   | VDD                 | -0.3 | +4.0    | [Volt] |
| LCD Input Signal Voltage  | V <sub>SIGNAL</sub> | -0.3 | VDD+0.3 | [Volt] |
| LED BLU Drive Voltage     | VLED                | -0.3 | +15     | [Volt] |
| LED Dimming Input Voltage | VPWM_EN             | -0.3 | +15     | [Volt] |
| LED Enable Input Voltage  | VLED_EN             | -0.3 | +15     | [Volt] |

### 4.2 Absolute Ratings of Environment

| Item                  | Symbol | Min | Max          | Unit  |
|-----------------------|--------|-----|--------------|-------|
| Operating Temperature | TOP    | -20 | +70(Central) | [°C]  |
| Operation Humidity    | HOP    | 5   | 90           | [%RH] |
| Storage Temperature   | TST    | -30 | +80          | [°C]  |
| Storage Humidity      | HST    | 5   | 90           | [%RH] |

Note 1: Permanent damage to the device may occur if exceed maximum values

Note 2: Maximum wet-bulb temperature is less than 39°C and no condensation







## 5. Electrical Characteristics

### 5.1 TFT LCD Module

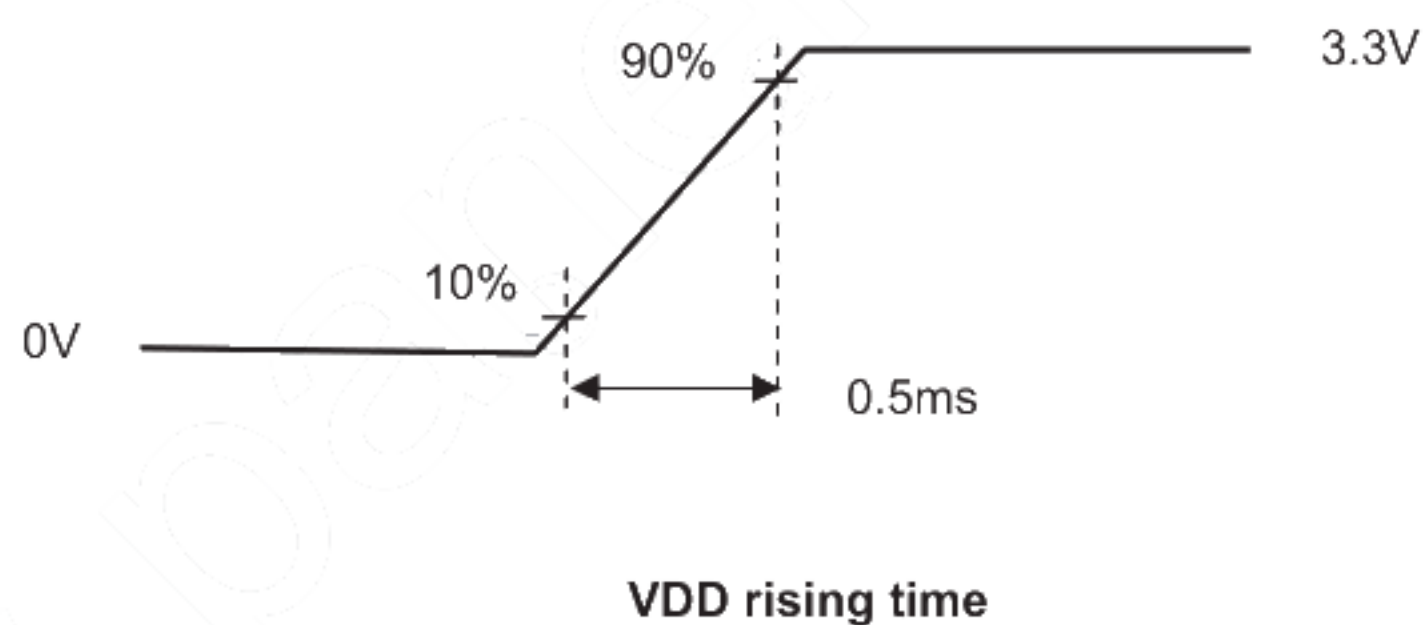
#### 5.1.1 Power Specification

The power specification are measured under 25°C and frame frequency under 60Hz

| Symbol | Parameter                                | Min | Typ  | Max  | Units       | Remark                                   |
|--------|------------------------------------------|-----|------|------|-------------|------------------------------------------|
| VDD    | Logic/LCD Drive Voltage                  | 3.0 | 3.3  | 3.6  | [Volt]      |                                          |
| IDD    | VDD Current                              | -   | 192  | 270  | [mA]        | All Black Pattern<br>(VDD=3.3V, at 60Hz) |
| Irush  | LCD Inrush Current                       | -   | -    | 1500 | [mA]        | Note 1                                   |
| PDD    | VDD Power                                | -   | 0.63 | 0.89 | [Watt]      | All Black Pattern<br>(VDD=3.3V, at 60Hz) |
| VDDrp  | Allowable Logic/LCD Drive Ripple Voltage | -   | -    | 100  | [mV]<br>p-p | All Black Pattern<br>(VDD=3.3V, at 60Hz) |

Note 1 : Maximum Measurement Condition : White Pattern at 3.3V driving voltage. ( $P_{\max} = V_{3.3} \times I_{\text{white}}$ )

Note 2 : Measure Condition



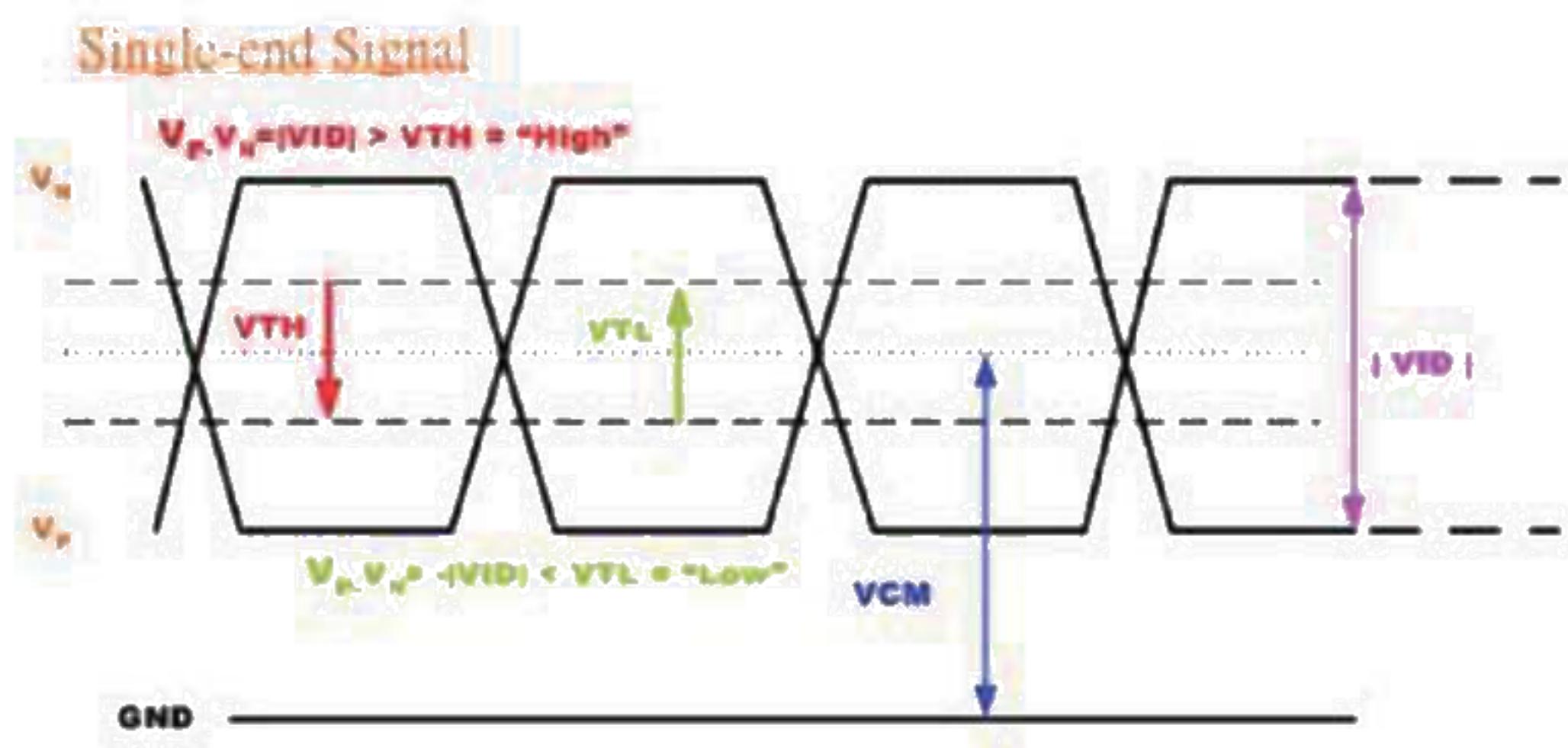
## 5.1.2 Signal Electrical Characteristics

Input signals shall be low or Hi-Z state when VDD is off.

Signal electrical characteristics are as follows;

| Symbol | Item                                   | Min. | Typ. | Max. | Unit | Remark          |
|--------|----------------------------------------|------|------|------|------|-----------------|
| VTH    | Differential Input High Threshold      | ---  |      | 100  | [mV] | VCM=1.2V        |
| VTL    | Differential Input Low Threshold       | -100 |      | ---- | [mV] | VCM=1.2V        |
| VID    | Input Differential Voltage             | 100  |      | 600  | [mV] |                 |
| VICM   | Differential Input Common Mode Voltage | 0.7  |      | 1.6  | [V]  | VTH/VTl=+-100mV |

Note: LVDS Signal Waveform.







## 5.2 Backlight Unit

### 5.2.1 Parameter guideline for LED

Following characteristics are measured under a stable condition using an inverter at 25 (Room Temperature):

| Symbol              | Parameter               | Min.   | Typ. | Max. | Unit   | Remark                                    |
|---------------------|-------------------------|--------|------|------|--------|-------------------------------------------|
| VLED                | Input Voltage           | 10.8   | 12   | 13.2 | [Volt] |                                           |
| I <sub>VLED</sub>   | Input Current           | -      | 300  | 480  | [mA]   | 100% Brightness<br>(VLED = 12V)           |
| P <sub>VLED</sub>   | Power Consumption       | -      | 3.6  | 5.76 | [Watt] | 100% Brightness<br>(VLED = 12V)           |
| V <sub>EN_HI</sub>  | Enable Input High Level | 2.5    | -    | 5.5  | [Volt] | VLED_EN                                   |
| V <sub>EN_LO</sub>  | Enable Input Low Level  | -      | -    | 0.8  | [Volt] |                                           |
| F <sub>PWM</sub>    | Dimming Frequency       | 200    | -    | 20K  | [Hz]   | PWM Dimming                               |
| V <sub>PWM_HI</sub> | Logic Input High Level  | 2.5    | -    | 5.5  | [Volt] |                                           |
| V <sub>PWM_LO</sub> | Logic Input Low Level   | -      | -    | 0.8  | [Volt] |                                           |
|                     | Dimming duty cycle      | 5      | -    | 100  | %      |                                           |
| I <sub>F</sub>      | LED Forward Current     | -      | 45   | -    | [mA]   | Ta = 25°C                                 |
| Operation Life      |                         | 50,000 | -    | -    | Hrs    | (Ta=25°C), Note 2<br>I <sub>F</sub> =45mA |

Note 1: Ta means ambient temperature of TFT-LCD module.

Note 2: VLED, I<sub>VLED</sub>, P<sub>VLED</sub> are defined for LED backlight.(100% duty of PWM dimming)

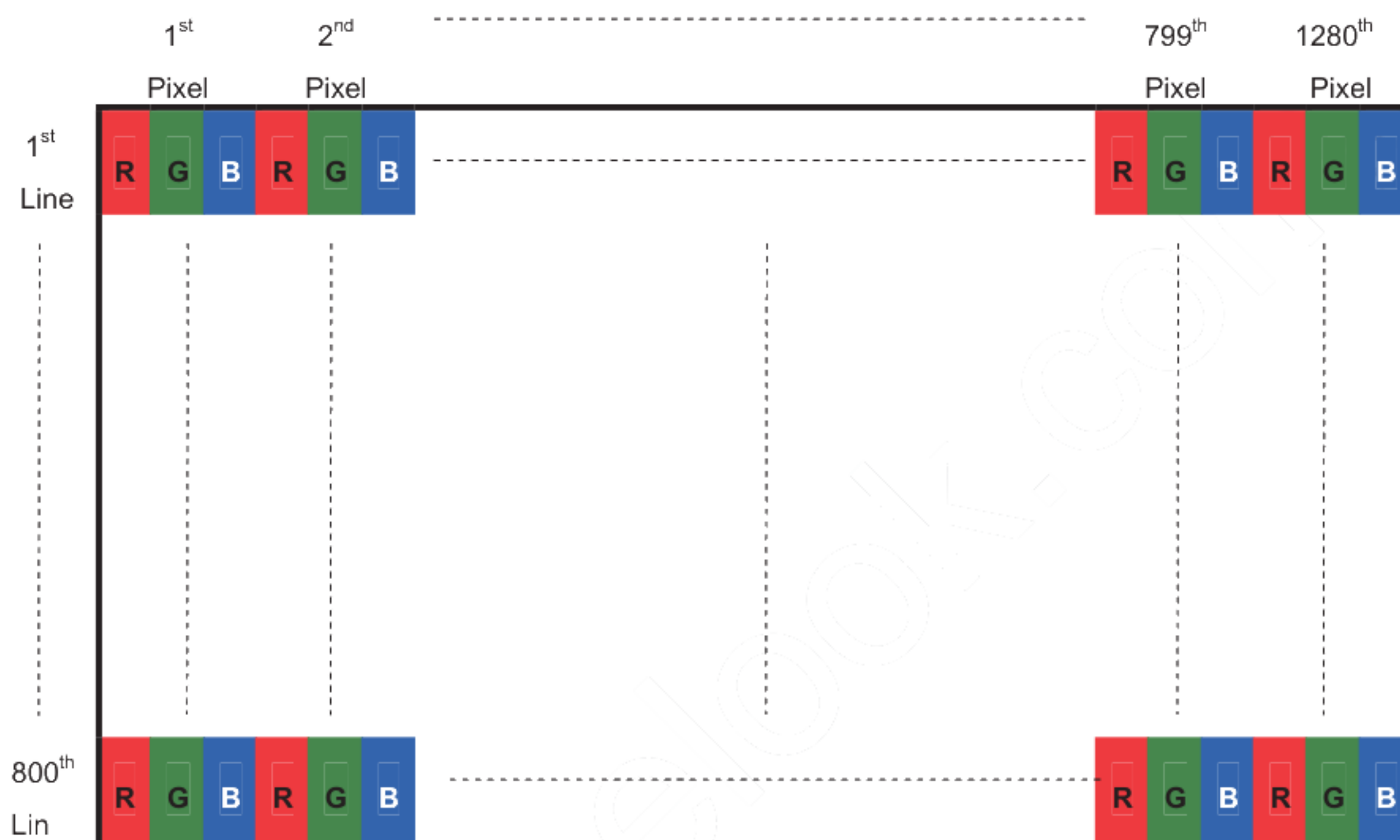
Note 3: If G101EVN0X.X module is driven by high current or at high ambient temperature & humidity condition. The operating life will be reduced.

Note 4: Operating life means brightness goes down to 50% initial brightness. Minimum operating life time is estimated data.

## 6. Signal Characteristic

### 6.1 Pixel Format Image

Following figure shows the relationship between input signal and LCD pixel format.







## 6.2 Signal Description

The module uses a LVDS receiver embedded in AUO's ASIC. LVDS is a differential signal technology for LCD interface and a high-speed data transfer device.

### 6.2.1 LVDS Connector Description

Physical interface is described as for the connector on module.

These connectors are capable of accommodating the following signals and will be following components.

| Connector Name / Designation | For Signal Connector                                                     |
|------------------------------|--------------------------------------------------------------------------|
| Manufacturer                 | Hirose or Compatible                                                     |
| Type / Part Number           | Recommended FFC or FPC described in the datasheet of FH41-40S-0.5SH(0.5) |
| Mating Housing/Part Number   | FH41-40S-0.5SH(0.5)                                                      |

### 6.2.2 LVDS Pin Assignment

LVDS is a differential signal technology for LCD interface and high speed data transfer device.

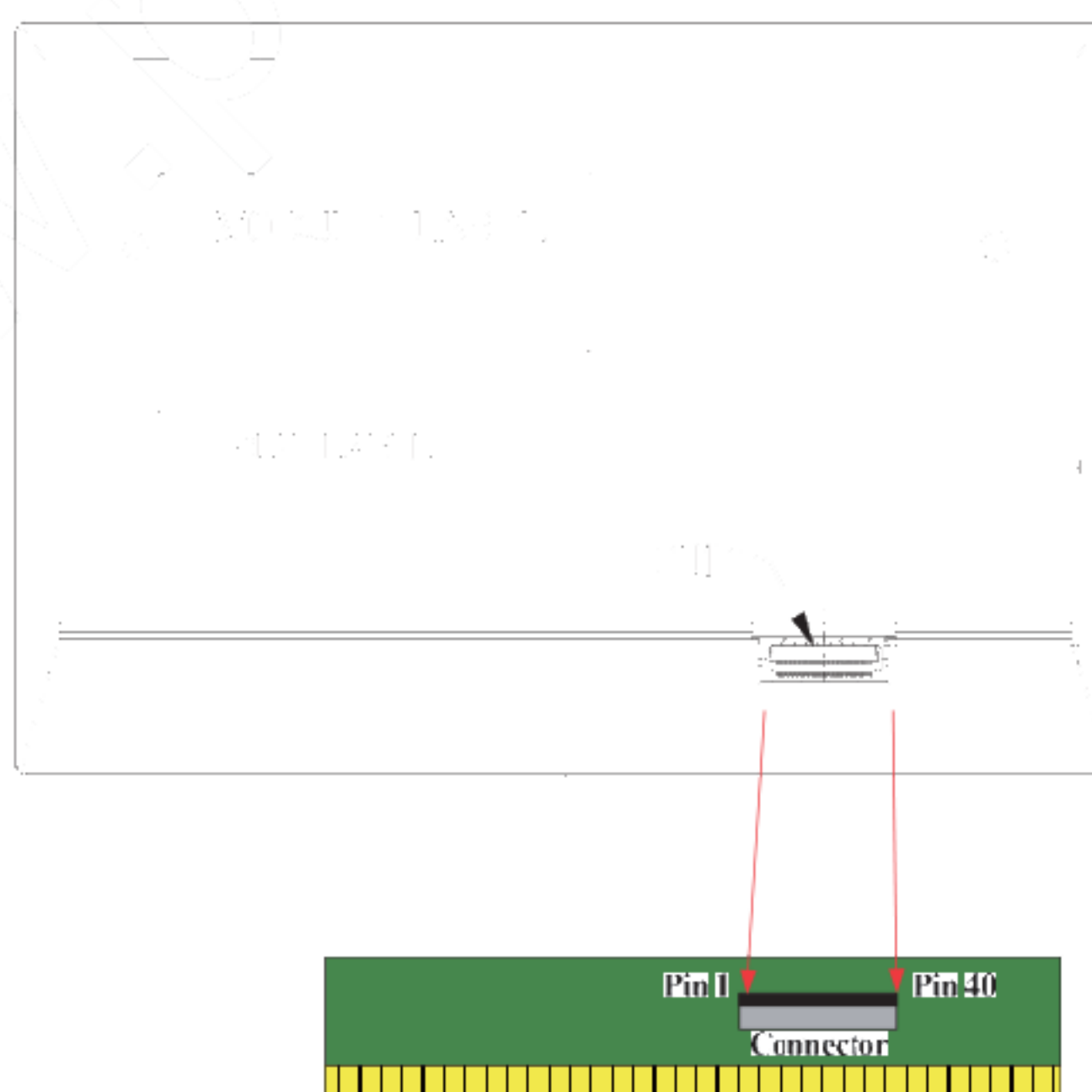
| Pin No | Symbol | Function                                         |
|--------|--------|--------------------------------------------------|
| 1      | NC     | No Connection (Reserve)                          |
| 2      | VDD    | Power Supply +3.3V                               |
| 3      | VDD    | Power Supply +3.3V                               |
| 4      | VDD    | Power Supply +3.3V                               |
| 5      | NC     | No Connection (Reserve)                          |
| 6      | NC     | No Connection (Reserve)                          |
| 7      | NC     | No Connection (Reserve)                          |
| 8      | Rin0-  | -LVDS differential data input(R0-R5,G0)          |
| 9      | Rin0+  | +LVDS differential data input(R0-R5,G0)          |
| 10     | GND    | Ground                                           |
| 11     | Rin1-  | -LVDS differential data input(G1-G5,B0-B1)       |
| 12     | Rin1+  | +LVDS differential data input(G1-G5,B0-B1)       |
| 13     | GND    | Ground                                           |
| 14     | Rin2-  | -LVDS differential data input(B2-B5,DE)          |
| 15     | Rin2+  | +LVDS differential data input(B2-B5,DE)          |
| 16     | GND    | Ground                                           |
| 17     | ClkIN- | -LVDS differential clock input                   |
| 18     | ClkIN+ | +LVDS differential clock input                   |
| 19     | GND    | Ground-Shield                                    |
| 20     | Rin3-  | -LVDS differential data input(R6-R7,G6-G7,B6-B7) |
| 21     | Rin3+  | +LVDS differential data input(R6-R7,G6-G7,B6-B7) |
| 22     | GND    | Ground-Shield                                    |

|    |          |                                                                                                                                                          |
|----|----------|----------------------------------------------------------------------------------------------------------------------------------------------------------|
| 23 | SEL68    | Selection for either 6bit or 8bit LVDS input:<br>SEL68 = " High" or "NC", accepts 6bit LVDS data input;<br>SEL68 = " Low", accepts 8bit LVDS data input. |
| 24 | NC       | No Connection (Reserve)                                                                                                                                  |
| 25 | GND      | Ground–Shield                                                                                                                                            |
| 26 | NC       | No Connection (Reserve)                                                                                                                                  |
| 27 | NC       | No Connection (Reserve)                                                                                                                                  |
| 28 | GND      | Ground–Shield                                                                                                                                            |
| 29 | NC       | No Connection (Reserve)                                                                                                                                  |
| 30 | NC       | No Connection (Reserve)                                                                                                                                  |
| 31 | VLED_GND | LED Ground                                                                                                                                               |
| 32 | VLED_GND | LED Ground                                                                                                                                               |
| 33 | VLED_GND | LED Ground                                                                                                                                               |
| 34 | NC       | No Connection (Reserve)                                                                                                                                  |
| 35 | VPWM_EN  | System PWM Logic Input Level                                                                                                                             |
| 36 | VLED_EN  | LED enable input level                                                                                                                                   |
| 37 | NC       | No Connection (Reserve)                                                                                                                                  |
| 38 | VLED     | LED Power Supply                                                                                                                                         |
| 39 | VLED     | LED Power Supply                                                                                                                                         |
| 40 | VLED     | LED Power Supply                                                                                                                                         |

Note 1: Input signals shall be low or High-impedance state when VDD is off.

Note 2: High stands for "3.3V", Low stands for "0V", NC means "No Connection".

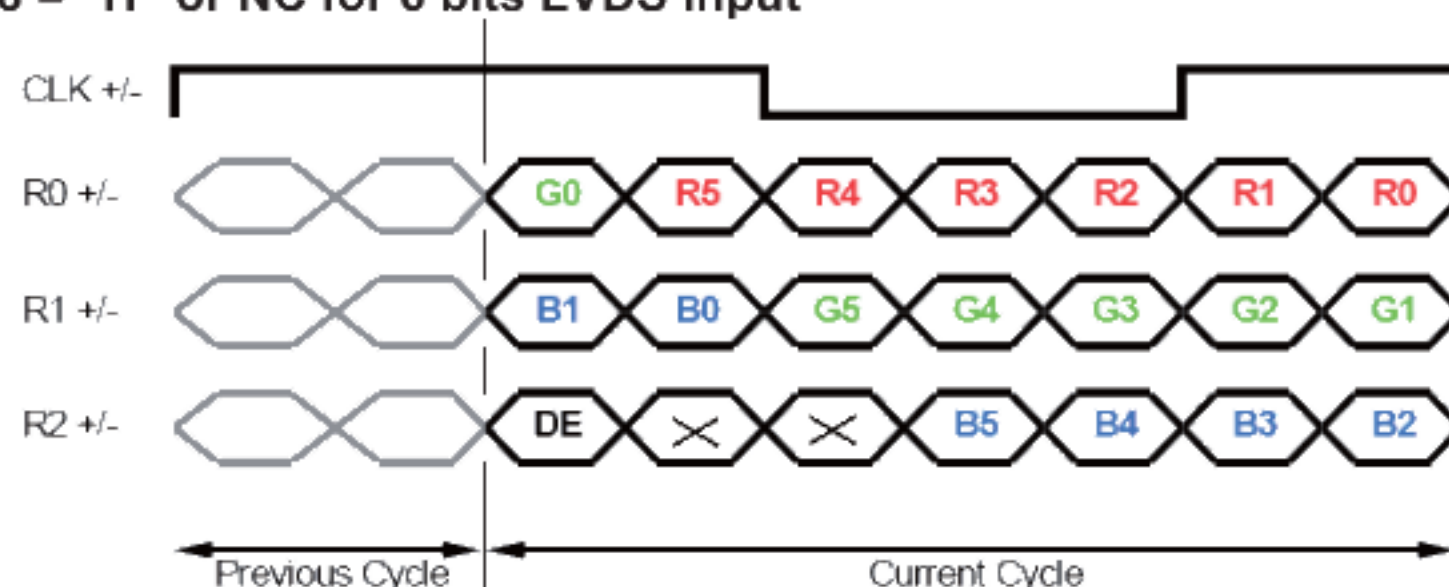
Note 3: RSV means "Reserved".



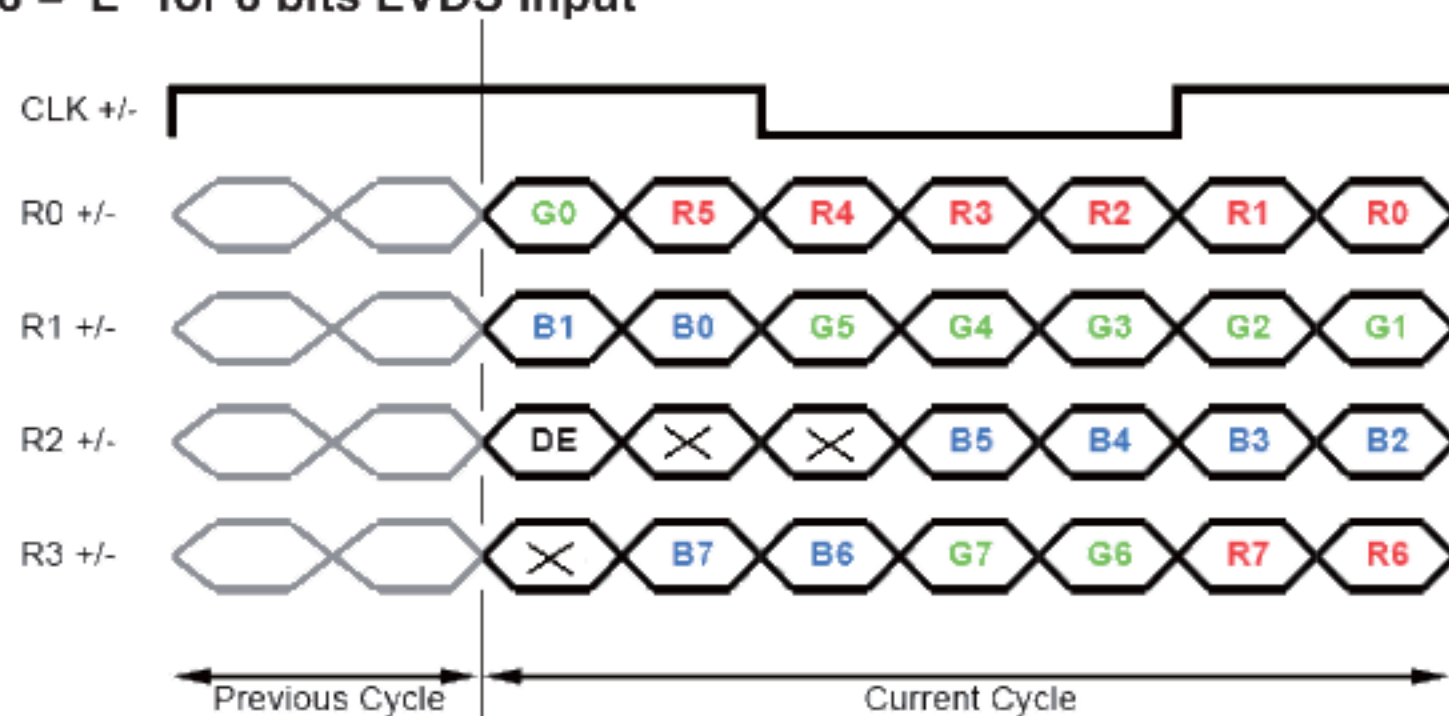


## 6.3 The Input Data Format

SEL68 = "H" or NC for 6 bits LVDS Input



SEL68 = "L" for 8 bits LVDS Input



| Signal Name | Description        |                                                                                                                                           |
|-------------|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------|
| R7          | Red Data 7 (MSB)   | Red-pixel Data                                                                                                                            |
| R6          | Red Data 6         |                                                                                                                                           |
| R5          | Red Data 5         | For 8Bits LVDS input<br>MSB: R7 ; LSB: R0                                                                                                 |
| R4          | Red Data 4         |                                                                                                                                           |
| R3          | Red Data 3         | For 6Bits LVDS input<br>MSB: R5 ; LSB: R0                                                                                                 |
| R2          | Red Data 2         |                                                                                                                                           |
| R1          | Red Data 1         |                                                                                                                                           |
| R0          | Red Data 0 (LSB)   |                                                                                                                                           |
| G7          | Green Data 7 (MSB) | Green-pixel Data                                                                                                                          |
| G6          | Green Data 6       |                                                                                                                                           |
| G5          | Green Data 5       | For 8Bits LVDS input<br>MSB: R7 ; LSB: R0                                                                                                 |
| G4          | Green Data 4       |                                                                                                                                           |
| G3          | Green Data 3       | For 6Bits LVDS input<br>MSB: R5 ; LSB: R0                                                                                                 |
| G2          | Green Data 2       |                                                                                                                                           |
| G1          | Green Data 1       |                                                                                                                                           |
| G0          | Green Data 0 (LSB) |                                                                                                                                           |
| B7          | Blue Data 7 (MSB)  | Blue-pixel Data                                                                                                                           |
| B6          | Blue Data 6        |                                                                                                                                           |
| B5          | Blue Data 5        | For 8Bits LVDS input<br>MSB: R7 ; LSB: R0                                                                                                 |
| B4          | Blue Data 4        |                                                                                                                                           |
| B3          | Blue Data 3        | For 6Bits LVDS input<br>MSB: R5 ; LSB: R0                                                                                                 |
| B2          | Blue Data 2        |                                                                                                                                           |
| B1          | Blue Data 1        |                                                                                                                                           |
| B0          | Blue Data 0 (LSB)  |                                                                                                                                           |
| RxCLKIN     | Data Clock         | The signal is used to strobe the pixel data and DE signals. All pixel data shall be valid at the falling edge when the DE signal is high. |
| DE          | Display Timing     | This signal is strobed at the falling edge of RxCLKIN. When the signal is high, the pixel data shall be valid to be displayed.            |

Note: Output signals from any system shall be low or High-impedance state when VDD is off.

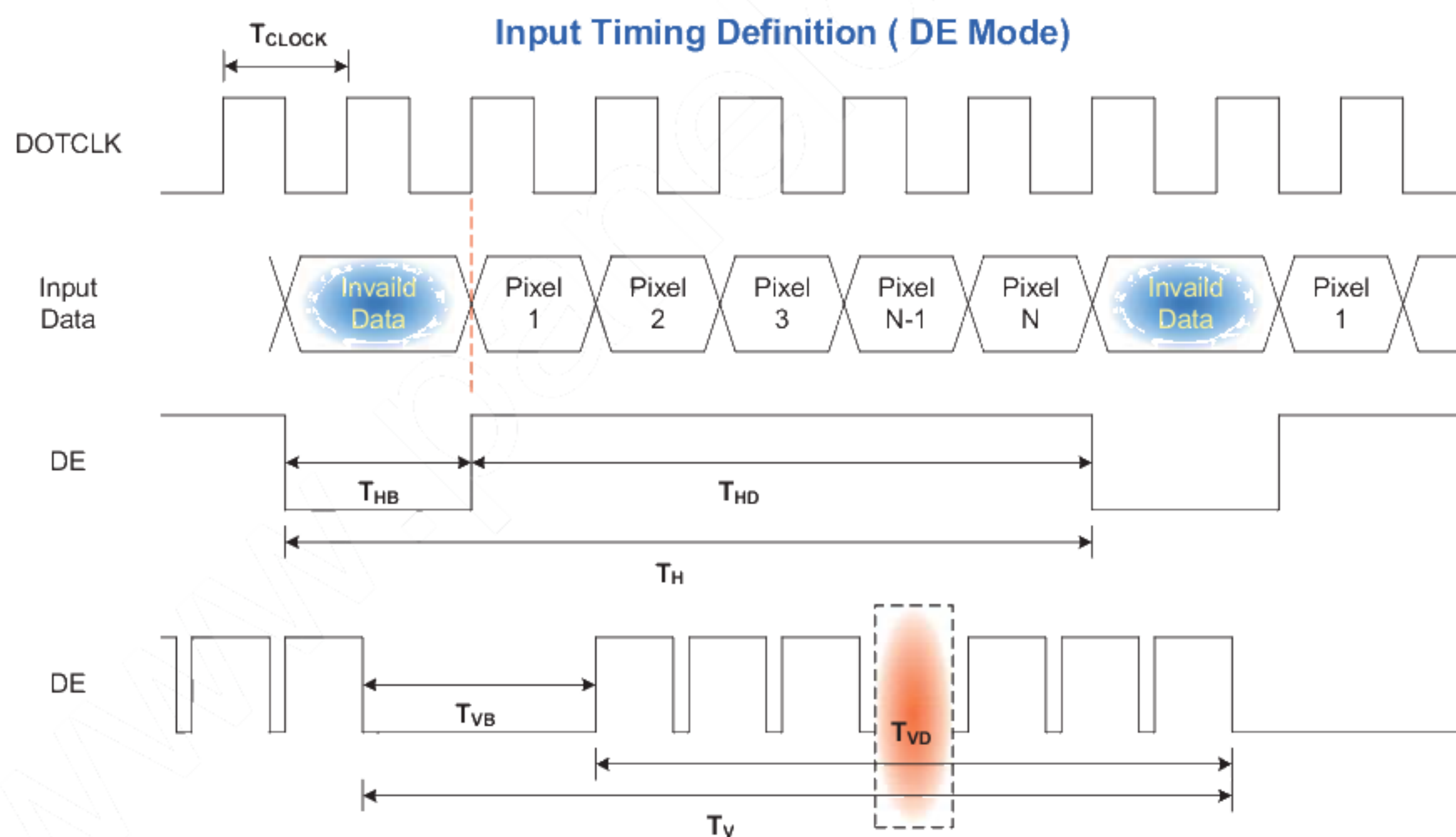
## 6.4 Interface Timing

### 6.4.1 Timing Characteristics

| Signal                |          | Symbol                | Min. | Typ.  | Max. | Unit               |
|-----------------------|----------|-----------------------|------|-------|------|--------------------|
| Clock Frequency       |          | 1/ T <sub>Clock</sub> | 64   | 68.93 | 85   | MHz                |
| Vertical<br>Section   | Period   | T <sub>V</sub>        | 808  | 816   | 1023 | T <sub>Line</sub>  |
|                       | Active   | T <sub>VD</sub>       | 800  |       |      |                    |
|                       | Blanking | T <sub>VB</sub>       | 8    | 16    | 223  |                    |
| Horizontal<br>Section | Period   | T <sub>H</sub>        | 1330 | 1408  | 2047 | T <sub>Clock</sub> |
|                       | Active   | T <sub>HD</sub>       | 1280 |       |      |                    |
|                       | Blanking | T <sub>HB</sub>       | 50   | 128   | 767  |                    |
| Frame Rate            |          | F                     | ---  | 60    | ---  | Hz                 |

Note : DE mode.

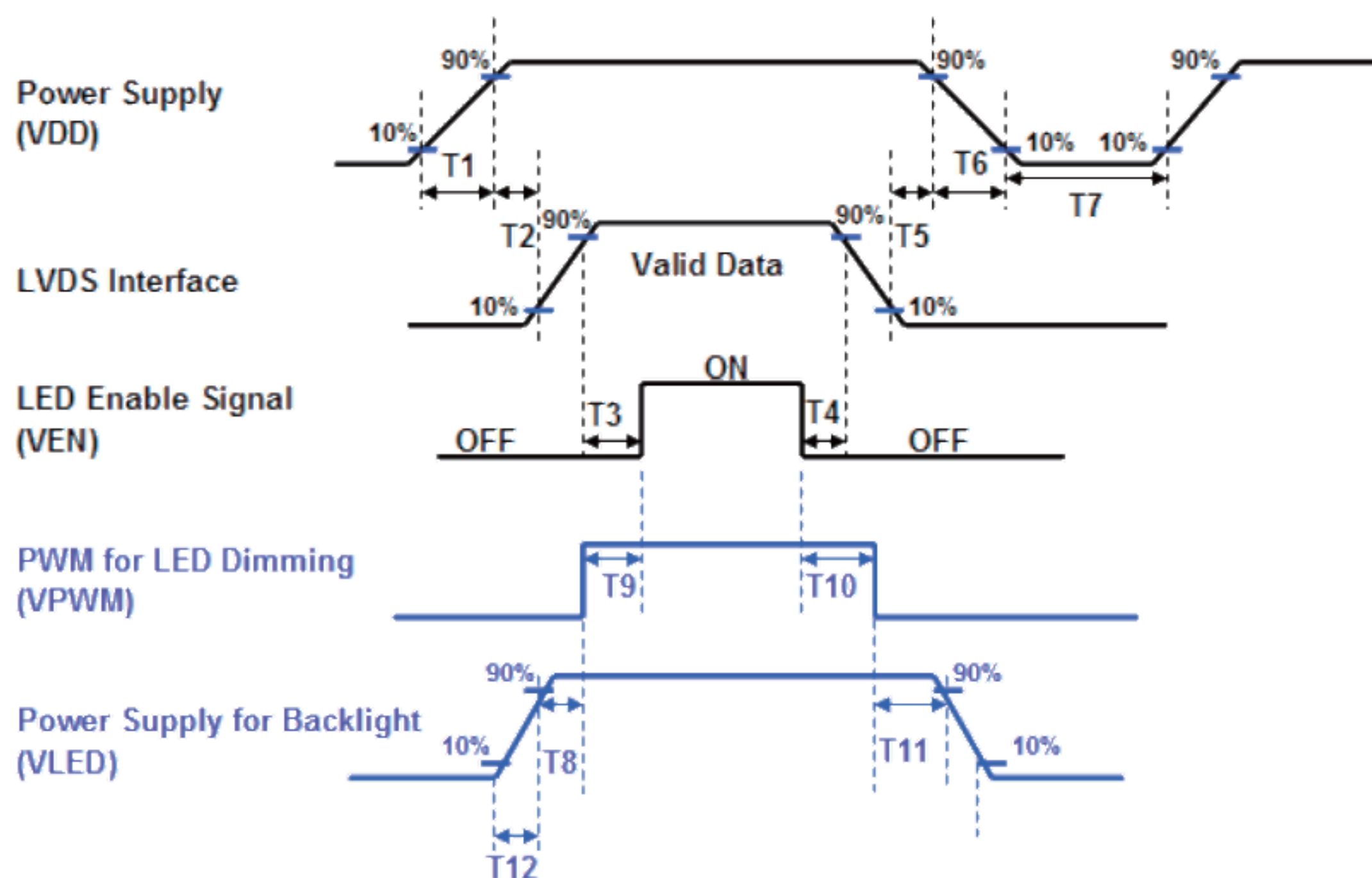
### 6.4.2 Input Timing Diagram





## 6.5 Power ON/OFF Sequence

VDD power and lamp on/off sequence is as below. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.



Power ON/OFF sequence timing

| Parameter | Value |      |      | Units |
|-----------|-------|------|------|-------|
|           | Min.  | Typ. | Max. |       |
| T1        | 0.5   |      | 10   | [ms]  |
| T2        | 0     |      | 50   | [ms]  |
| T3        | 200   |      | -    | [ms]  |
| T4        | 200   |      | -    | [ms]  |
| T5        | 0     |      | 50   | [ms]  |
| T6        | 0     |      | 10   | [ms]  |
| T7        | 500   |      | -    | [ms]  |
| T8        | 10    |      | -    | [ms]  |
| T9        | 0     |      | 180  | [ms]  |
| T10       | 0     |      | 180  | [ms]  |
| T11       | 10    |      | -    | [ms]  |
| T12       | 0.5   |      | 10   | [ms]  |

The above on/off sequence should be applied to avoid abnormal function in the display. Please make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.



## 7. Reliability Test Criteria

| Items                      | Required Condition                                                       | Note   |
|----------------------------|--------------------------------------------------------------------------|--------|
| Temperature Humidity Bias  | 40 °C /90%,300Hr                                                         |        |
| High Temperature Operation | 70 °C, 300Hr (center point of panel surface)                             |        |
| Low Temperature Operation  | -20 °C, 300Hr                                                            |        |
| Hot Storage                | 80 °C, 300Hr                                                             |        |
| Cold Storage               | -30 °C, 300Hr                                                            |        |
| Thermal Shock Test         | -30 °C /30 min , 70 °C /30 min , 20cycles                                |        |
| Hot Start Test             | 70 °C /1 Hr min. power on/off per 5 minutes, 5 times                     |        |
| Cold Start Test            | -20 °C /1 Hr min. power on/off per 5 minutes, 5 times                    |        |
| On/off test                | On/10 sec, Off/10 sec, 30,000 cycles                                     |        |
| ESD                        | Contact : ± 8KV/ operation, Class B<br>Air : ± 15KV / operation, Class B | Note 1 |

Note1: According to EN61000-4-2 , ESD class B: Some performance degradation allowed. No data lost

. Self-recoverable. No hardware failures.

Note2:

- Water condensation is not allowed for each test items.
- Each test is done by new TFT-LCD module. Don't use the same TFT-LCD module repeatedly for reliability test.
- The reliability test is performed only to examine the TFT-LCD module capability.
- To inspect TFT-LCD module after reliability test, please store it at room temperature and room humidity for 24 hours at least in advance.



## 8. Mechanical Characteristics

### 8.1 LCM Outline Dimension (Front View)



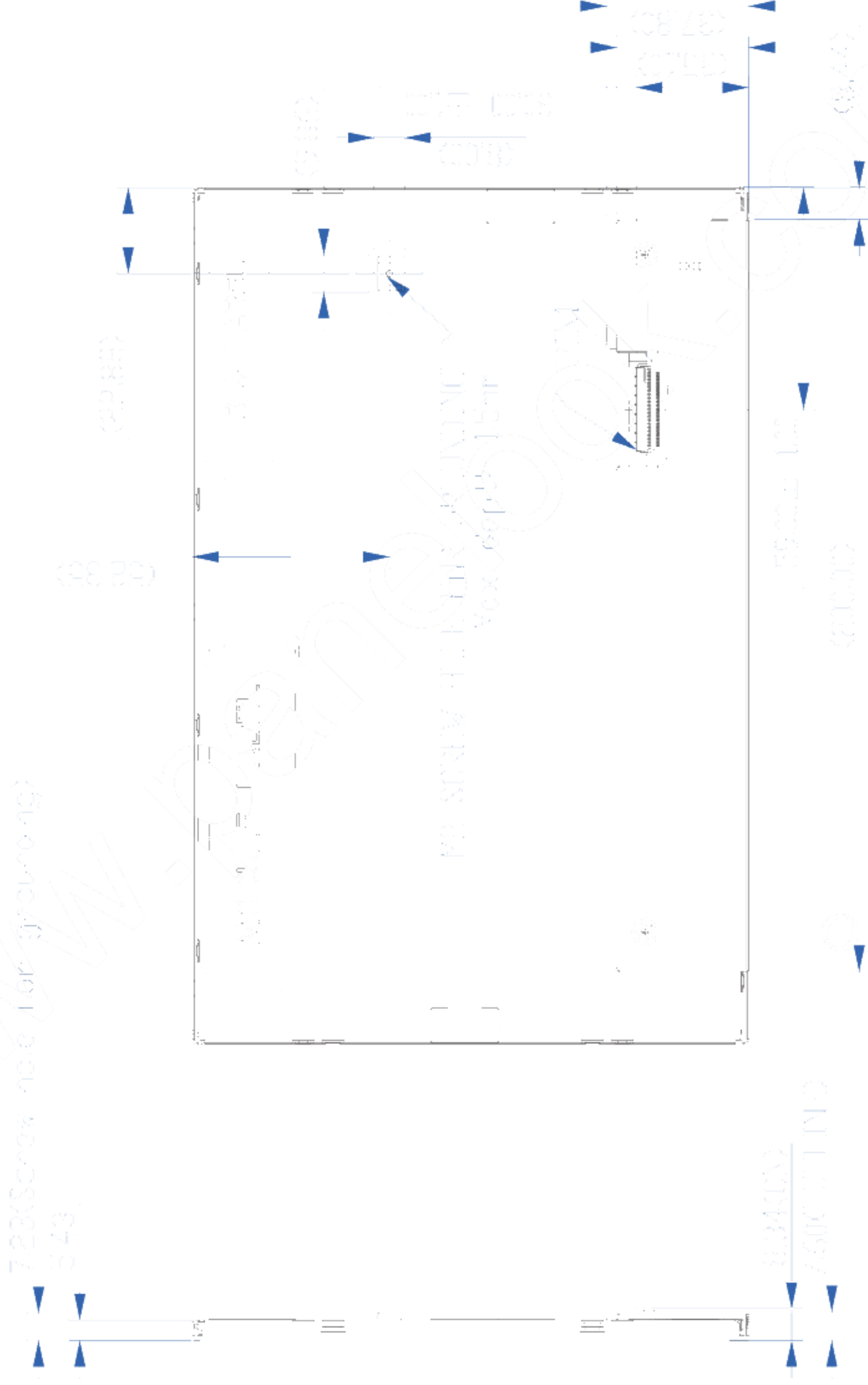


## Product Specification

AU OPTRONICS CORPORATION

G101EVN01.3

### 8.2 LCM Outline Dimension (Rear View)

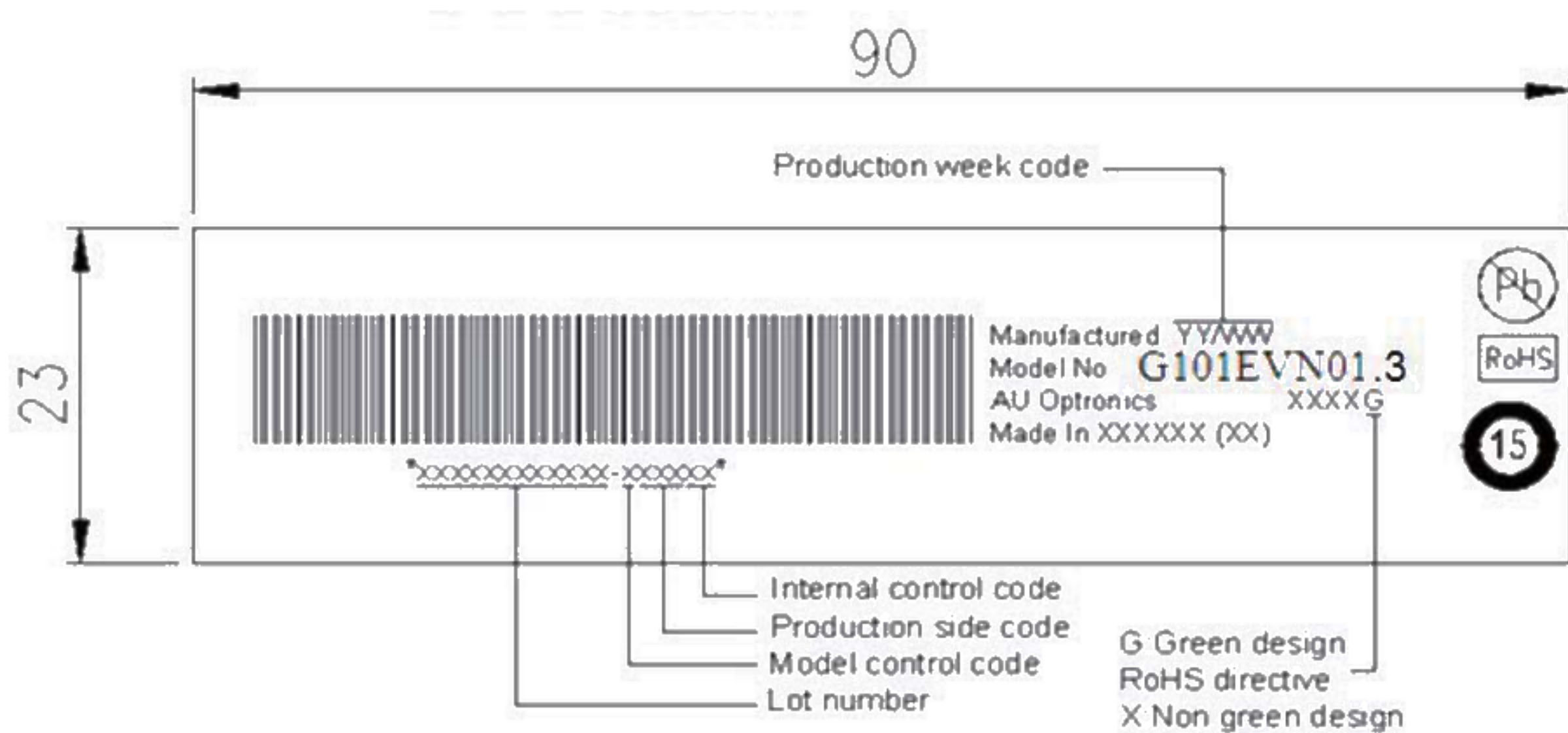




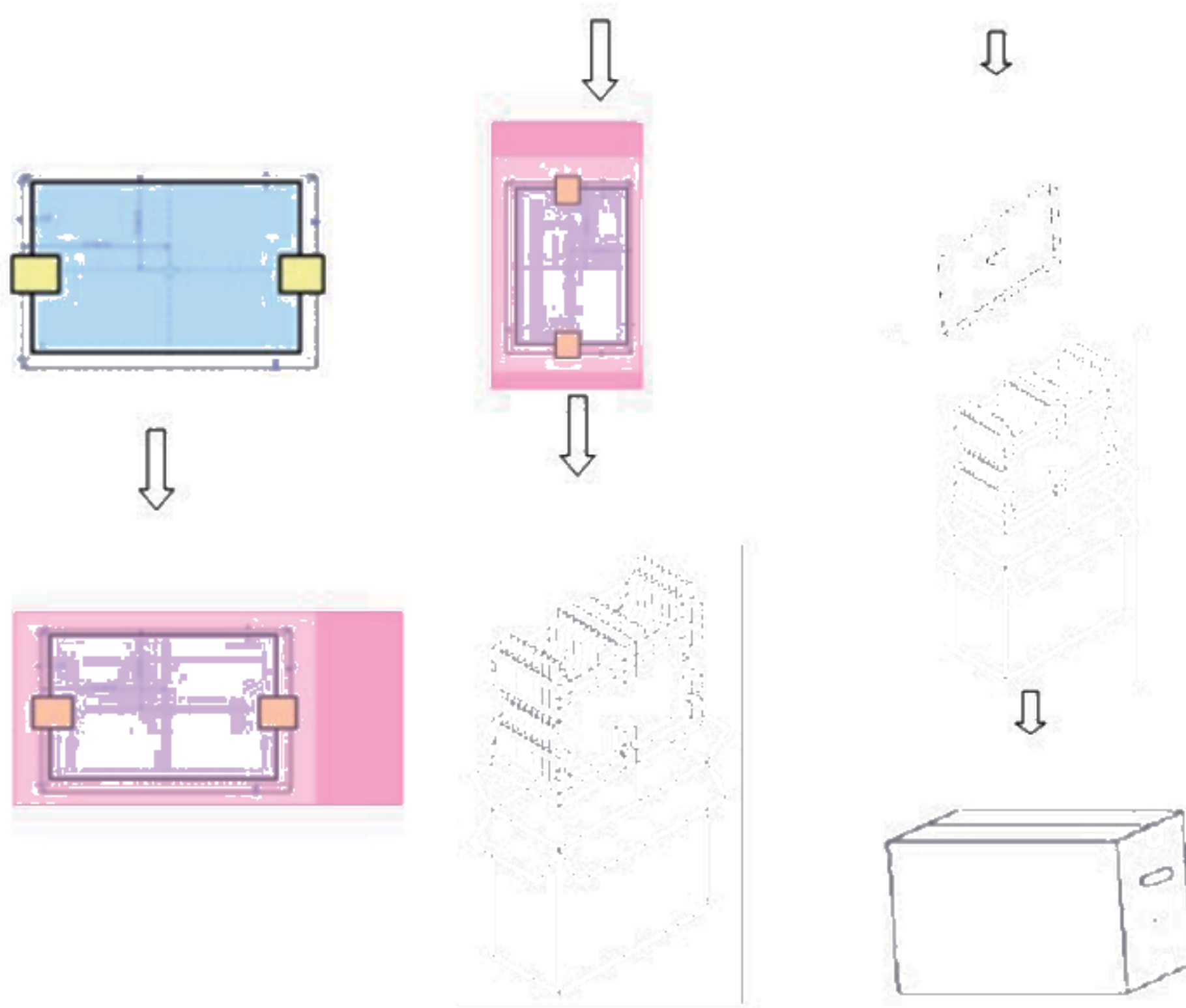
## 9. Label and Packaging

### 9.1 Shipping Label (on the rear side of TFT-LCD display)

Shipping label size: 90mm\*23mm



### 9.2 Carton/Pallet Package



44 pcs/Carton 24Carton/pallet



|                         | SHIPPING BY AIR       | SHIPPING BY SEA       | SHIPPING BY SEA_HQ    |
|-------------------------|-----------------------|-----------------------|-----------------------|
| MODULE/CARTON           | 44                    | 44                    | 44                    |
| CARTON/LAYER            | 6                     | 6                     | 6                     |
| LAYER/PALLET            | 4                     | 4+1                   | 4+2                   |
| MODULE/PALLET           | 1056                  | 1320                  | 1584                  |
| CARTON SIZE(MM)         | 450(L)*375(W)*320(H)  | 450(L)*375(W)*320(H)  | 450(L)*375(W)*320(H)  |
| PALLET SIZE(MM)         | 1150(L)*910(W)*132(H) | 1150(L)*910(W)*132(H) | 1150(L)*910(W)*132(H) |
| TOTAL PALLET HEIGHT(MM) | 1412                  | 1864                  | 2184                  |
| TOTAL PALLET WEIGHT(KG) | 298                   | 372                   | 446                   |



ONE PALLET SHIPMENT EXAMPLE  
SHIPPING BY AIR USED

TWO PALLETS SHIPMENT EXAMPLE  
SHIPPING BY SEA USED

TWO PALLETS SHIPMENT EXAMPLE  
SHIPPING BY SEA\_HQ USED





## 10 Safety

### 10.1 Sharp Edge Requirements

There will be no sharp edges or comers on the display assembly that could cause injury.

### 10.2 Materials

#### 10.2.1 Toxicity

There will be no carcinogenic materials used anywhere in the display module. If toxic materials are used, they will be reviewed and approved by the responsible AUO toxicologist.

#### 10.2.2 Flammability

All components including electrical components that do not meet the flammability grade UL94-V1 in the module will complete the flammability rating exception approval process.

The printed circuit board will be made from material rated 94-V1 or better. The actual UL flammability rating will be printed on the printed circuit board.

### 10.3 Capacitors

If any polarized capacitors are used in the display assembly, provisions will be made to keep them from being inserted backwards.

### 10.4 National Test Lab Requirement

The display module will satisfy all requirements for compliance to:

UL 60950-1 second edition

U.S.A. Information Technology Equipment