

# SPECIFICATION FOR APPROVAL

<b>(•</b> )	Preliminary	specification
( )	Final specifi	ication

Title	27.0" FHD TFT LCD				

BUYER	General
MODEL	

SUPPLIER	LG Display Co., Ltd.
*MODEL	LB270WF1
SUFFIX	

<sup>\*</sup>When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE DATE
Please return 1 copy for your	confirmation with
your signature and o	omments.

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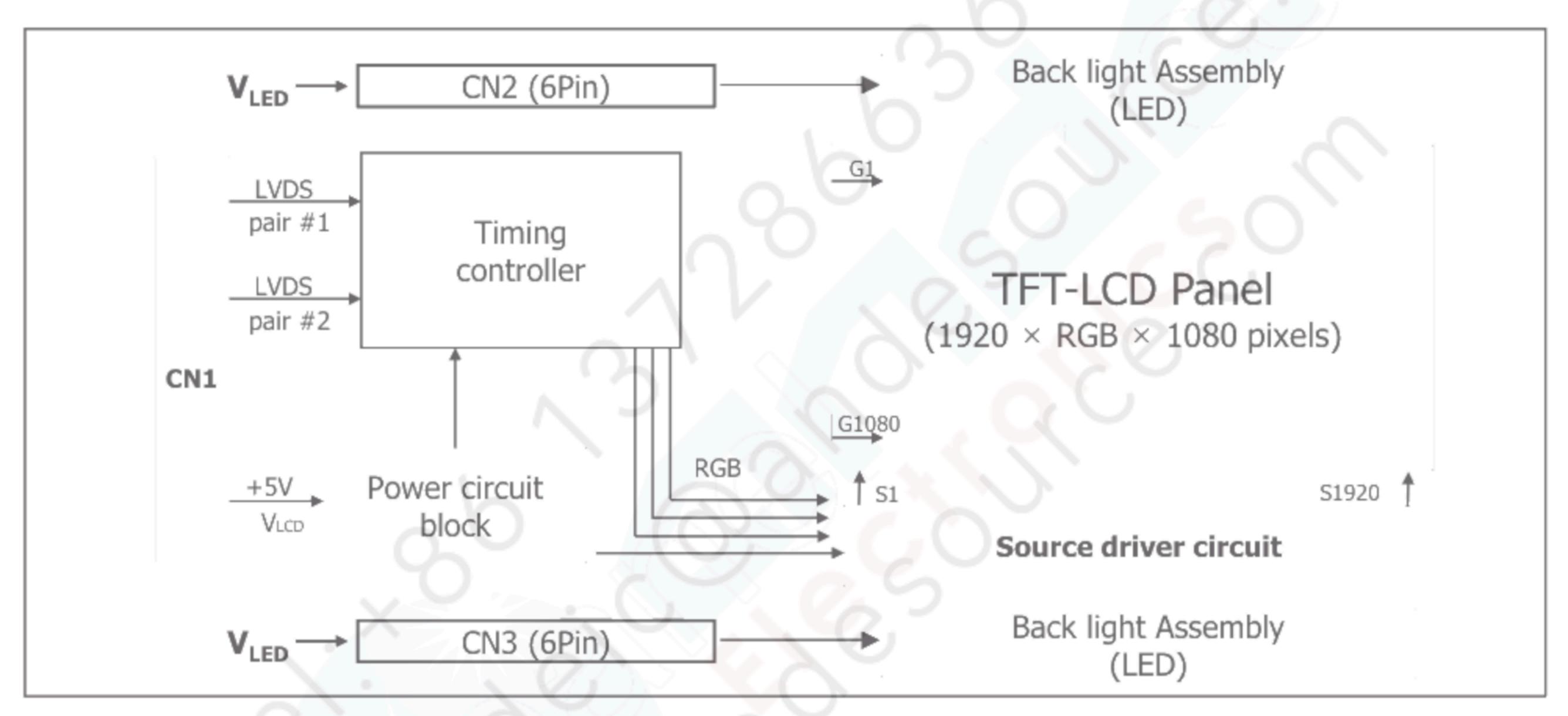
# Record of revisions

Revision Revision Page Before		After	Application Date		
0.0	Oct. 12. 2017	-	First Draft, Preliminary Specifications	_	-
0.1	Dec. 21. 2017	_	Update		
0.2	Jan. 2. 2018	14	Update timing table  - Vsync max freq. : 63Hz  - Hsync max freq. : 69.3KHz  - DCLK max freq. : 75.4MHz  - DCLK min period : 12.44ns  - DCLK max period : 17.76ns	<ul> <li>Vsync max freq.: 75Hz</li> <li>Hsync max freq.: 82.5KHz</li> <li>DCLK max freq.: 89.7MHz</li> <li>DCLK min period: 11.14ns</li> <li>DCLK max period: 17.40ns</li> </ul>	
		24	Update front view		
			Update rear view		
		25			
			A fine of the state of the stat		
				Add Pressure Spec.	
0.3	Jan. 8. 2018	26	7. No item.		700~1060hpa 500~1060hpa
0.4	Jan. 19, 2018	24,25	Add Side Mount		



#### 1. General description

LB270WF1-SLA1 is a color active matrix liquid crystal display with a light emitting diode (WLED) backlight assembly without LED driver. The matrix employs a-Si thin film transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 27 inch diagonally measured active display area with FHD resolution.(1920 horizontal by 1080 vertical pixels array) Each pixel is divided into red, green and blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 10-bit gray scale signal for each dot, thus, presenting a palette of more than 1.07Billion colors. It has been designed to apply the 10-Bit 2port LVDS interface. It is intended to support displays where high brightness, super wide viewing angle, high color saturation, and high color are important.



[FIG. 1] Block diagram

#### **General features**

Active screen size	27 inches(68.6cm) (Aspect ratio 16:9)
Outline dimension	622.4(H) x 364.2(V) x 17.35 mm (Typ.)
Pixel pitch	0.3114(H)mm x 0.3114(V)mm
Pixel format	1920(H) x 1080(V) Pixels. RGB stripes arrangement
Color depth	1.07B colors, 10Bit
Luminance (@White)	1000 cd/m² ( Center 1 Point, Typ.)
Viewing angle(CR>10)	View angle free (R/L 178(Typ.), U/D 178(Typ.))
Power consumption	Total TBD Watt (TBD Watt @V <sub>LCD</sub> , (47.20) Watt @Is= (130)mA)
Weight	TBDg (Typ.)
Display operating mode	Transmissive mode, normally black
Panel type	Reverse type
Surface treatment	Anti-glare treatment of the front polarizer (Haze 25%, 3H)



## 2. Absolute maximum ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

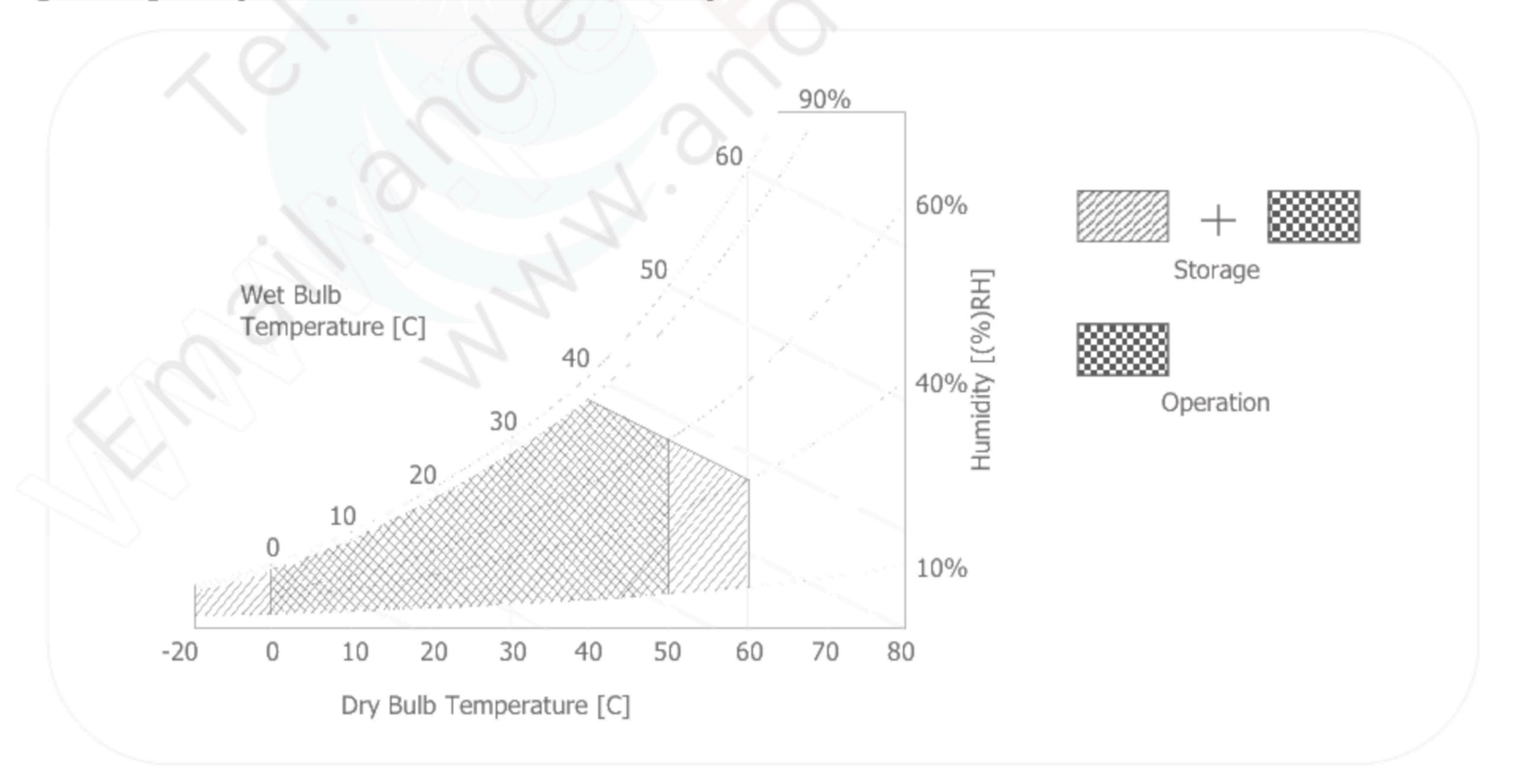
Table 1. Absolute maximum ratings

Darameter	Symbol	Va	lues	Units	Nietos
Parameter		Min.	Max.	Units	Notes
Power supply input voltage	V <sub>LCD</sub>	-0.3	6.0	V <sub>DC</sub>	At 25℃
Operating temperature	T <sub>OP</sub>	0	50	°C	
Storage temperature	T <sub>ST</sub>	-20	60	°C	1 2 2
Operating ambient humidity	H <sub>OP</sub>	10	90	%RH	1,2,3
Storage humidity	H <sub>ST</sub>	10	90	%RH	
LCM surface temperature (Operation)	T <sub>Surface</sub>	0	65	°C	1, 4

#### Notes:

- 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C Max., and no condensation of water.
- 2. Maximum storage humidity is up to 40°C, 70% RH only for 4 corner light leakage mura.
- 3. Storage condition is guaranteed under packing condition
- 4. LCM surface temperature should be measured under the condition of  $V_{LCD}$ =5.0V, fv=60Hz,  $T_a$ =25 °C, no humidity and typical LED string current.
  - \*.  $T_a$ = Ambient temperature

[FIG. 2] Temperature and relative humidity



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## 3. Electrical specifications

#### 3-1. Electrical characteristics

It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The other input power for the LED/Backlight, is typically generated by a LED Driver. The LED Driver is an external unit to the LCDs.

Table 2-1. Electrical characteristics

Dawanatas	Cumbal	Values			Haite	BI-A
Parameter	Symbol	Min	Тур	Max	Units	Notes
MODULE :						
Power supply input voltage	V <sub>LCD</sub>	4.5	5	5.5	V	4
Permissive power input ripple	V <sub>ripple</sub>		-	400	mVp-p	1
Dower cumply input current	I <sub>LCD</sub> Typ.		TBD	TBD	А	
Power supply input current	I <sub>LCD</sub> Max.	-	TBD	TBD	Α	2
Dower concumption	Рс Тур.	-	TBD	TBD	Watt	
Power consumption	Pc Max.		TBD	TBD	Watt	
Rush current	IRUSH			3.5	А	3

#### Notes:

- Permissive power ripple should be measured under the condition of V<sub>LCD</sub>=5.0V, 25°C,\*fv=max.
   Refer to page 7 for the pattern and more information.
- 2. The specified current and power consumption can be measured under the  $V_{LCD}$ =5.0V, 25°C,  $f_V$ =60Hz and the pattern should be changed according to the typical or maximum power condition. The max. current can be measured only with the maximum power pattern. See the page 7 for details.
- 3. Maximum condition of inrush current: The duration of rush current is about 5ms and rising time of power input is 500us  $\pm$  20%. (min.).
- 4.  $V_{LCD}$  level must be measured between two points on PCB of LCM [ $V_{LCD}$  (test point) ~ LCM Ground) (Test condition: maximum power pattern, 25°C,  $f_V$ =60Hz)
- \* fv=frame frequency

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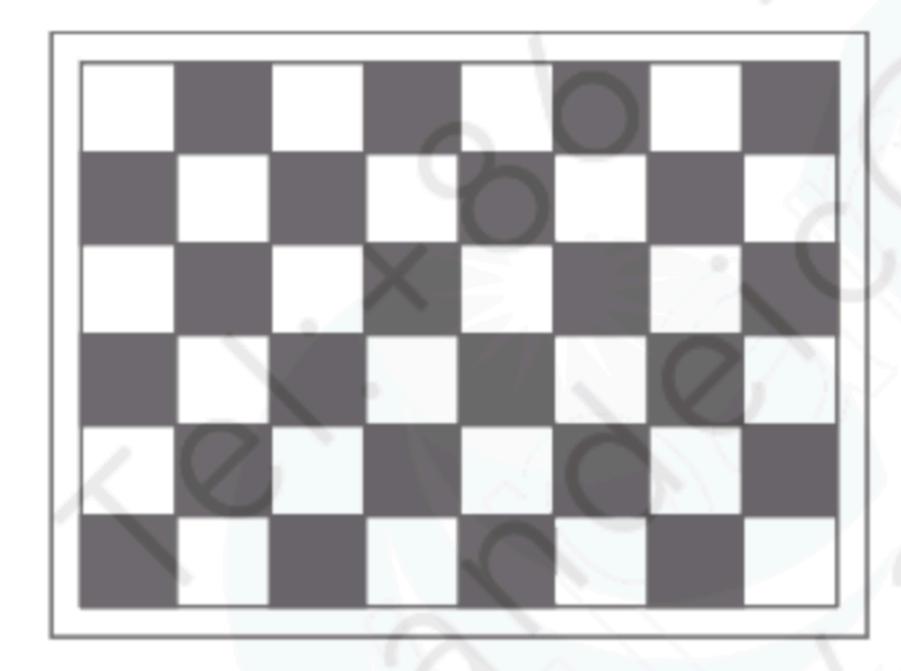
• Permissive power input ripple (V<sub>LCD</sub> =5.0V, 25°C, fv (frame frequency)=Max. condition)



White pattern

For the exact ripple measurement, the condition of max. 20Mhz is recommended in the bandwidth configuration of oscilloscope.

Power consumption (V<sub>LCD</sub> =5V, 25°C, fv (frame frequency=60Hz condition)



Typical power pattern



Maximum power pattern

[FIG. 3] Mosaic pattern & White pattern for power consumption measurement



Table 2-2. Electrical characteristics of LED bar in normal operating condition

Dawanahan	Cumulant	Values			I I m i i m	Mahaa
Parameter	Symbol	Min.	Тур.	Max.	Units	Notes
LED string current	Is	_	(130)	(135)	mA	1, 2
LED string voltage	Vs	(45.3)	(48.5)	(51.7)	V	1, 3
Power consumption	PBar	_	(50.4)	(53.7)	Watt	1, 2, 5
LED life time	LED_LT	30,000			Hour	4

Notes: The LED bar consists of 144 LED packages, 4 strings (parallel) x 18 packages (serial) x 2 bar

- 1. The specified values are for single LED bar.
- 2. The specified current is defined as the input current for single LED string with 100% duty cycle.
- 3. The specified voltage is the input LED string voltage at typical current 100% duty cycle.
- 4. The LED life time is defined as the time when the LED PKG brightness reach to the 50% of initial value under the conditions at  $Ta = 25 \pm 2$ °C and typical LED string current.
- 5. The power consumption shown above does not include the loss of external LED driver. The typical power consumption is calculated as  $P_{Bar} = V_s(Typ.) \times I_s(Typ.) \times No.$  of strings. The maximum power consumption is calculated as  $P_{Bar} = V_s(Max.) \times I_s(Typ.) \times No.$  of strings.

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#### 3-2. Interface connections

#### 3-2-1. LCD Module

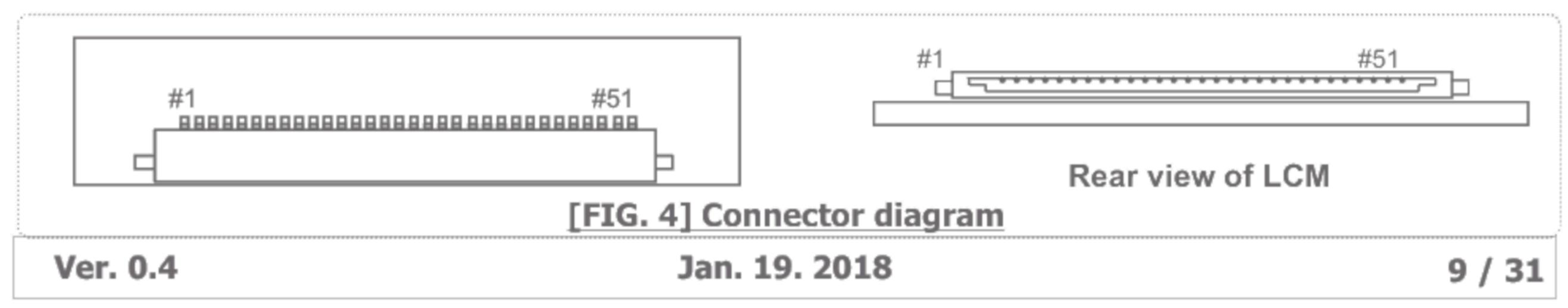
- LCD Connector(CN1): IS050-C51B-C39-A (manufactured by UJU) or FI-RE51S-F (manufactured by JAE)
- or GT05P-51S-H38-E1500 (manufactured by LSM) or equivalent.
- Mating Connector: FI-RE51HL(JAE) or compatible

Table 3-1. Module connector (CN1) pin configuration

No	Symbol	Description	No	Symbol	Description
1	GND	Ground	27	GND	Ground
2	NC	No Connection	28	REAN	SECOND LVDS Receiver signal (A-)
3	NC	No Connection	29	REAP	SECOND LVDS Receiver signal (A+)
4	NC	No Connection (I2C serial interface for LCM)	30	REBN	SECOND LVDS Receiver signal (B-)
5	NC	No Connection (I2C serial interface for LCM)	31	REBP	SECOND LVDS Receiver signal (B+)
6	GND	Ground	32	RECN	SECOND LVDS Receiver signal (C-)
7	GND	Ground	33	RECP	SECOND LVDS Receiver signal (C+)
8	NC	No Connection	34	GND	Ground
9	PWM_OUT	Reference signal for LED driver control	35	RECLKN	SECOND LVDS Receiver Clock signal(-)
10	GND	Ground	36	RECLKP	SECOND LVDS Receiver Clock signal(+)
11	GND	Ground	37	GND	Ground
12	ROAN	FIRST LVDS Receiver signal (A-)	38	REDN	SECOND LVDS Receiver signal (D-)
13	ROAP	FIRST LVDS Receiver signal (A+)	39	REDP	SECOND LVDS Receiver signal (D+)
14	ROBN	FIRST LVDS Receiver signal (B-)	40	REEN	SECOND LVDS Receiver signal (E-)
15	ROBP	FIRST LVDS Receiver signal (B+)	41	REEP	SECOND LVDS Receiver signal (E+)
16	ROCN	FIRST LVDS Receiver signal (C-)	42	GND	Ground
17	ROCP	FIRST LVDS Receiver signal (C+)	43	GND	Ground
18	GND	Ground	44	GND	Ground
19	ROCLKN	FIRST LVDS Receiver Clock signal(-)	45	GND	Ground
20	ROCLKP	FIRST LVDS Receiver Clock signal(+)	46	NC	No Connection
21	GND	Ground	47	NC	No Connection
22	RODN	FIRST LVDS Receiver signal (D-)	48	VLCD	Power Supply +5.0V
23	RODP	FIRST LVDS Receiver signal (D+)	49	VLCD	Power Supply +5.0V
24	ROEN	FIRST LVDS Receiver signal (E-)	50	VLCD	Power Supply +5.0V
25	ROEP	FIRST LVDS Receiver signal (E+)	51	VLCD	Power Supply +5.0V
26	GND	Ground	-	-	-

#### Notes:

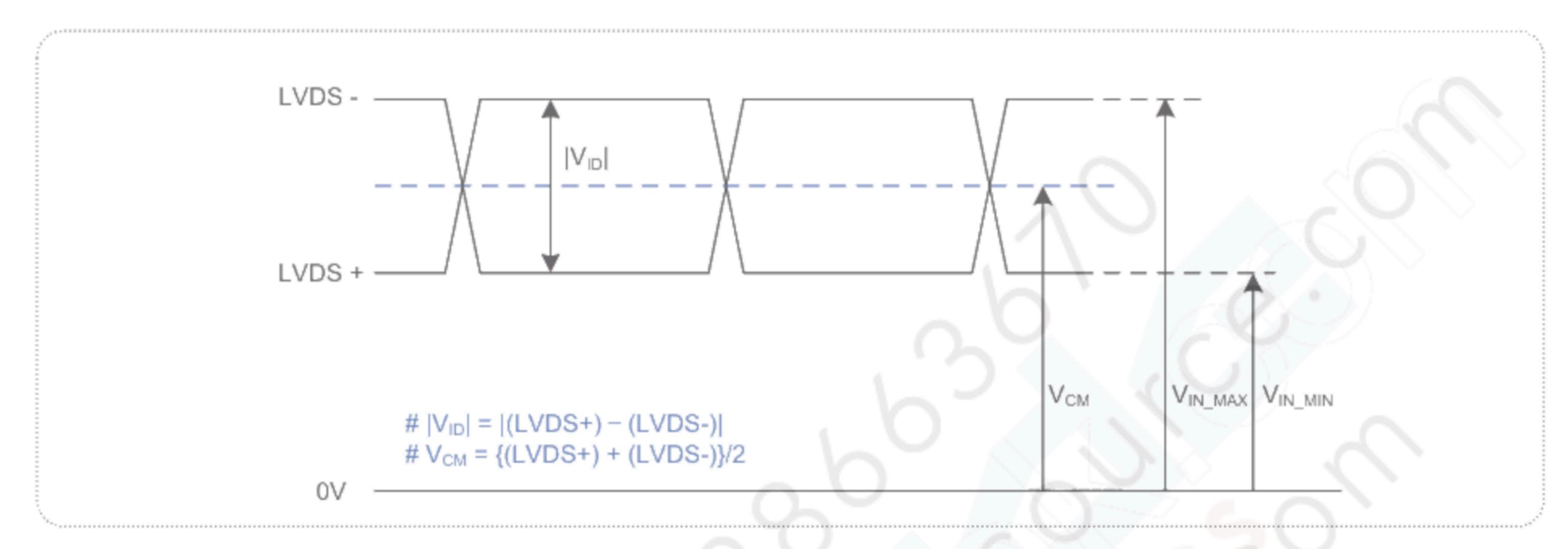
- 1. All GND(ground) pins should be connected together to the LCD module's metal frame.
- 2. All VLCD (input power) pins should be connected together.
- 3. All input level of LVDS signals are based on the EIA 644 standard.
- 4. PWM\_OUT is a reference signal for LED PWM control. This PWM signal is synchronized with vertical frequency. If the system don't use this pin, do not connect.





## 3-2-2. LVDS Input characteristics

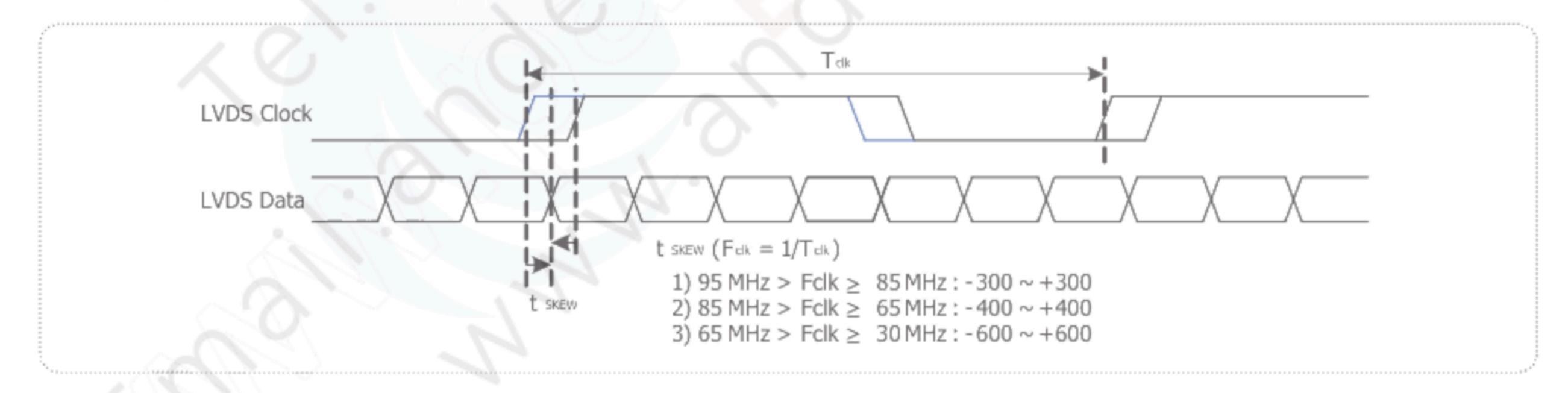
#### DC Specification



Description	Symbol	Min	Max	Unit	Notes
LVDS Differential voltage	V <sub>ID</sub>	150	600	mV	-
LVDS Common mode voltage	V <sub>CM</sub>	1.0	1.5	V	_
LVDS Input voltage range	V <sub>IN</sub>	0.7	1.8	V	-
Change in common mode voltage	ΔVcM		250	mV	-

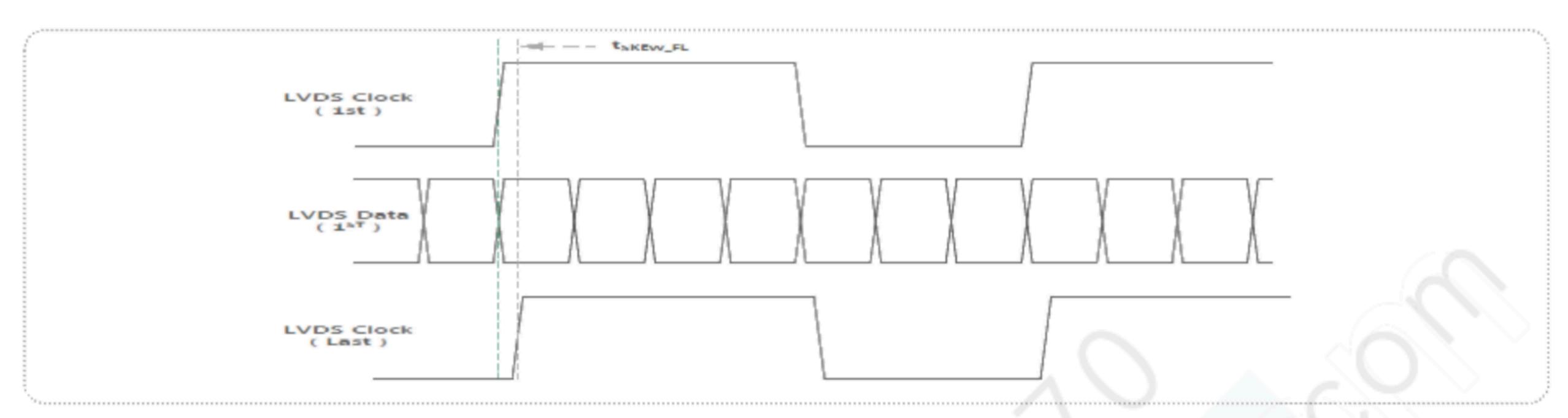
Notes: Dose not have any Noise & Peaking in LVDS Signal

## 2. AC Specification



Description	Symbol	Min	Max	Unit	Notes
	t <sub>SKEW</sub>	- 300	+ 300	ps	95MHz > Fclk ≥ 85MHz
LVDS Clock to data skew margin	t <sub>SKEW</sub>	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz
	t <sub>SKEW</sub>	- 600	+ 600	ps	65MHz > Fclk ≥ 30MHz
LVDS Clock to clock skew margin (Even to odd)	t <sub>SKEW_EO</sub>	- 1/7	+ 1/7	T <sub>clk</sub>	_

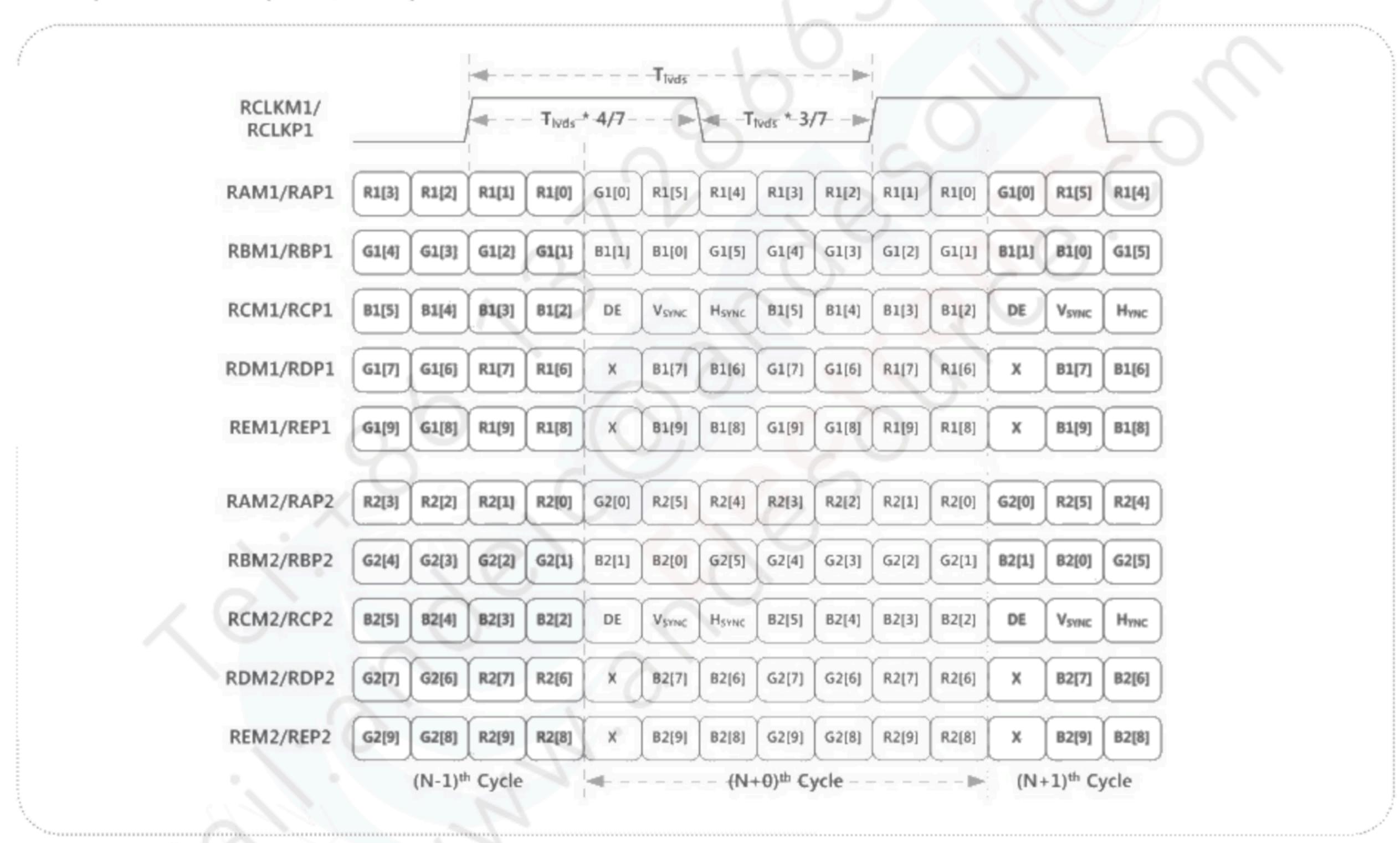




< Clock skew margin between channel >

#### Data Format

1) LVDS 2 Port (10Bit, VESA)



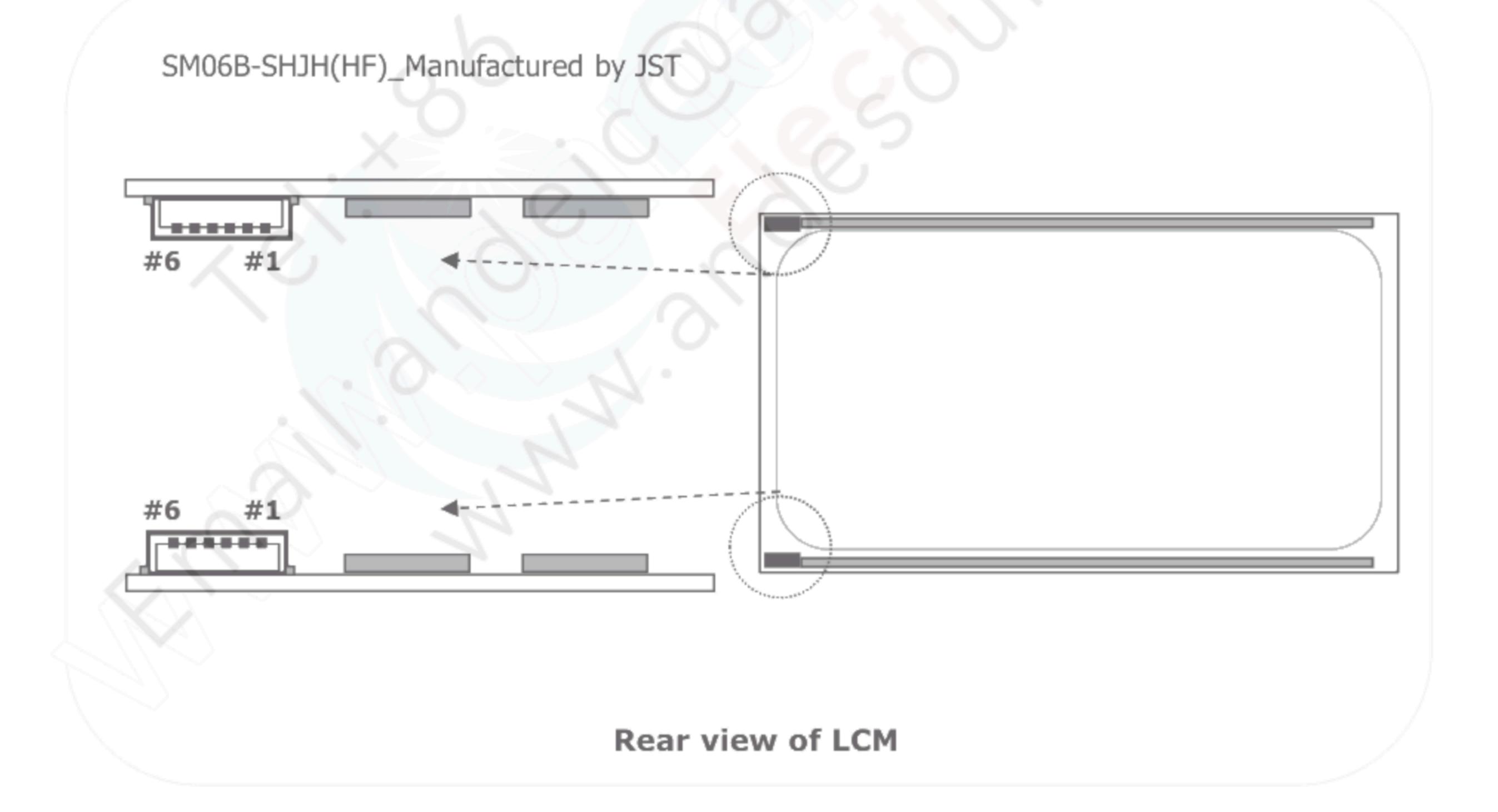


#### 3-2-3. Backlight connector pin configuration

#### Table 5. Backlight connector pin configuration(CN2, CN3)

The LED interface connector is SM06B-SHJH(HF)\_wire-locking type manufactured by JST or equivalent. The mating connector is a SHJP-06V-S(HF) or 10035HS-H06C(HF) or equivalent. The pin configuration for the connector is shown in the table below.

Pin	Symbol	Description Notes
1	FB1	Channel1 current feedback
2	FB2	Channel2 current feedback
3	VLED	LED power supply (Common anode)
4	VLED	LED power supply (Common anode)
5	FB3	Channel3 current feedback
6	FB4	Channel4 current feedback



[FIG. 5] Backlight connector view



### 3-3. Signal timing specifications

This is signal timing requirement from the signal transmitter. All of the interface signal timing should satisfy the following specifications for its proper operation.

Table 6. Timing table

ITEM	Symbol		Min.	Тур.	Max.	Unit	Note		
Period		tCLK	12.44	13.9	17.76	ns	Pixel frequency		
DCLK	Frequency	_	57.4	71.8	75.4	MHz	(Typ. 144Mhz)		
Period		tHP	1076	1088	1100	tCLK			
LI CO (TO C	Horizontal Valid	tHV	960	960	960	tCLK	1 3 4		
Hsync	Horizontal Blank	tHB	116	128	140	tCLK	1,3,4		
	Frequency	fH	52.8	66	69.3	KHz			
	Period	tVP	1090	1100	1160	tHP			
Verme	Vertical Valid		1080	1080	1080	tHP	2.4		
Vsync Vertical Blank		tVB 10		20	80	tHP	2,4		
	Frequency	fV	48	60	63	Hz			

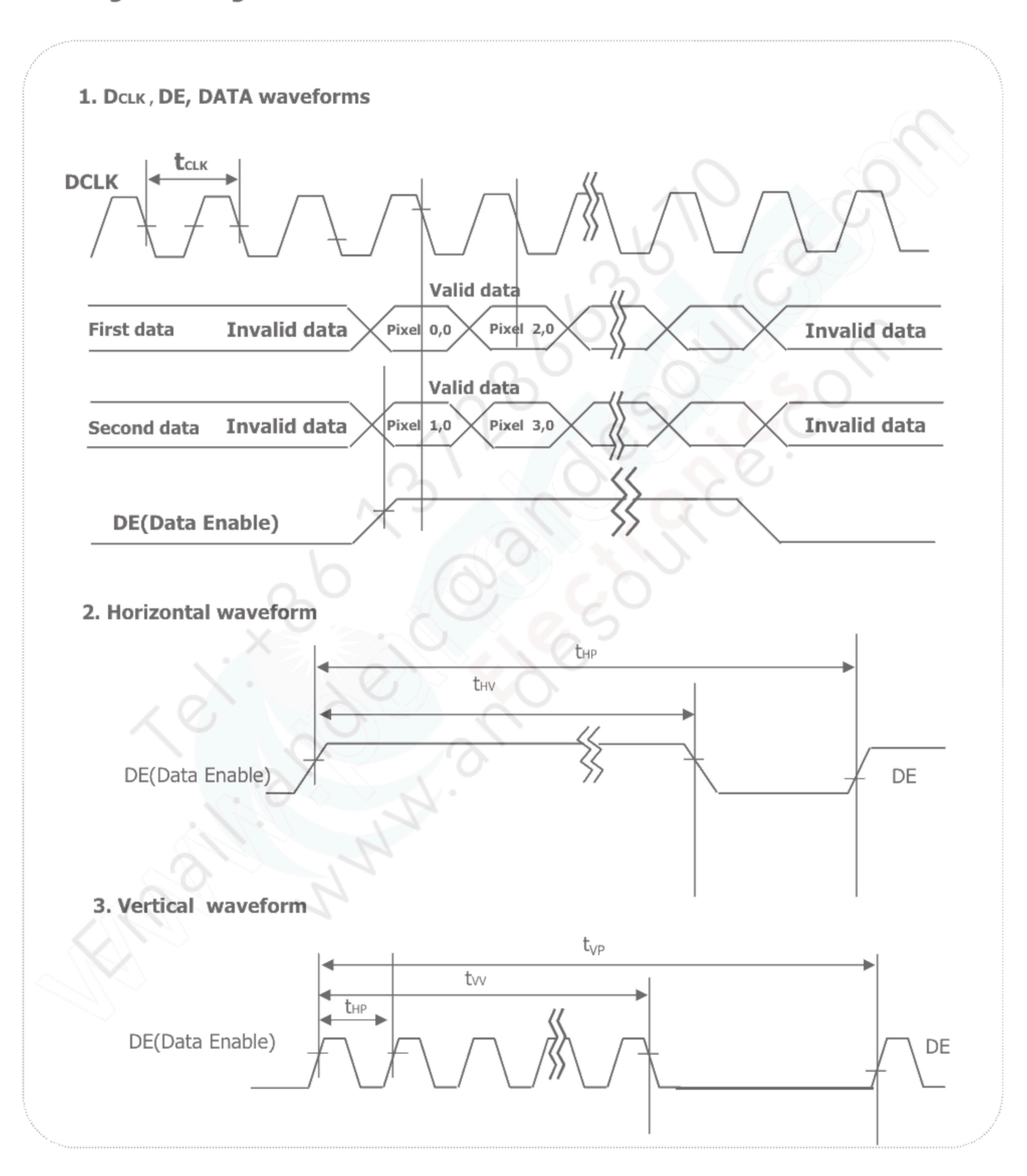
#### Notes:

- 1.The value of Hsync period, Hsync Blank and Hsync valid should be even number times of tCLK.

  If the value is odd number times of tCLK, it can make asynchronous signal timing and cause abnormal display.
- 2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rates.
- 3. The value of Hsync Period, Hsync Blank should be divided by 4 without a remainder.
- 4. The polarity of Hsync, Vsync is not restricted.
- 5. The input of HSYNC & VSYNC signal does not have an effect on normal operation (DE Only Mode).



# 3-4. Signal timing waveforms





# 3-5. Color input data reference

The brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. Color data reference

			Input Color Data																												
	Color					RE	D								G	RI	Ę	N	1							BL	UE				
	COIOI	MSE	3					LS	В			MS	В				X	$\bigcirc$		LS	В	MS	В	1	۰			l	LSE	3	
		R!	9 R8	R7	R6	R5 I	R4 F	₹3 R	2 R	1 R	0	G9	) G8	G7	' G6	G5	G4	G3	G2	G1 G0	1	В	9 B8	B B7	В6	B5	В4	В3 Е	32 B	1 B	0
	Black	0	0	0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0		0	0	0	0	0	0	0	0	0	0
	Red (1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0 0		0	0	0	0	0	0	0	0	0	0
	Green (1023)	0	0	0	0	0	0	0	0 (	0	0	1	1	1	1	1	1	1	1	1 1		0	0	0	0	0	0	0	0	0	0
Basic	Blue (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0		1	1	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1 1	0	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0 0	,	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1 1	``	0	0	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1 1		1	1	1	1	1	1	1	1	1	1
	RED (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 0		0	0	0	0	0	0	0	0	0	0
	RED (001)	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0 0	• • •	0	0	0	0	0	0	0	0	0	0
RED						Ġ		1	3		• • • •		Α,								• • •										
	RED (1022)	<b>1</b>	1	1	1	1	1	1	 1	 1	0	0	0	0	0	0	0	0	0	0 0	• • •	0	0	0	0	0	0	0	0	0	0
	RED (1023)	<b>1</b>	1	1	1	1	 1	 1	 1	1	1	0	0	0	0	0	0	0	0	0 0	• • •	0	0	0	0	0	0	0	0	0	0
	GREEN (000)	0	0	0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	)	0	0	0	0	0	0	0	0	0	0
	GREEN (001)									٠.											٠.,							• • •	• • •		
GREEN						7	7.														٠										٠
J CINCLIN	GREEN (1022)		Ω	0	0			 O	 N _ 1	 n	0		1	1	1			1		1.0			 n	 O	0				 O	 O	
												1									• • •										
	GREEN (1023)																				-										-
	BLUE (000)					0														0 0	٠.,										
	BLUE (001)			0	0	0	0	0	0 (	0	0	0			0		0	0	0	0 0			0	0	0					0	
BLUE																					٠										
	BLUE (1022)	0	0	0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0	٠		1	1	1	1	1		<b>1</b>	1	0
	BLUE (1023)	0	0	0	0	0	0	0	0 (	0	0	0	0	0	0	0	0	0	0	0 0		1	1	1	1	1	1	1	1	1	1



## 3-6. Power sequence

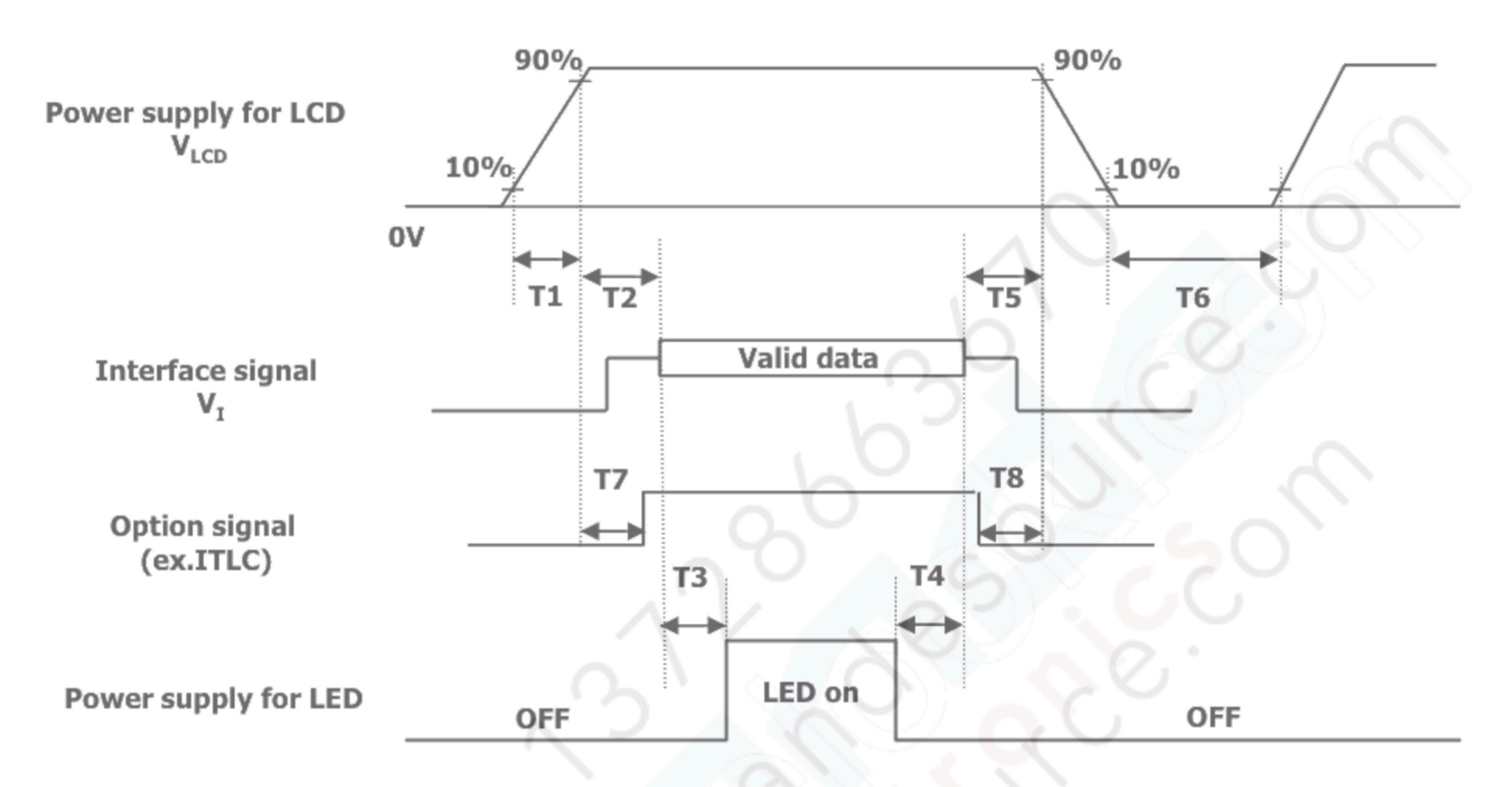


Table 8. Power sequence

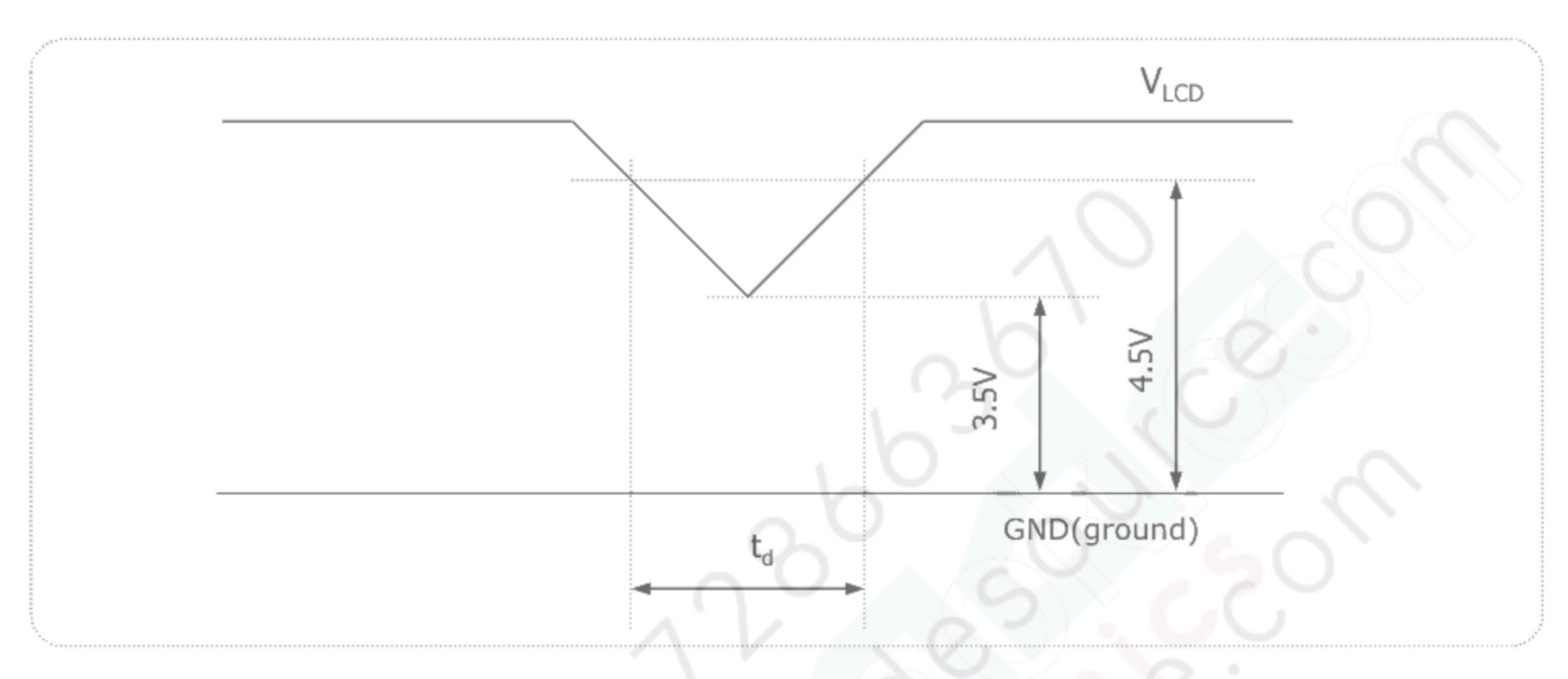
Danamatan		Values		I I an i de a
Parameter	Min.	Typ.	Max.	Units
T1	0.5		10	ms
T2	0.01		50	ms
Т3	500	-	-	ms
T4	200	-	-	ms
T5	0.01	-	50	ms
Т6	1000		-	ms
T7	0.5	-	T2	ms
T8	0		-	ms

#### Notes:

- 1. Power sequence should be kept all the time including below cases for normal operation.
  - -.AC/DC Power On/Off
  - -.Mode change (resolution, frequency, timing, sleep mode, color depth change, etc. )
    The violation of power sequence can cause a significant trouble in display and reliability.
- 2. Please avoid floating state of interface signal during signal invalid period.
- 3. When the interface signal is invalid, be sure to pull down the  $V_{LCD}$  (0V).
- 4. Please turn off the power supply for LED when the level of VLCD changes to prevent noise issue.
- 5. When measuring valid data starting point, it can be measured that LVDS signal starts swing.



# 3-7. V<sub>LCD</sub> Power dip condition



[FIG. 6] Power dip condition

For proper operation, stable power supply of  $V_{LCD}$  is necessary and power dip is allowed only in below condition. Except this condition, power on/off should follow power sequence specification in previous page exactly.

Dip condition

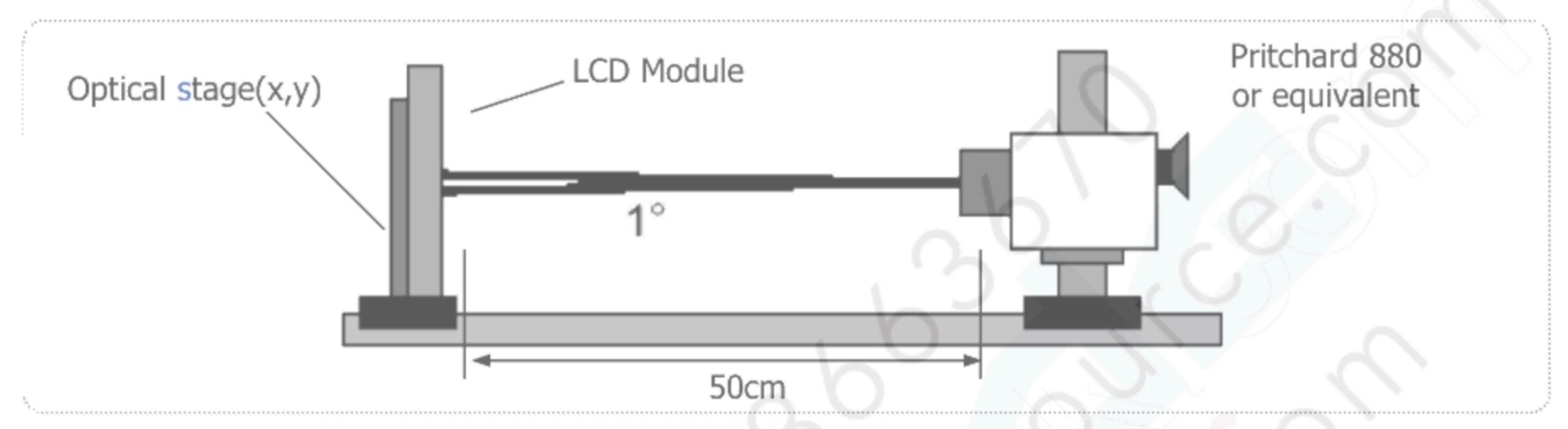
$$3.5V \le V_{LCD} < 4.5V$$
,  $t_d \le 20ms$ 



# 4. Optical specifications

Optical characteristics are determined after the unit has been 'ON' for approximately 30 minutes in a dark environment at 25 $\pm$ 2°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of  $\Phi$  and  $\theta$  equal to 0° and aperture 1 degree.

FIG. 7 presents additional information concerning the measurement equipment and method.



[FIG. 7] Optical characteristic measurement equipment and method

Table 9. Optical characteristics

(Ta=25 °C,  $V_{LCD}$ =5.0V,  $f_{V}$ =60Hz Dclk=144MHz,  $I_{S}$ = (130)mA)

Davamed		Symphol		Values		Limite	Notos
Paramet	ter	Symbol	Min.	Typ.	Max.	Units	Notes
Contrast Ratio		CR	700	1000	-		1
Surface luminance, white		L <sub>WH</sub>	800	1000	_	cd/m <sup>2</sup>	2
Luminance variation		$\delta$ WHITE	75	2	-	%	3
Response time	Gray To Gray	$T_{GTG\_AVR}$		14	25	ms	4
Color gamut (CIE1931	.)	sRGB	(96)	(99)	_	%	
	Red	Rx		TBD			
	IXCU	Ry		TBD			
	Green	Gx		TBD			
Color coordinates [CIE1931]		Gy	Тур.	TBD	Тур.		
(By PR650)	Divis	Bx	-0.03	TBD	+0.03		
	Blue	Ву		TBD			
	White	Wx		0.313			
	White	Wy		0.329			
Color temperature		_	_	6500	_	K	
Viewing angle	Horizontal	$\theta_{H}$	170	178	_	Dograc	
(CR>10, General)	Vertical	$\theta_{V}$	170	178	_	Degree	5
Gray Scale		-		2.2			6

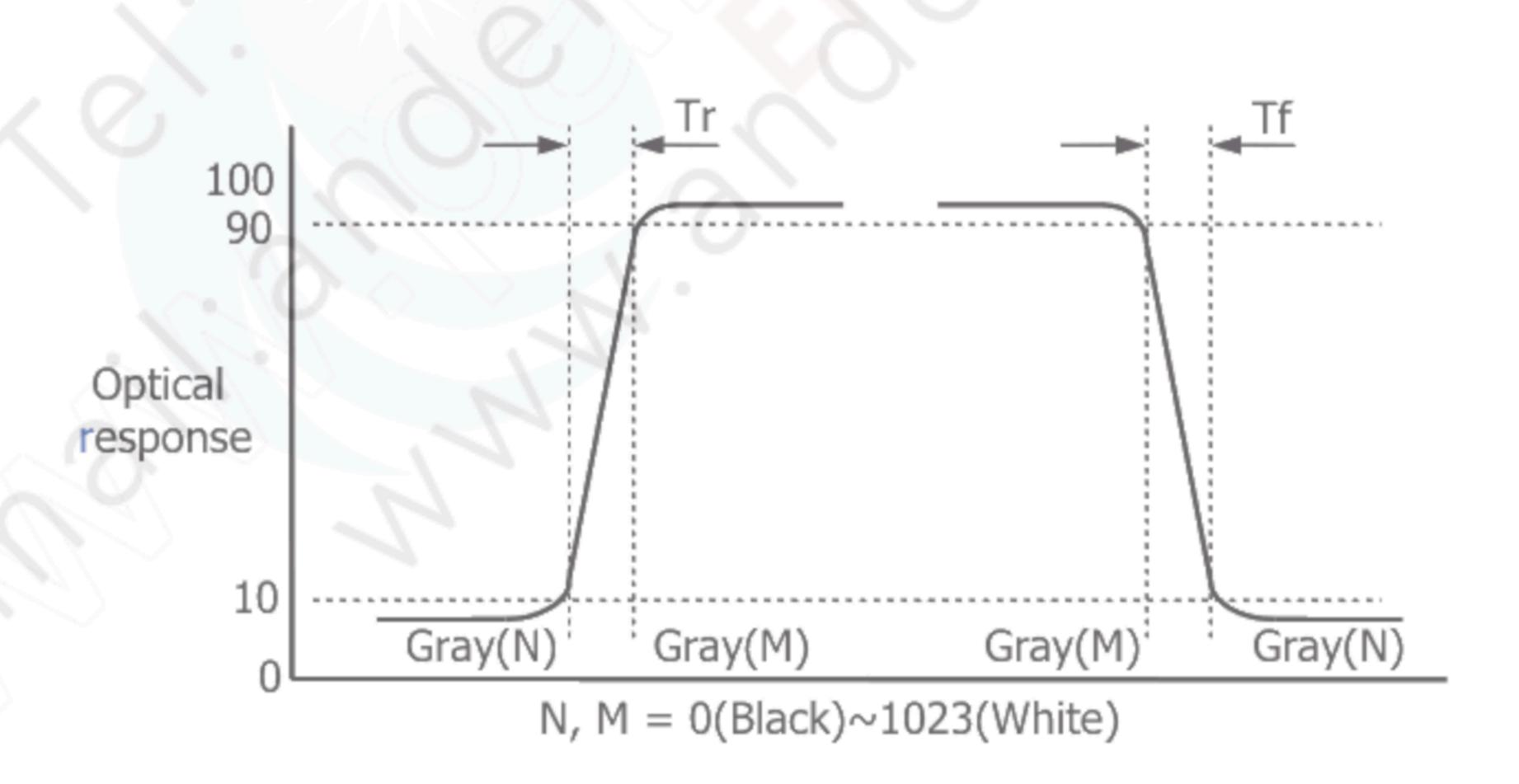


- 4. The Gray To Gray response time is defined as the following figure and shall be measured by switching the input signal for "Gray To Gray".
  - Gray step: 5 Step
  - TGTG\_AVR is the total average time at rising time and falling time for "Gray To Gray".
  - By RD80S

Table 10. GTG Gray table

Crov to C		Rising Time									
Gray to Gr	ay	G1023	G767	G511	G255	G0					
Falling Time	G1023										
	G767					50					
	G511										
	G255										
	G0										

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".



[FIG. 9] Response Time



Notes:

1. Contrast Ratio(CR) is defined mathematically as: (By PR880)

$$Contrast \, Ratio = \frac{Surface \, luminance \, with \, all \, white \, pixels}{Surface \, luminance \, with \, all \, black \, pixels}$$

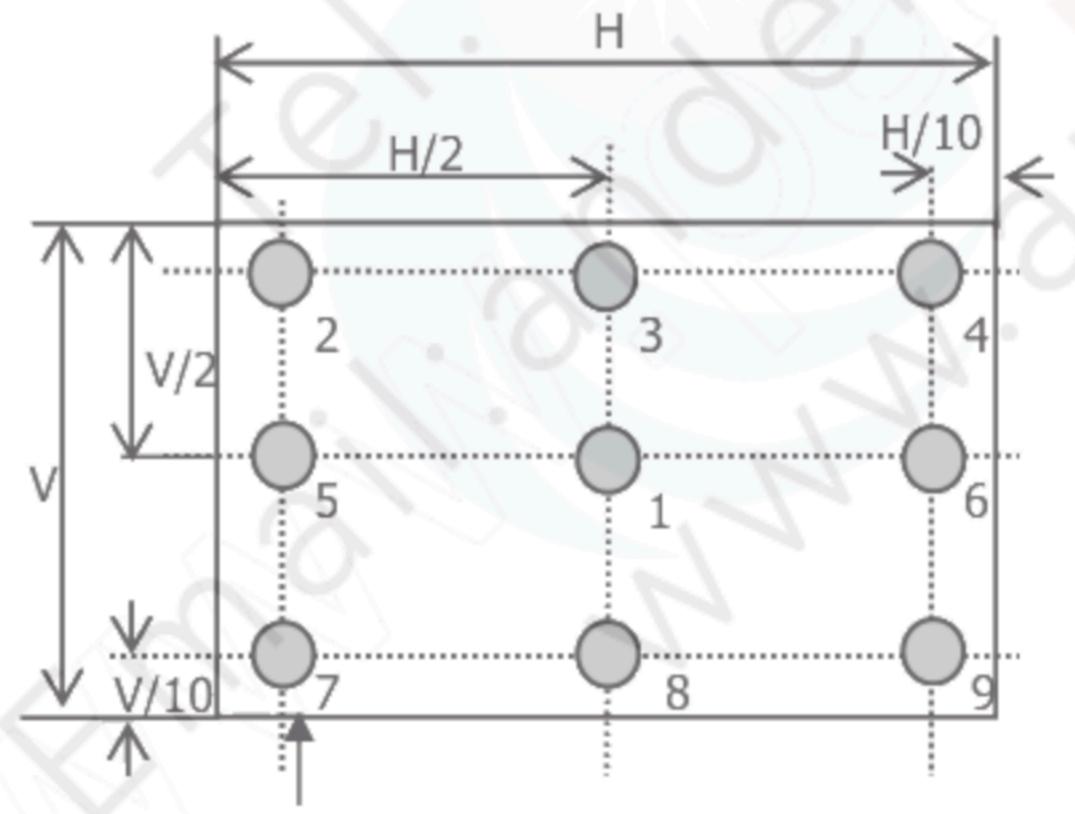
It is measured at center point(Location P1)

- 2. Surface luminance(Lwh)is luminance value at Center 1 point(P1) across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG.7 (By PR880)
- 3. The variation in surface luminance ,  $\delta$  WHITE is defined as : (By PR880)

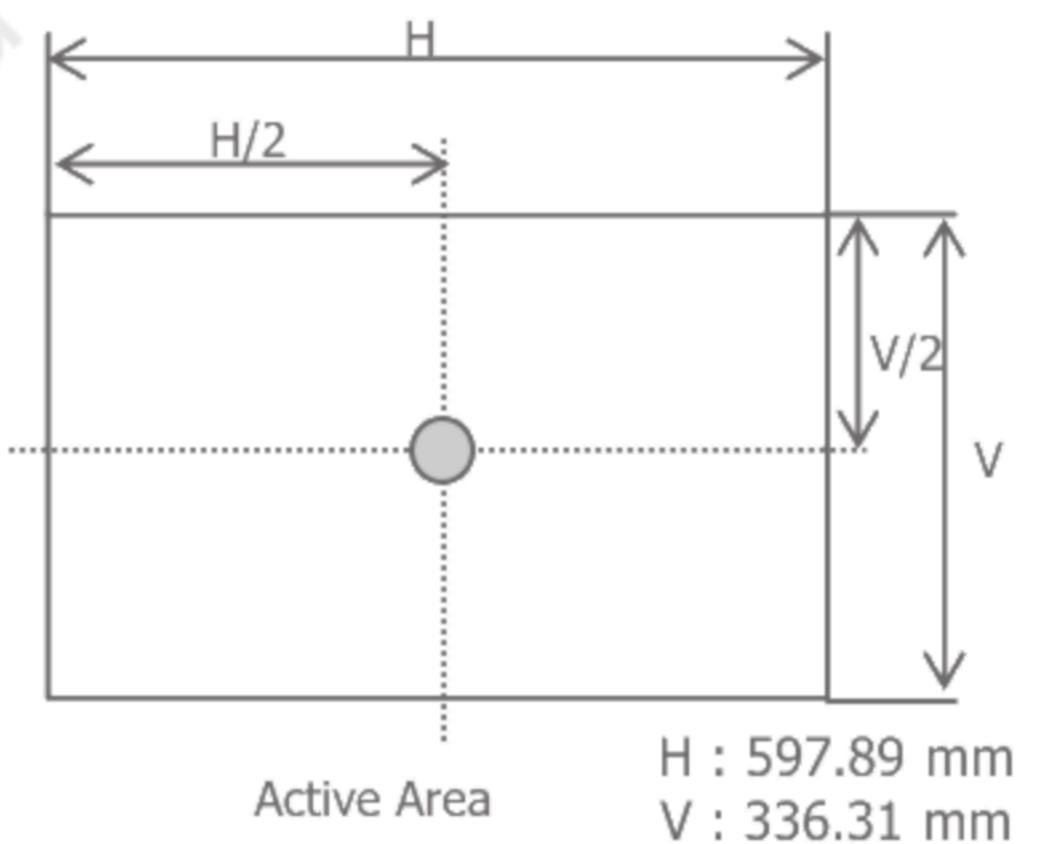
$$\delta_{WHITE} = \frac{M inimum(L_{P1}, L_{P2}, ...., L_{P9})}{M aximum (L_{P1}, L_{P2}, ...., L_{P9})} \times 100$$

Where L1 to L9 are the luminance with all pixels displaying white at 9 locations. For more information see FIG.8





#### <Measuring point for surface luminance>



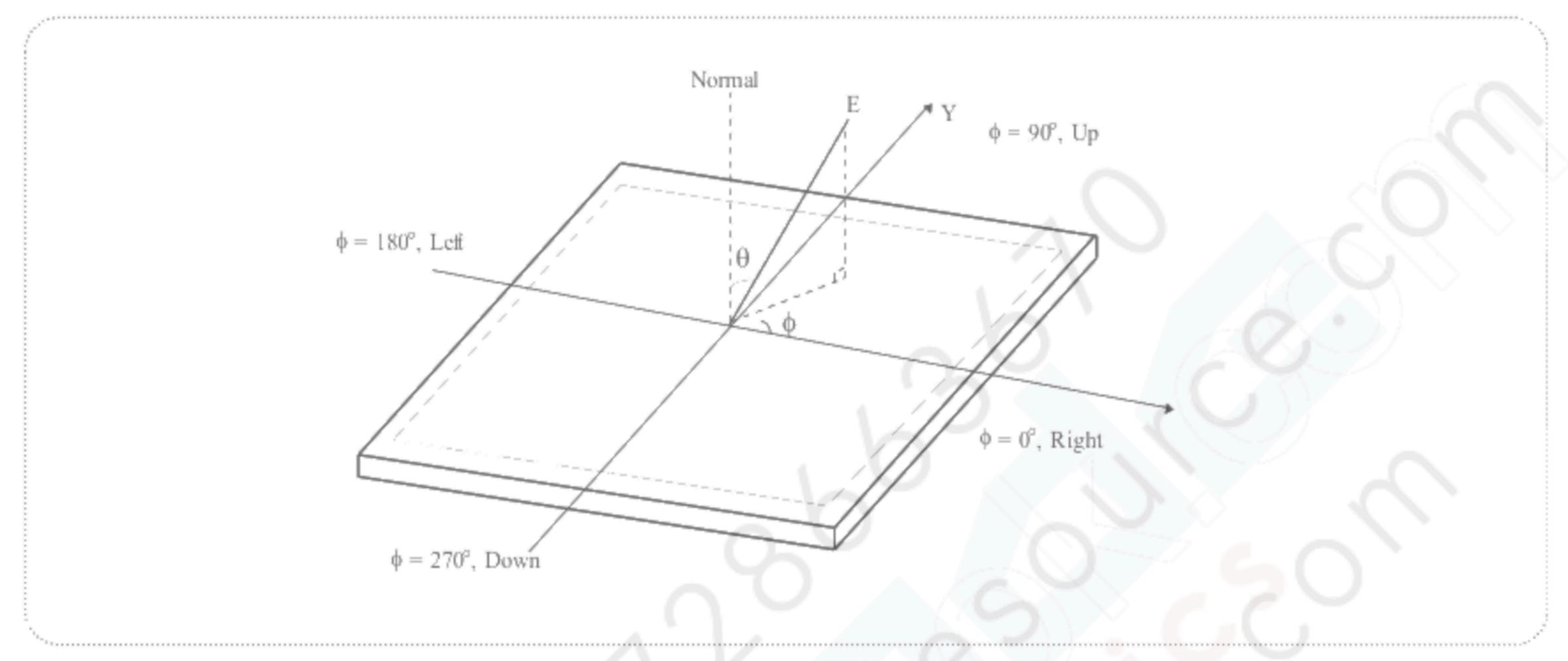
0 : 336.31 mm0 H,V : Active Area

[FIG.8] Measure point for luminance

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5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG.10 (By PR880)



[FIG. 10] Viewing angle

6. Gamma Value is approximately 2.2. For more information see Table 11.

Table 11. Gray Scale Specification

Gray Level	Relative Luminance [%] (Typ.)
0	0.10
63	0.30
127	1.08
191	2.50
255	4.72
319	7.70
383	11.49
447	16.20
511	21.66
575	28.20
639	35.45
703	43.80
767	53.00
831	63.30
895	74.48
959	86.80
1023	100



#### 5. Mechanical characteristics

The contents provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	622.4mm				
Outline dimension	Vertical	364.2mm				
	Depth	17.35 mm				
Bezel area	Horizontal	601.3mm				
Dezei area	Vertical	339.7mm				
Active display area	Horizontal	597.89mm				
Active display area	Vertical	336.31mm				
Weight	Typ. : TBDg, Max. : TBDg					
Surface treatment	Low Haze treatment of the front polarizer (3H)					

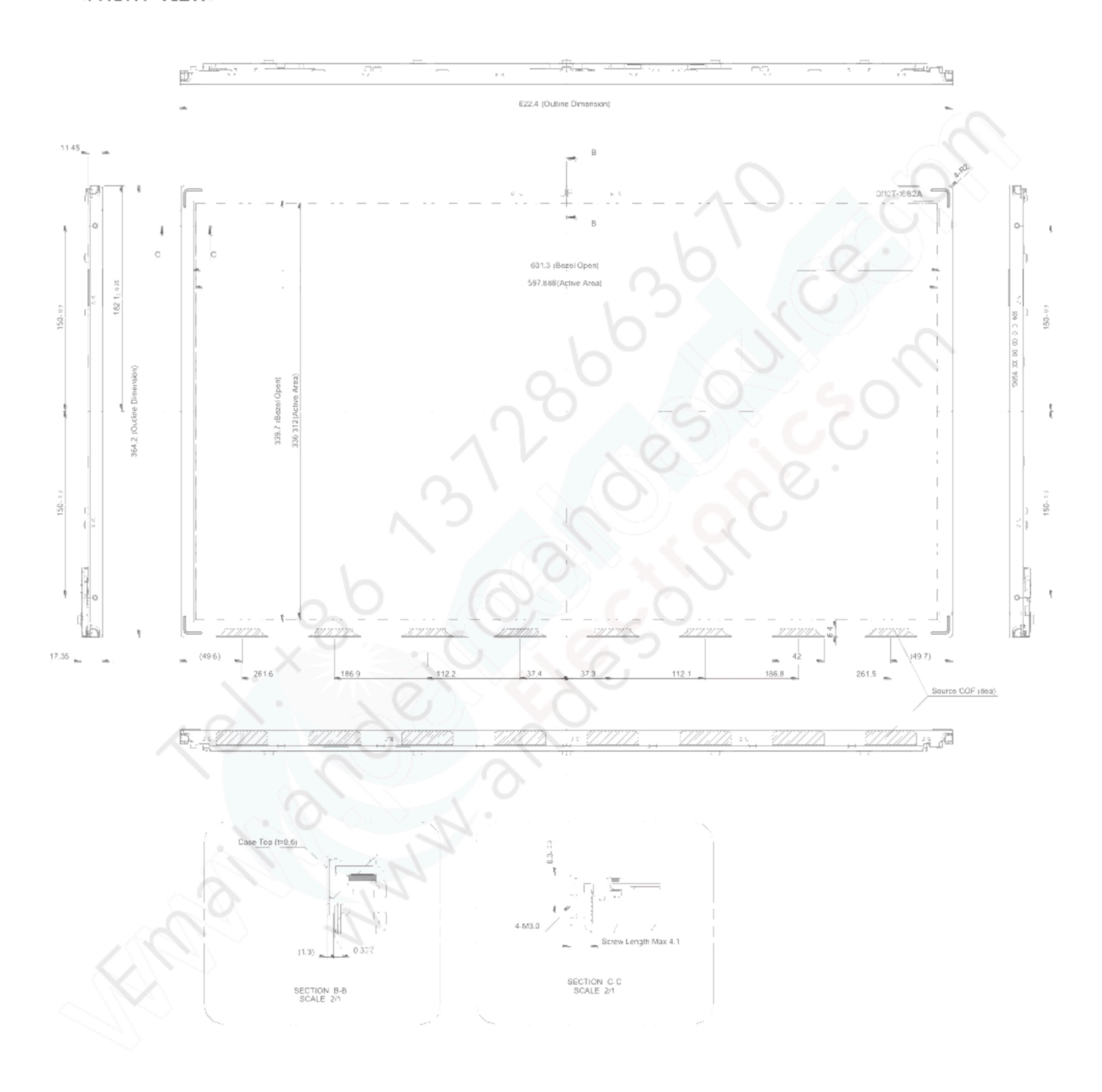
Notes: Please refer to a mechanic drawing in terms of tolerance at the next page.

Outline dimensions (horizontal, vertical and outside depth) are measured by using vernier calipers.

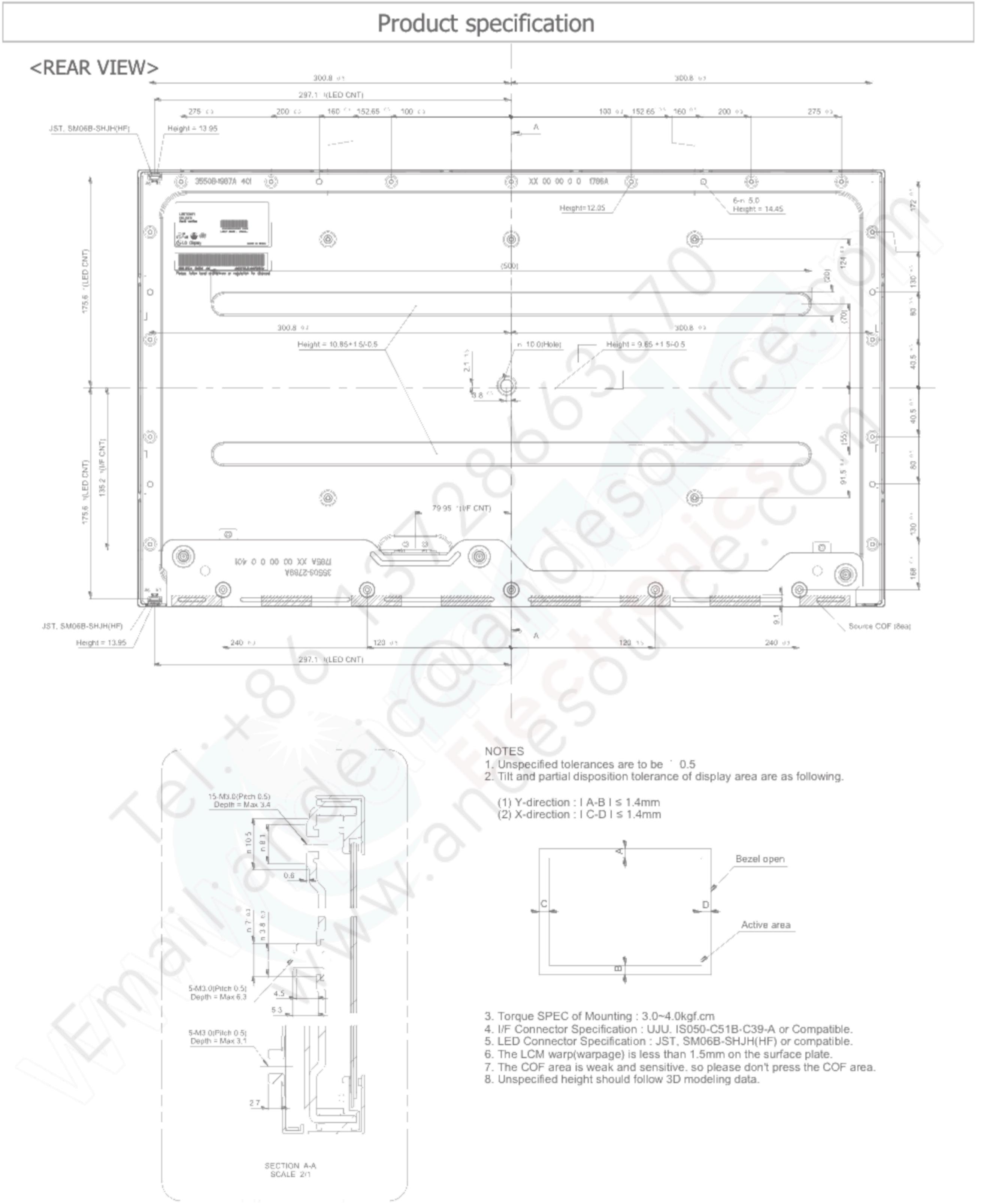
The inside depth dimensions are measured by using height gauge, when LCM is put face down onto a flat surface.



#### <FRONT VIEW>







LGD Highly recommendation:

System chassis or frame should be designed to keep the IPS Panel flat as it is vulnerable to panel light-leakage caused by deformation.



## 6. Reliability

Environment test condition

No	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Wave form: random Vibration level: 1.0G RMS Bandwidth: 10-300Hz Duration: X,Y,Z, 10 min One time each direction
6	Shock test (non-operating)	Shock level: 100Grms Waveform: half sine wave, 2ms Direction: ±X, ±Y, ±Z One time each direction
7	Pressure Operating Storage	700~1060hpa 500~1060hpa
8	Altitude Operating Storage / Shipment	0 - 16,400 feet (5,000m) 0 - 40,000 feet (12,192m)

#### Note 1. Result evaluation criteria:

TFT-LCD panels test should take place after cooling enough at room temperature.

In the standard condition, there should be no particular problems that may affect the display function.

※. T<sub>a</sub>= Ambient Temperature



#### 7. International standards

#### 7-1. Safety

- a) UL 60950-1, Underwriters Laboratories Inc.
  Information Technology Equipment Safety Part 1 : General Requirements.
- b) CAN/CSA-C22.2 No. 60950-1-07, Canadian Standards Association.
  Information Technology Equipment Safety Part 1: General Requirements.
- c) EN 60950-1, European Committee for Electrotechnical Standardization (CENELEC). Information Technology Equipment - Safety - Part 1: General Requirements.
- d) IEC 60950-1, The International Electrotechnical Commission (IEC).
  Information Technology Equipment Safety Part 1: General Requirements

#### 7-2. Environment

a) RoHS, Directive 2011/65/EU of the European Parliament and of the council of 8 June 2011





# 8. Packing

# 8-1. Designation of lot mark

a) Lot mark

Α	В	С	D	Е	F	G	Н	I	J	K	L	М
---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C: SIZE(INCH)

E: MONTH

D: YEAR

F ~ M: SERIAL NO.

Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	Α	В	С	D	Œ	$\mathcal{I}_{F}$	G	Н	J	K

#### 2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

#### b) Location of lot mark

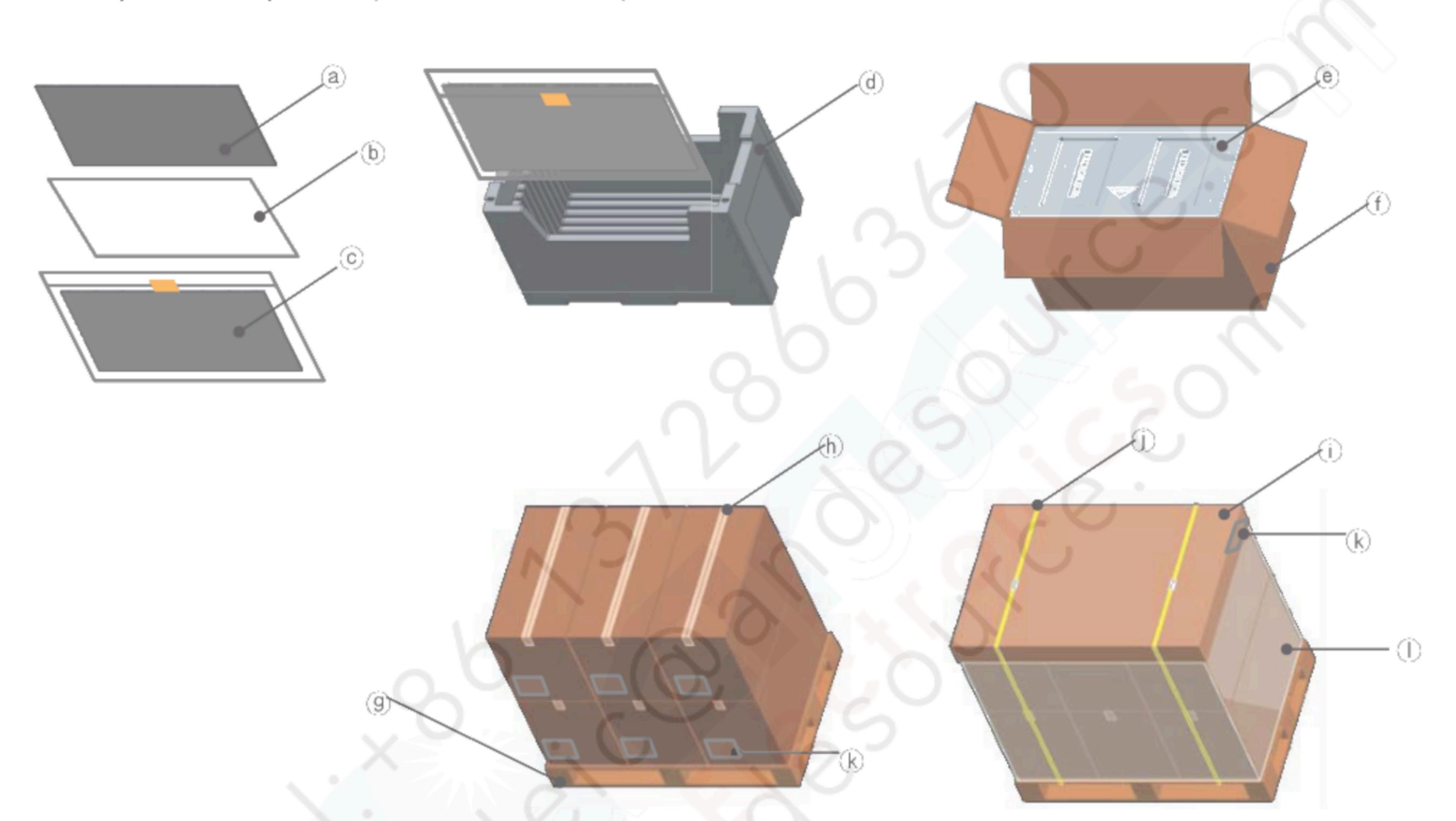
Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.



# 8-2. Packing form

a) Package quantity in one box: 8ea Package quantity in one Pallet: 48ea b) Box Size: 365mm X 710mm X 448mm

C) Pallet Ass'y Size: 1,140mmX740mmX1,019mm



No.	Description	Material					
(a)	LCM	-					
(b)	Protect film	PET					
©	AL-Bag	AL					
(d)	Packing,Bottom	EPS					
(e)	Packing,Top	EPS					
①	Box	Paper(SW)					
(9)	Pallet	Plywood					
(h)	Таре	OPP					
0	Angle Cover	Paper(SW)					
0	BAND	PP					
(k)	LABEL	YUPO PAPER					
0	Wrap	_					



#### 9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

## 9-1. Mounting precautions

- (1) You must mount a module using holes arranged in rear side.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
  Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

# 9-2. Operating precautions

- (1) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (2) Brightness depends on the temperature. (In higher temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (3) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (4) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (5) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (6) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (7) A screw which is fastened up the steels should be a machine screw. (If not, it causes metallic foreign material and deal LCM a fatal blow)
- (8) Please do not set LCD on its edge.
- (9) When LCMs are used for public display, defects such as Yogore & image sticking can not be guaranteed.
- (10) LCM cannot support "Interlaced scan method"
- (11) When this reverse model is used as a forward-type model (PCB on top side), LGD can not guarantee any defects of LCM.
- (12) Please conduct image sticking test after 2-hour aging with Rolling pattern and normal temperature.
  (25~40℃)

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#### 9-3. Electrostatic discharge control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

#### 9-4. PRECAUTIONS FOR STRONG LIGHT AND HAZARDOUS MATERIALS EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

The LCM should be avoided direct contact with Hazardous materials such as sulfur, acetic acid, chlorine, etc. These materials may cause chemical reaction such as sulfurization, corrosion, discoloration, etc.

### 9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.
  It is recommended that they be stored in the container in which they were shipped.

## 9-6. Handling precautions for protection film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normalhexane.