

# SHARP

No.	LA1-06061
DATE	Nov. 6, 2006

TECHNICAL LITERATURE

FOR

TFT - LCD module

MODEL No. LQ084V3DG01

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MOBILE LIQUID CRYSTAL DISPLAY GROUP

SHARP CORPORATION



## RECORDS OF REVISION

MODEL No.: LQ084V3DG01

SPEC No.: LA1-06061

[illegible]

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# 1 Applicable TFT-LCD module

This specification applies to the color TFT-LCD module, LQ084V3DG01.

## 2 Overview

This module is a color active matrix transmissive LCD module incorporating amorphous silicon TFT (Thin Film Transistor).

It is composed of a color TFT-LCD panel, driver ICs, control circuits and power supply circuitry and a backlight unit. Graphics and texts can be displayed on a  $640 \times \text{RGB} \times 480$  dots panel with 262,144 colors by feeding 18 bit data signal (6bit/each of R,G,B), 4(four) timing signals, +3.3V DC power supply for TFT-LCD and AC power supply for backlight.

(Note: Backlight-driving DC/AC inverter is not built in this module.)

- a) Fine images with stripe aligned 307,200 pixels on 8.4 inch diagonal screen
- b) Adapting a wide viewing angle technology [best viewing angle: 6 o'clock direction]
- c) High contrast, thanks to active matrix drive system
- d) AG(Anti Glare) polarizing filter
- e) Light and slim compact module (12 o'clock direction is also available by the function to flip the screen horizontally or vertically)
- f) Natural coloring reproducibility by employing normally-white-mode, which has good nature in coloring
- g) Image inversion both horizontally and vertically
- h) This module is adapted to RoHS compliance.

## 3 Mechanical Specifications

items	specifications	unit
Display size (Diagonal)	21.4 (8.4")	cm
Active display area	170.88 (H) $\times$ 128.16 (V)	mm
Pixel format	640(H) $\times$ RGB $\times$ 480(V)	dot
	(1 pixel=R+G+B dots)	-
Pixel pitch	0.267 (H) $\times$ 0.267(V)	mm
Pixel configuration	R,G,B vertical stripe	-
LCD mode	Normally white/Twisted Nematic	-
Dimension *	199.5 (W) $\times$ 149.5 (H) $\times$ 11.6(D)	mm
Mass	T.B.D.(max)	g

\*. Protrusion such as backlight harness is not included.

Fig.1 shows dimensions of the module



## 4 Input Signal Assignment

### 4.1 TFT-LCD Panel driving section

CN1 Used connector: IMSA-9637S-33Y902 (IRISO ELECTRONICS CO.,LTD.)

Pin No.	Symbol	Function	Polarity
1	GND	-	
2	CK	Clock signal for sampling each datum	
3	Hsync	Horizontal Sync signal	Negative
4	Vsync	Vertical Sync signal	Negative
5	GND		
6	R0	RED Data signal (LSB)	
7	R1	RED Data signal	
8	R2	RED Data signal	
9	R3	RED Data signal	
10	R4	RED Data signal	
11	R5	RED Data signal (MSB)	
12	GND		
13	G0	GREEN data signal (LSB)	
14	G1	GREEN data signal	
15	G2	GREEN data signal	
16	G3	GREEN data signal	
17	G4	GREEN data signal	
18	G5	GREEN data signal (MSB)	
19	GND		
20	B0	BLUE data signal (LSB)	
21	B1	BLUE data signal	
22	B2	BLUE data signal	
23	B3	BLUE data signal	
24	B4	BLUE data signal	
25	B5	BLUE data signal (MSB)	
26	GND		
27	ENAB	Data Enable signal (Control signal for image location in horizontal direction)	[Note 1]
28	Vcc	Power supply in	
29	Vcc	Power supply in	
30	R/L	Horizontal scanning direction control signal (Rightwards/Leftwards)	[Note 2]
31	U/D	Vertical scanning direction control signal (Upwards/downwards)	[Note 2]
32	NC		
33	GND		

The front shield case and back shield case is internally grounded to GND of the module.

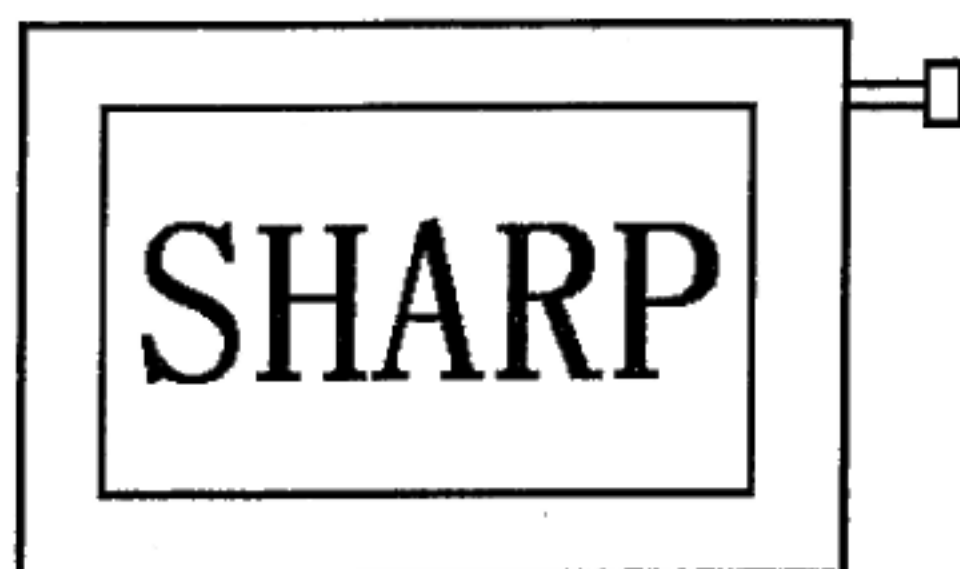
[Note 1]

The horizontal display location is designated and controlled by rising timing of ENAB signal.

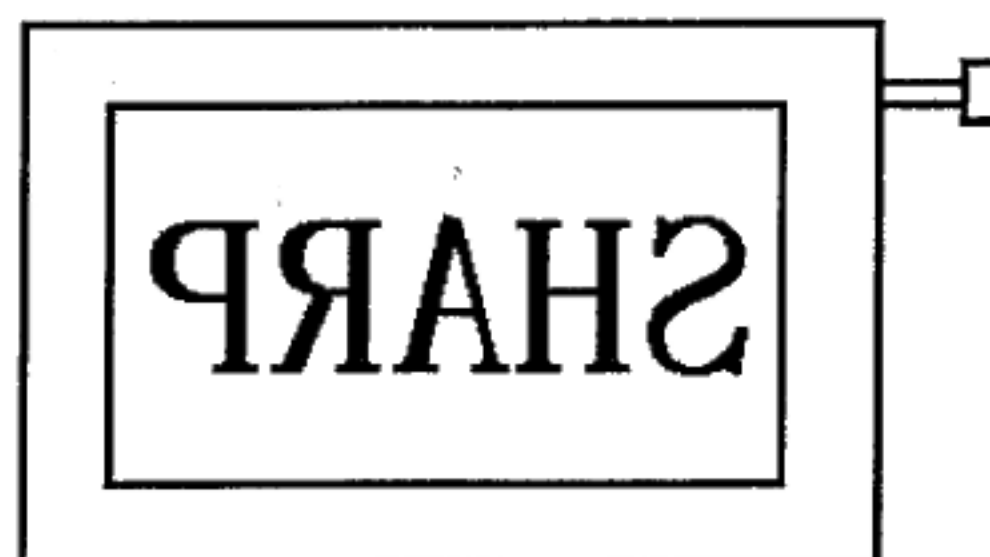
However if ENAB signal is fixed to "Low", display location is designated by the default setting in the module.

(Don't use the module by fixing ENAB to "High") .....See: Chapter 7-2

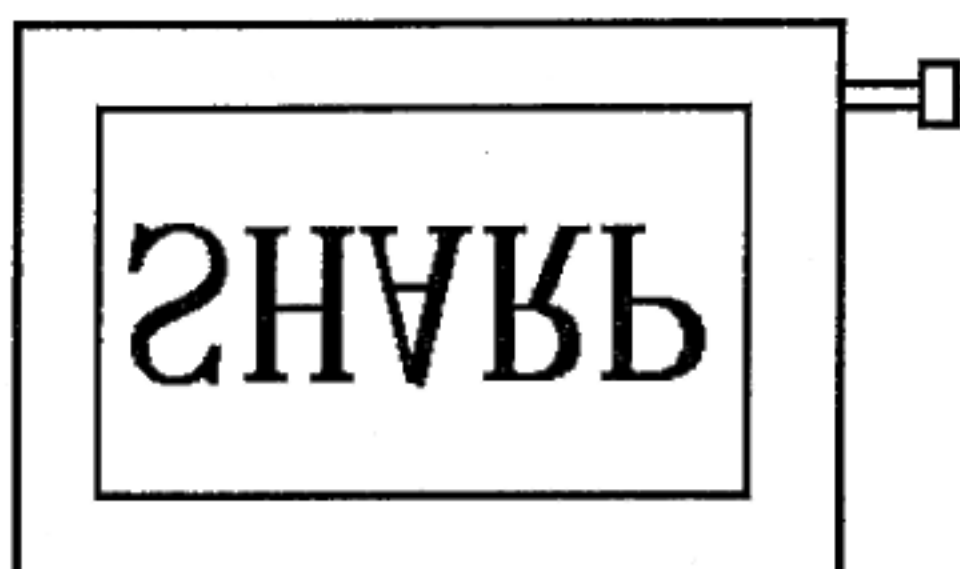
[Note 2]



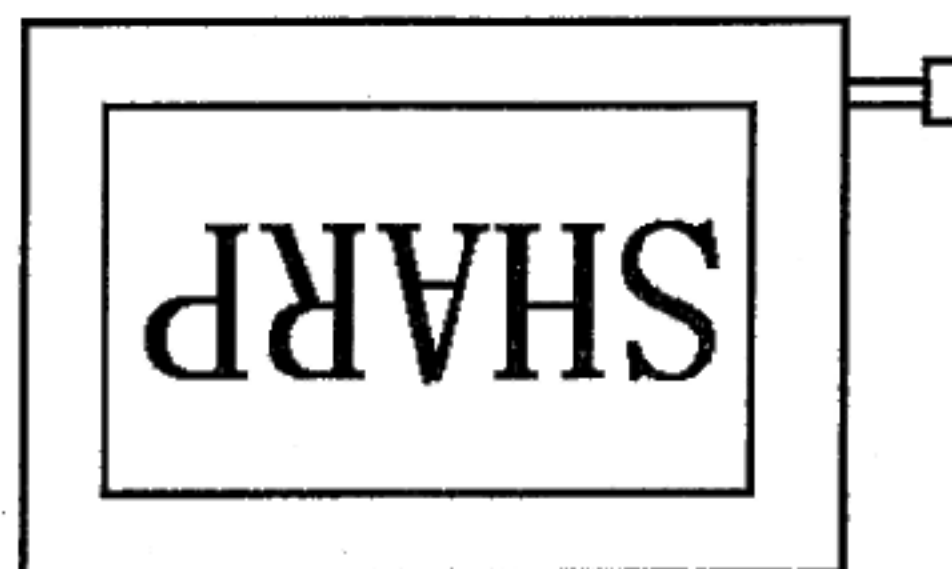
(R/L=High, U/D=Low)



(R/L=Low, U/D=Low)



(R/L=High, U/D=High)



(R/L=Low, U/D=High)

#### 4.2 Backlight section

Used connector : BHR-04VS-1(JST)

Corresponding connector : SM04(4.0)B-BHS(JST)

Pin no.	Symbol	Function
1	V <sub>HIGH</sub>	Power supply for lamp (High voltage terminal)
2	V <sub>HIGH</sub>	Power supply for lamp (High voltage terminal)
3	NC	This is electrically opened.
4	V <sub>LOW</sub>	Power supply for lamp (Low voltage terminal)

[Note]

The input voltage wave forms to terminal (1) and terminal (2) should be in a same phase.

It has the possibility to discharge abnormally between the terminals in case of input in a reversed phase.

#### 5 Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
Input voltage	V <sub>I</sub>	Ta=25°C	-0.3 ~ V <sub>CC</sub> +0.3	V	[Note 1]
Supply voltage	V <sub>CC</sub>	Ta=25°C	0 ~ +6.0	V	-
Storage temperature	T <sub>stg</sub>	-	(-30) ~ (+80)	°C	[Note 2,3,4]
Operating temperature (Panel surface)	T <sub>opp</sub>	-	(-30) ~ (+80)	°C	
Operating temperature (Ambient)	T <sub>opa</sub>	-	(T.B.D.)	°C	

[Note 1] CK, R0 ~ R5, G0 ~ G5, B0 ~ B5, Hsync, Vsync, ENAB, R/L and U/D

[Note 2] Humidity: Less than 95%RH at Ta ≤ 40°C and

Maximum wet-bulb temperature must not exceed 39°C at Ta > 40°C,  
with no condensation.[Note 3] The operating temperature only guarantees operation of the circuit. For contrast, speed response,  
and other factors related to display quality, judgment is done using the ambient temperature Ta = +25°C.

[Note 4] This rating applies to all parts of the module and should not be exceeded.

## 6 Electrical characteristics

### 6.1 TFT-LCD Panel driving section

Ta=25°C

Parameter	Symbol	Min	Typ	Max	Unit	Remarks
Supply voltage	V <sub>CC</sub>	+3.0	+3.3	+3.6	V	[Note1]
Current dissipation	I <sub>CC</sub>	-	(T.B.D.)	(T.B.D.)	mA	[Note2] V <sub>CC</sub> =3.3V
Allowed input ripple voltage	V <sub>RF</sub>	-	-	100	mV p-p	
Input voltage ("Low" state)	V <sub>IL</sub>	-	-	0.8	V	[Note3]
Input voltage ("High" state)	V <sub>IH</sub>	2.1	-	-	V	
Input leakage current (low)	I <sub>OL1</sub> [Note4]	-10.0	-	10.0	μA	V <sub>I</sub> =0V
	I <sub>OL2</sub> [Note5]	-800	-	-	μA	V <sub>I</sub> =0V
	I <sub>OL3</sub> [Note6]	-10.0	-	10.0	μA	V <sub>I</sub> =0V
Input leakage current (High)	I <sub>OH1</sub> [Note4]	-10.0	-	10.0	μA	V <sub>I</sub> =V <sub>CC</sub>
	I <sub>OH2</sub> [Note5]	-10.0	-	10.0	μA	V <sub>I</sub> =V <sub>CC</sub>
	I <sub>OH3</sub> [Note6]	-	-	800	μA	V <sub>I</sub> =V <sub>CC</sub>

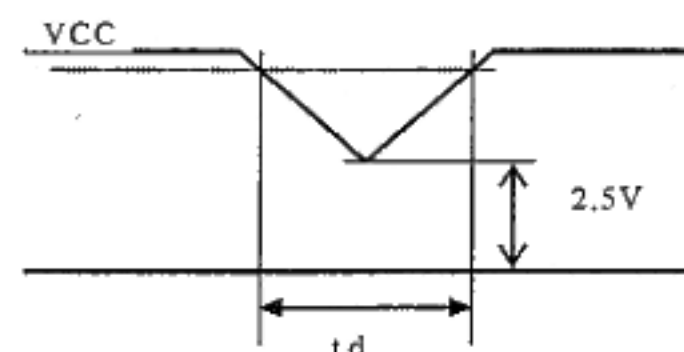
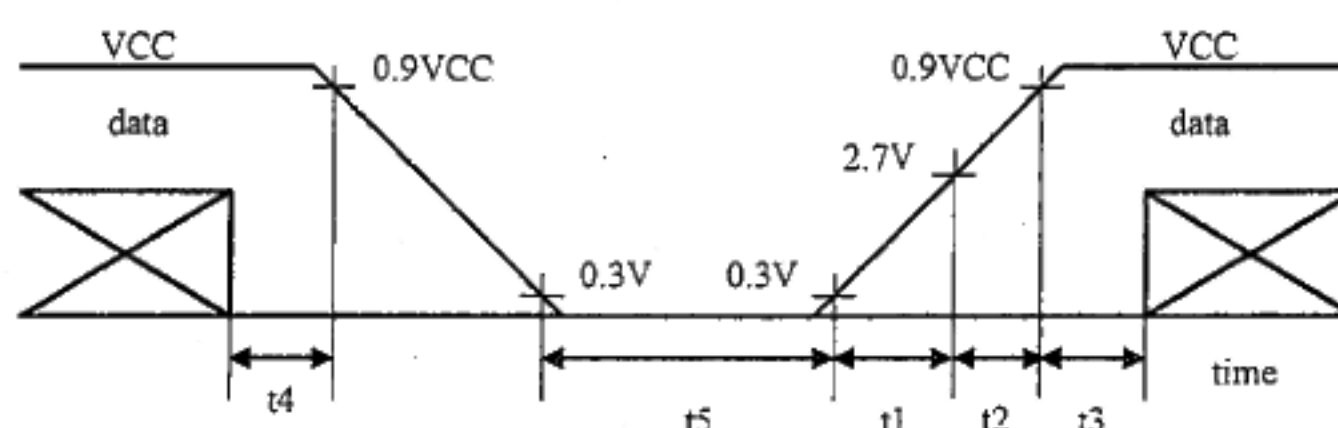
[Note1] V<sub>CC</sub> turn-on/off conditions

- $0 < t_1 \leq 15\text{ms}$   
 $0 < t_2 \leq 10\text{ms}$   
 $0 < t_3 \leq 100\text{ms}$   
 $0 < t_4 \leq 1\text{s}$   
 $t_5 > 200\text{ms}$

V<sub>CC</sub>-dip conditions

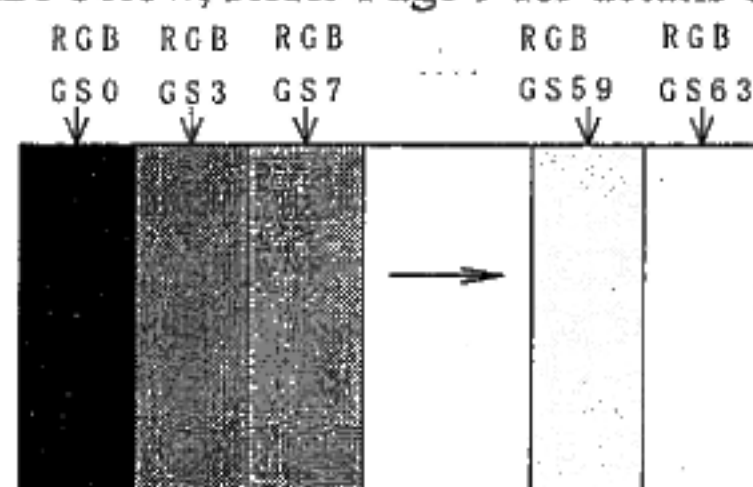
- 1) At  $2.5\text{V} \leq V_{CC}$   
 $t_d \leq 10\text{ms}$   
 2) At  $V_{CC} < 2.5\text{V}$

V<sub>CC</sub> dip conditions should also follow the V<sub>CC</sub> turn-on/off conditions



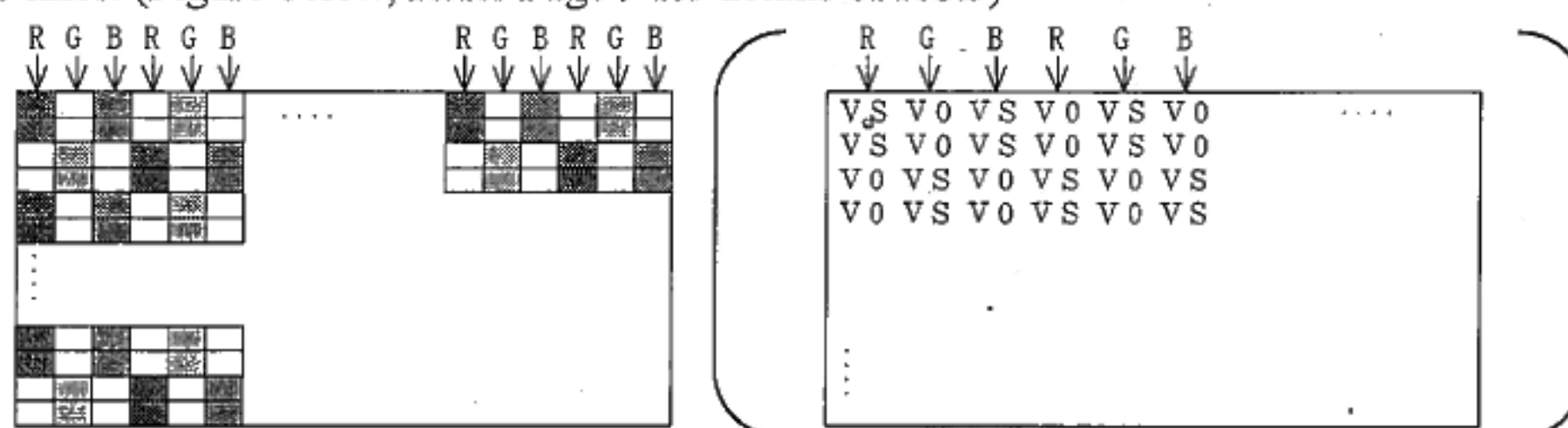
[Note2] Current dissipation (Typ.):

When Monochrome 16 level-gray-bar pattern is displayed  
(Figure below, Refer Page 9 for details of RGB)



Current dissipation(Max):

When VS(V63) and V0 stripe pattern are displayed alternately at intervals of two lines. (Figure below, Refer Page 9 for details of RGB)

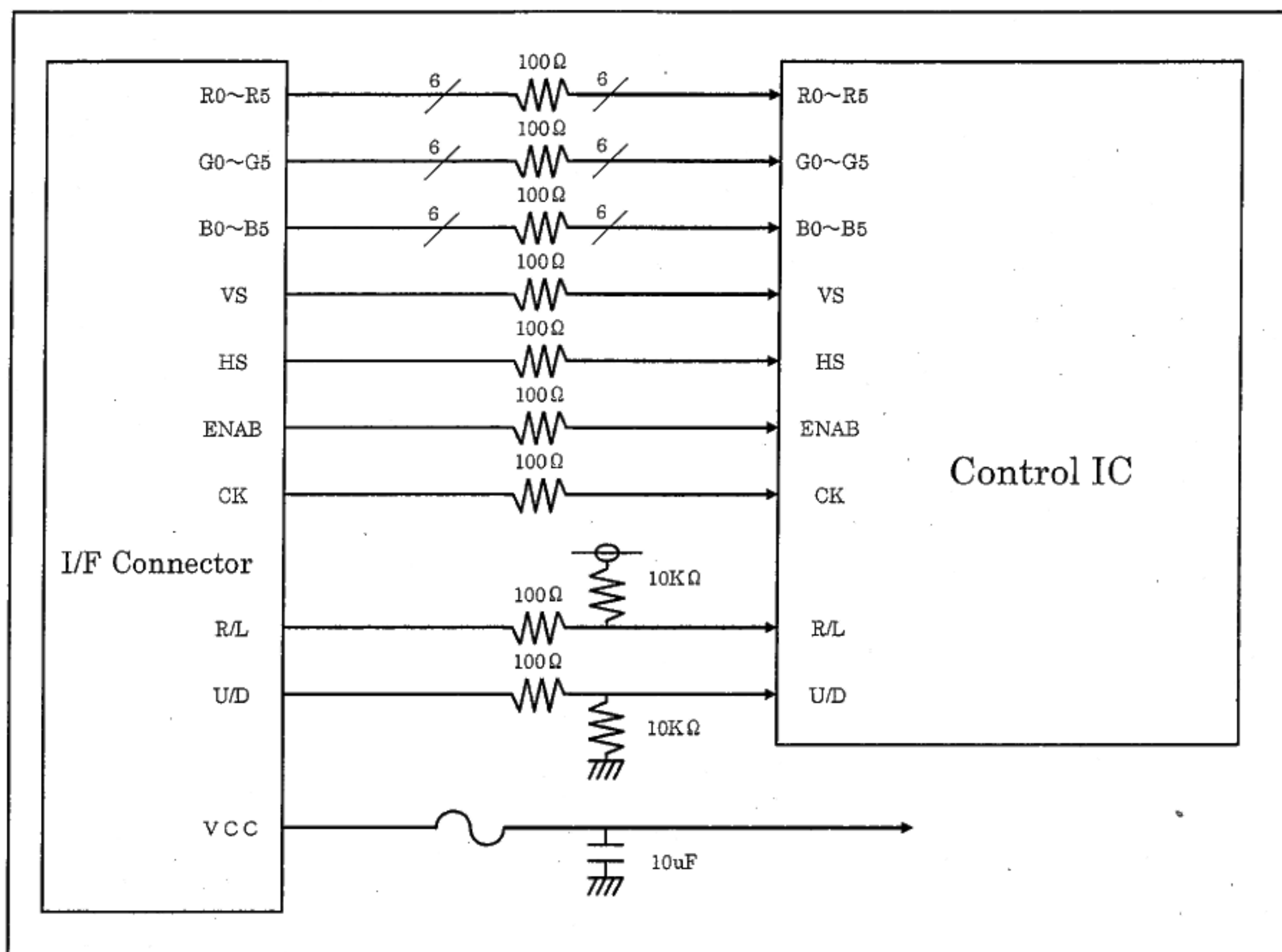


[Note3] CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L and U/D

[Note4] CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, and ENAB

[Note5] R/L

[Note6] U/D





## 6.2 Backlight driving Section

The backlight system is an edge-lighting type with two CCFTs (Cold Cathode Fluorescent Tube).

The characteristics of single lamp are shown in the following table.

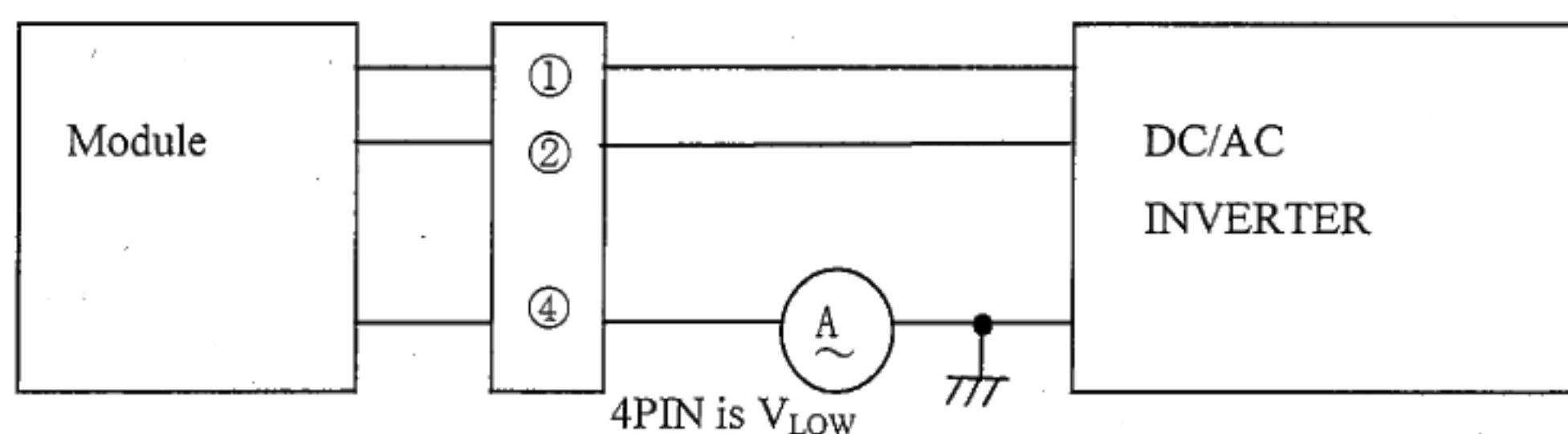
Recommended DC/AC inverter : (CXA-0463 :TDK)

$T_a=25^{\circ}\text{C}$

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remarks
Lamp voltage	VL	340	380	420	Vrms	$I_L=6\text{mA}_{\text{rms}}$
Lamp current	$I_L$	3.0	6.0	6.5	mA <sub>rms</sub>	【Note1】
Lamp power consumption	PL	-	2.3	-	W	【Note2】
Lamp frequency	FL	45	-	100	KHz	
Kick-off voltage	Vs	-	-	890	Vrms	$T_a=25^{\circ}\text{C}$
		-	-	1,000		$T_a=-10^{\circ}\text{C}$ 【Note3】
				1,070		$T_a=-30^{\circ}\text{C}$ 【Note3】
Lamp life time	LL	-	50,000	-	hour	【Note4】

【Note1】Lamp current is measured with current meter for high frequency as shown below.

【Note2】At the condition of  $Y_L = 350 \text{ cd/m}^2$



【Note3】The open output voltage of the inverter shall be maintained for more than 1sec; otherwise the lamp may not be turned on.

The input voltage wave forms to terminal ① and terminal ② should be in a same phase. It has the possibility to discharge abnormally between the terminals in case of input of a reversed phase.

For the sake of the safety, please so design the inveter as to prevent abnormal discharge when one of the two lamps is broken or reaches the end of life.

【Note4】 a)Lamp life time is defined as the time until it becomes the conditions either ① or ②by continuous lighting under the standard condition( $T_a=25^{\circ}\text{C}$ ,  $I_L=6\text{mA}_{\text{rms}} \times 2$ ).

① When a brightness of lamp surface became 50% of the initial value under the standard condition.

② When a kick-off voltage in  $T_a=-30^{\circ}\text{C}$  exceeded maximum value 1,070 Vrms.

b) In case of operating under lower temp. environment, the lamp exhaustion is accelerated and the brightness becomes lower.

(Continuous operating for around 1 month under lower temp. condition may reduce the brightness to half of the original brightness.)

In case of such usage under lower temp. environment, periodical lamp exchange is recommended.

【Note】The performance of the backlight, for example life time or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. When you design or order the inverter, please make sure that an inadequate lighting caused by the mismatch of the backlight and the inverter (miss-lighting, flicker, etc.) never occur. When you confirm it, the module should be operated in the same condition as it is installed in your instrument.

## 7 Timing Characteristics of Input Signals

Timing diagrams of input signal are shown in Fig.2.

## 7.1 Timing Characteristics

Parameter		Symbol	Min.	Typ.	Max.	Unit	Remark
Clock	Frequency	1/Tc	-	25.18	28.33	MHz	
	“High” time	Tch	5	-	-	ns	
	“Low” time	Tcl	10	-	-	ns	
Data	Setup time	Tds	5	-	-	ns	
	Hold time	Tdh	10	-	-	ns	
Hsync	Period	TH	30.00	31.78		μs	
			750	800	900	clock	
	Pulse width	THp	2	96	200	clock	
Vsync	Period	TV	515	525	560	line	
	Frequency	1/Tv	50	60	-	Hz	[Note7-1]
	Pulse width	TVp	1	-	34	line	
Horizontal display period		THd	640	640	640	clock	
Phase difference between Hsync and clock		THc	10	-	Tc-10	ns	
Phase difference between Hsync and Vsync		TVh	0	-	TH-THp	clock	
Vertical display period		TVs	34	34	34	line	

Note7-1) In case of lower frequency, the deterioration of display quality, flicker etc., may occur.

Please use this module in more than 50Hz.(1/Tv)

## 7.2 Display Position in horizontal direction

Display position in horizontal direction is designated by rising timing of ENAB signal.

Parameter		Symbol	MIN	TYP	MAX	Unit	Note
ENAB signal	Setup time	Tes	5	-	Tc-10	ns	
	Pulse width	Tep	2	640	TH-10	clock	
Phase difference between Hsync and ENAB signal		THe	44	-	TH-664	clock	

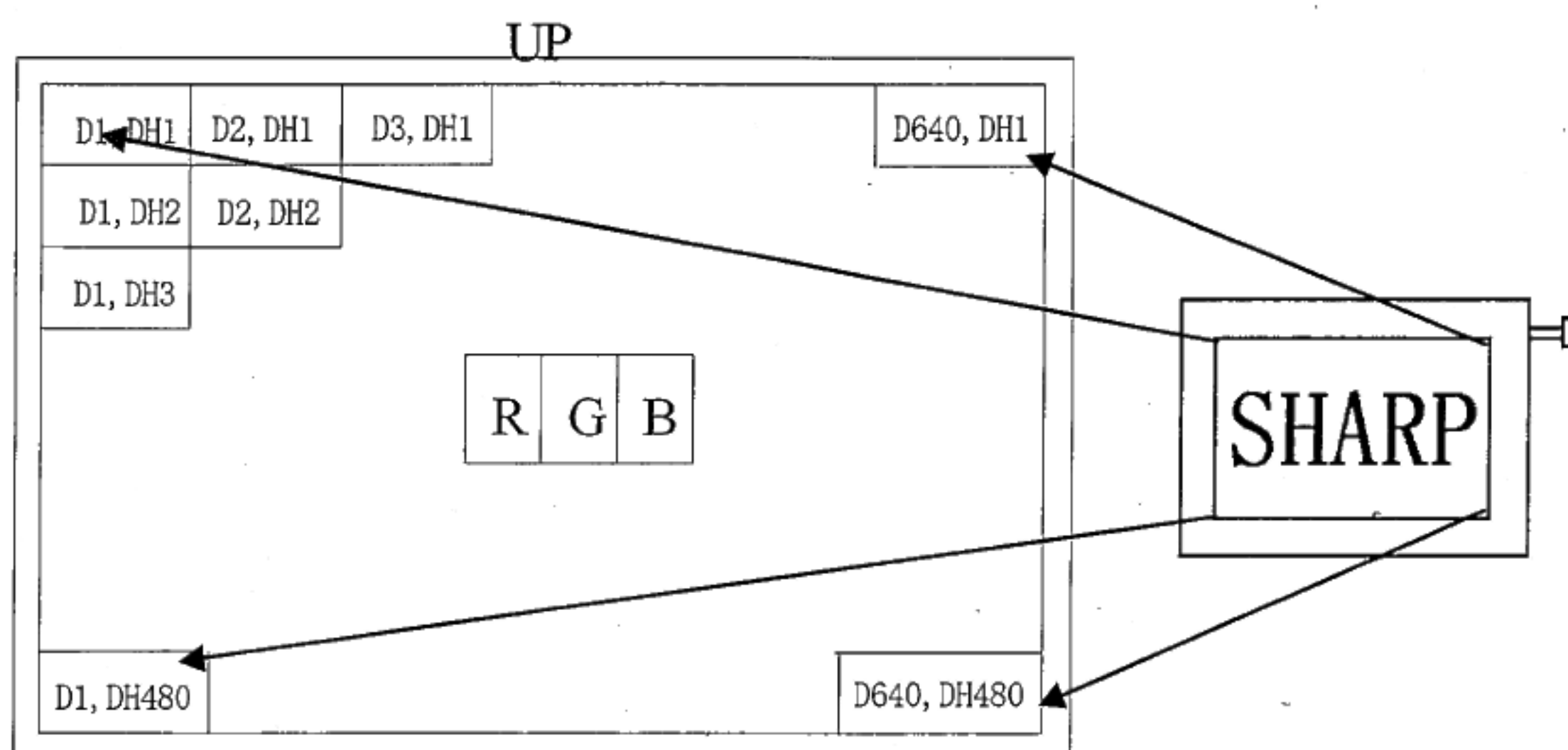
When ENAB is fixed to “Low”, the horizontal display will starts from the clock C104 (clock) as shown in Fig.2. When the phase difference is not greater than 104 clock, hold ENAB terminal in “High” state for more than (104-THe), otherwise display will start from C104 (clock).

### 7.3 Display position in vertical direction

Display start position in vertical direction is fixed to the 34th line.

#### 7.4 Input signal and display on the screen

### Display position of input data



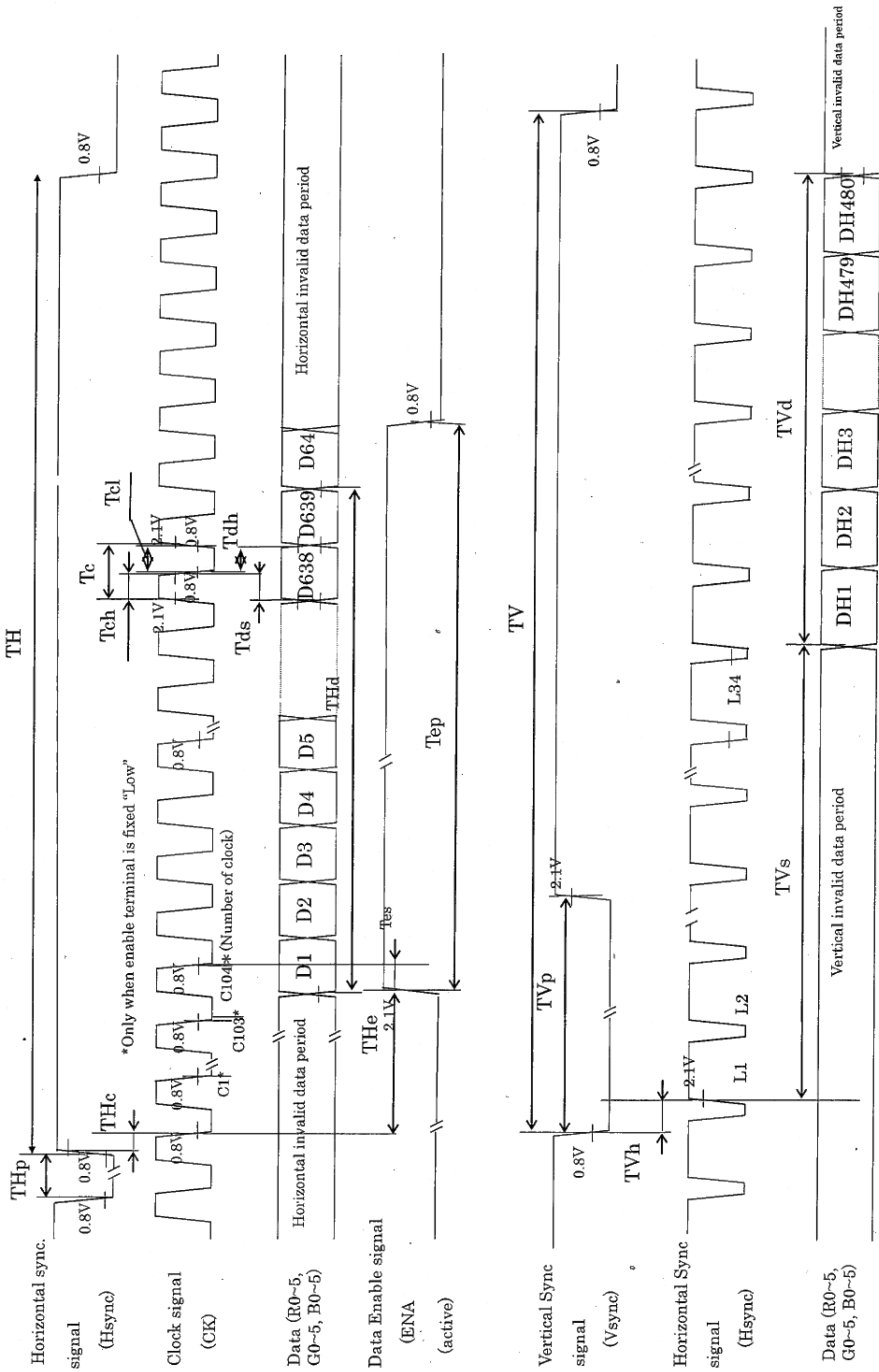


Fig 2. Input signal waveforms

## 8 Input Signals, Basic Display Colors and Gray Scale of Each Color

	Colors & Gray scale	Gray Scale	Data signal																	
			R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	B0	B1	B2	B3	B4	B5
Basic Color	Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	-	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Green	-	0	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0
	Cyan	-	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1
	Red	-	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0
	Magenta	-	1	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1
	Yellow	-	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of Red	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	↓	↓						↓						↓					
	↓	↓	↓						↓						↓					
	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	↓	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Green	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	GS1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	↑	↓	↓						↓						↓					
	↓	↓	↓						↓						↓					
	Brighter	GS61	0	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0
	↓	GS62	0	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0
Gray Scale of Blue	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
	↑	↓	↓						↓						↓					
	↓	↓	↓						↓						↓					
	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
	↓	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

0: Low level voltage, 1: High level voltage

Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.

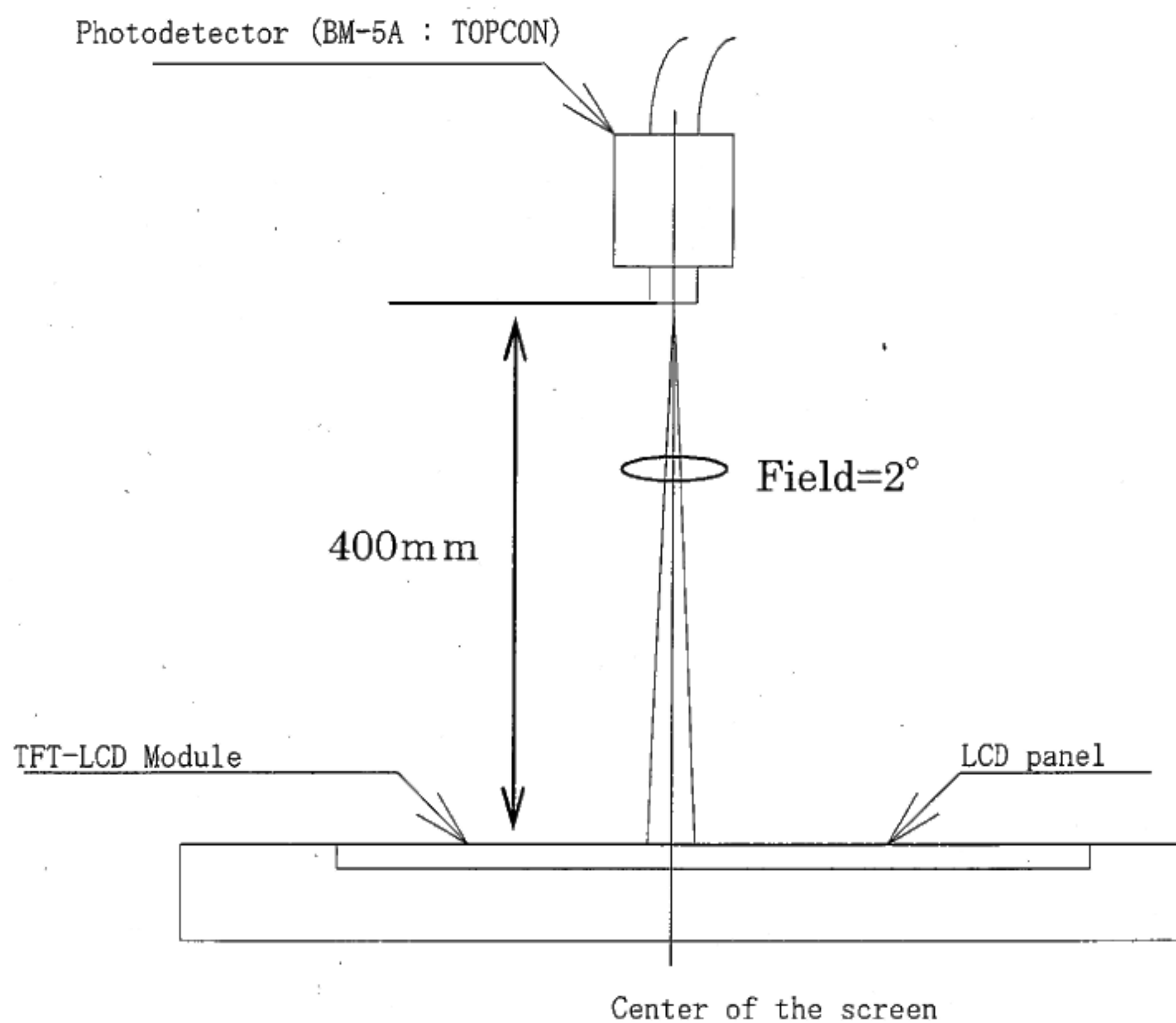


## 9 Optical Specification

Ta=25°C, Vcc=+3.3V

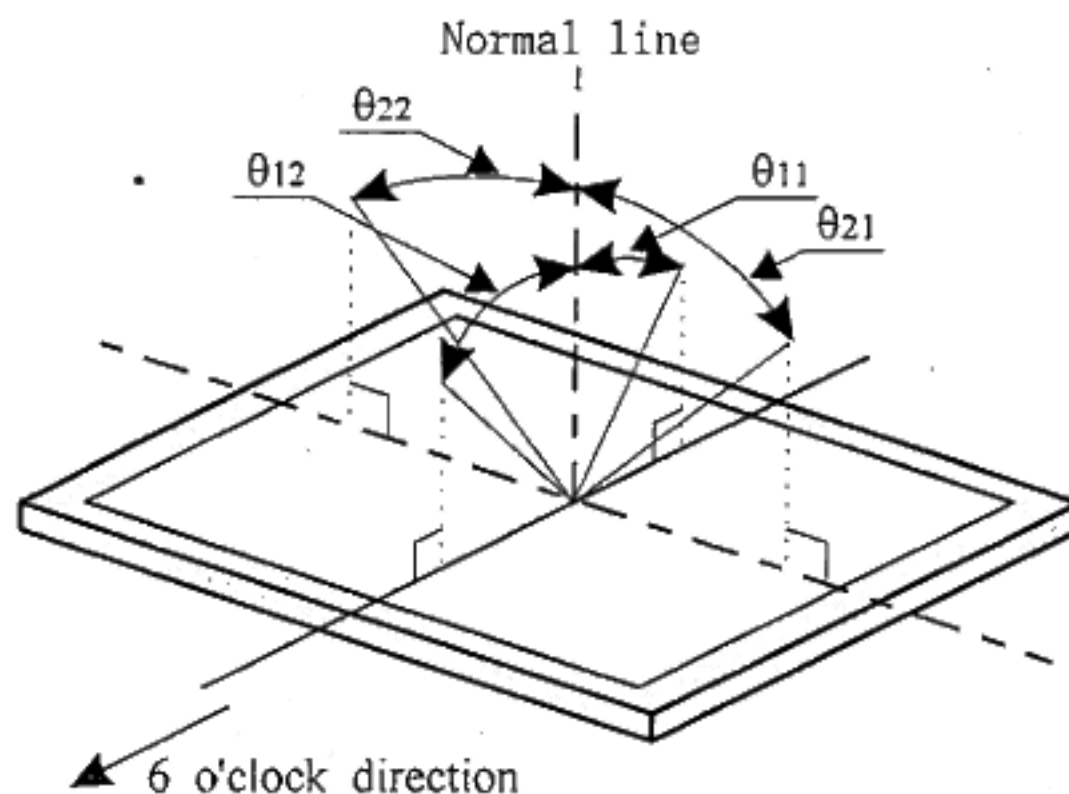
Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing angle Range	Horizontal	θ21,θ22	CR≥10	(50)	(60)	-	° (Deg.)	[Note9-1,4]
	Vertical	θ11		(45)	(55)	-	° (Deg.)	
		θ12		(50)	(60)	-	° (Deg.)	
Contrast ratio		CR max	Best viewing angle	-	(600)	-	-	[Note9-2]
Response time	Rise	Tr	θ = 0°	-	(8)	-	ms	[Note9-3]
	Fall	Td		-	(21)	-	ms	
Chromaticity of white		x	IL=6mA <sub>rms</sub>		(0.31)		-	[Note9-4]
		y			(0.33)		-	
Luminance		Y			(320)	(400)	-	

The optical specifications are measured 30 minute after turning lamp on and in a dark room or equivalent condition, according to the method shown in below.





[Note9-1] Definitions of viewing angle range:



The best viewing angle of this module is slightly leaned to 6 o'clock from normal line.

In the field where  $\theta_{12}$  exceeds this angle, gray-scale is reversed partially.

The gray-scale in the field of 12 o'clock direction is brighter than that of 6 o'clock direction and isn't reversed.

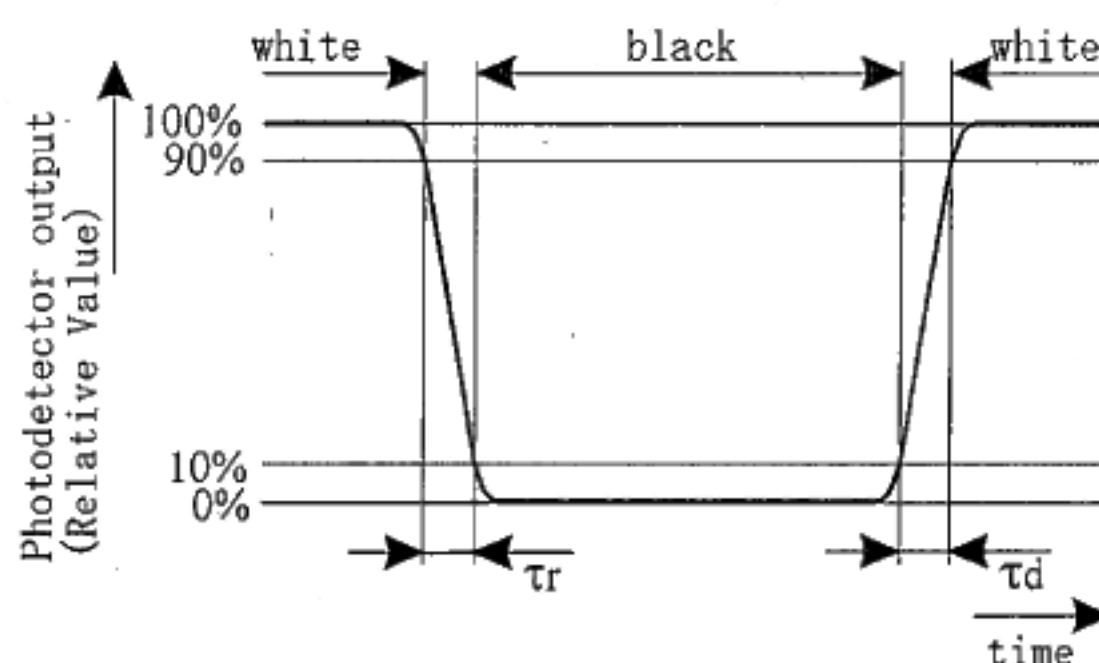
[Note9-2] Definition of contrast ratio:

The contrast ratio is defined as the following.

$$\text{Contrast Ratio (CR)} = \frac{\text{Luminance (brightness) with all pixels white}}{\text{Luminance (brightness) with all pixels black}}$$

[Note9-3] Definition of response time:

The response time is defined as the following figure and shall be measured by switching the input signal between "black" and "white" alternatively.



[Note9-4] This parameter should be measured at the center of the screen and 30 minutes after turn-on.

The characteristics are measured when the driver circuit is not powered.

## 10 Display Qualities

Please refer to the Outgoing Inspection Standard.

## 11 Handling Instruction

### 11.1 Assembling the module

- 1) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.
- 2) Please power off the module before you connect or disconnect input connector.
- 3) Make sure to connect module's metal shield case to GND of inverter circuit.

If its connection is not made perfectly, some of the following problems may occur.

- a) Increase of noise caused by backlight
- b) Unstable output from inverter circuit
- c) Possibly over-heat in some section

### 11.2 Instruction for assembling

- 1) Since the front polarizer is easily damaged, pay attention to avoid rubbing with something hard or sharp.
- 2) How to eliminate dust on polarizer.
  - a) Blow dust away by N<sub>2</sub> blower having measures of electrostatics
  - b) Since the front polarizer is easily damaged, wiping dust off is not adequate.  
If the polarizer is soiled, it is suggested to peel dust off by using adhesive surface of adhesive tape.
- 3) When the metal section [shield case or shield back case] is soiled, wipe it off with dry and soft cloth. If not so easy, breathe upon it then wipe off. If the water droplet or fat is left for long term, it may cause stain or tarnish, it should be wipe off immediately.
- 4) Since TFT-LCD panel is made of glass substrate, dropping the panel or banging it against hard objects may cause cracking or fragmentation.
- 5) Since CMOS LSIs are incorporated in this module, please pay special care to electrostatic while handling it, and earth human body.

### 11.3 Caution in product design

The notes and cautions below should be followed when product is designed with this module.

The module should be protected with cover to prevent salt content and/or water droplet.

Take enough shielding countermeasure not to interfere to peripheral electronic device.

#### 11.4 Others

The LCD has the nature that its performance is degradation by ultra-violet light. Don't leave the LCD module in direct sunlight or strong ultra violet ray.

If stored at the temperatures lower than the rated storage temperature, the LC may freeze and it may cause LCD panel damage. If storage temperature exceeds the specified rating, the molecular orientation of the LC may change to that of a liquid, and they may not revert to their original state. Store the module in normal room temperature.

The inductive loss caused by routing of lamp lead wire, which is closed to conductive section, may require the kick-off voltage greater than specified kick-off voltage.

The liquid crystal may leak out when the LCD is broken. If the liquid crystal drip into the eyes or mouth washes it out immediately.

The caution to other ordinal electronic component should be followed also.

#### 12 Packing Form

T.B.D.

#### 13 Marking of product name

T.B.D

#### 14 Miscellaneous





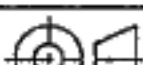
- a) Variable resistor has been set optimally before shipment, so do not change any adjusted value. If adjusted value is changed, the specification may be not satisfied.
- b) Don't disassemble this module, it may cause malfunction.
- c) Image retention may occur when the fixed image is display for long time.
- d) Liquid crystal panel drive input FFC/FPC specification
  - The following FFC/FPC is recommended for input connector.  
[IMSA-9637S-33Y902 (IRISO Electronics co.,ltd.) , 33pin 0.5mm pitch]
  - The terminal of FFC/FPC of input connector recommend to be gold or gold plated.  
Because point of contact of the connection is gold plated.

#### 15 Reliability Test Items

T.B.D.

[illegible]

Since this module is under development, all the specified value is tentative. The technical literature is subject to change without notice.

unit:mm		Please do not copy this material and do not disclose this to third party.		   			
DATE		2006.11.07		SCALE			
MODEL		LQ084V3DG01 (LQ084S1944)		free			
DRAWING NO		CM1-0638A		size A2			
						No.      DATE      REVISION	
ENGINEERING DEPARTMENT 1 MOBILE LCD DESIGN CENTER MOBILE LIQUID CRYSTAL DISPLAY GROUP 1 SHARP CORP.							

## OUTLINE DIMENSIONS