

Specification

LQ150X1LW73

Version March 2010

RECORDS OF REVISION

LQ150X1LW73

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1. Application

This specification applies to the color 15.0 XGA TFT-LCD module LQ150X1LW73.

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2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (<u>Thin Film Transistor</u>). It is composed of a color TFT-LCD panel, driver ICs, control circuit, power supply circuit and a back light unit. Graphics and texts can be displayed on a $1024 \times RGB \times 768$ dots panel with about 16 million colors by using LVDS (<u>Low Voltage Differential Signaling</u>) and supplying +3.3V DC supply voltages for TFT-LCD panel driving and supply voltage for backlight.

It is a wide viewing-angle-module

(Vertical viewing angle:170° Horizontal viewing angle:170°, CR≥10).

Backlight-driving DC/AC inverter is not built in this module.

3. Mechanical Specifications

Parameter	Specifications	Unit
Display size	38 (Diagonal)	cm
	15.0 (Diagonal)	Inch
Active area	304.1 (H)×228.1 (V)	mm
Pixel format	1024 (H)×768 (V)	Pixel
	(1 pixel=R+G+B dots)	
Pixel pitch	0.297 (H)×0.297 (V)	mm
Pixel configuration	R, G, B vertical stripe	
Display mode	Normally black	
Unit outline dimensions *1	331.6(W)×254.76(H)×12.5(D)	mm
Mass	1450(MAX)	g
Surface treatment	Anti-glare, LR-coating	
	and hard-coating 2H	
	(Haze value = 42)	

^{*1.}Note: excluding back light cables.

The thickness of module (D) doesn't contain the projection.

Outline dimensions are shown in Fig.1.

4. Input Terminals

4-1. TFT-LCD panel driving

CN1 (Interface signals and +3.3V DC power supply)

Using connectors : DF14H-20P-1.25H (Hirose Electric Co., Ltd.)

Corresponding connectors : DF14-20S-1.25C(Connector)

DF14-2628SCFA(Terminal)

Using LVDS Receiver : Contained in a control IC. [THC63LVDF84A(Thine) compatible]

Corresponding LVDS Transmitter : THC63LVDM83R(Thine) or compatible

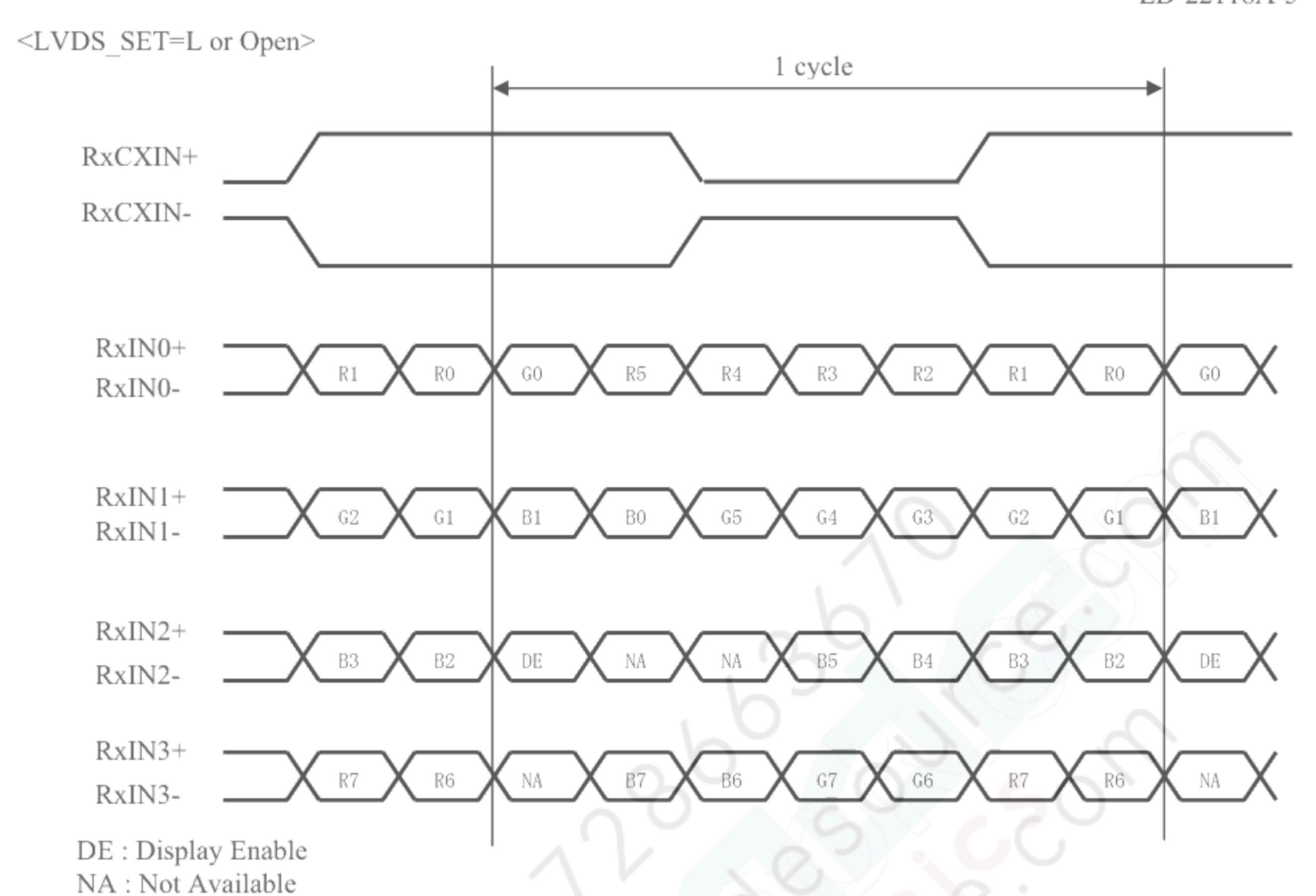
Pin No.	Symbol	Function	Remark
1	Vcc	+3.3V Power supply	
2	Vcc	+3.3V Power supply	
3	GND		
4	GND		
5	RXIN0-	Receiver signal (-)	LVDS
6	RXIN0+	Receiver signal (+)	LVDS
7	GND		
8	RXIN1-	Receiver signal (-)	LVDS
9	RXIN1+	Receiver signal (+)	LVDS
10	GND		
11	RXIN2-	Receiver signal (-)	LVDS
12	RXIN2+	Receiver signal (+)	LVDS
13	GND		
14	RXCKIN-	Clock signal (-)	LVDS
15	RXCKIN+	Clock signal (±)	LVDS
16	GND		
17	RXIN3-	Receiver signal (-)	LVDS
18	RXIN3+	Receiver signal (+)	LVDS
19	GND		
20	LVDS_SET	LVDS_SET	[note1]

4-2 Data Mapping

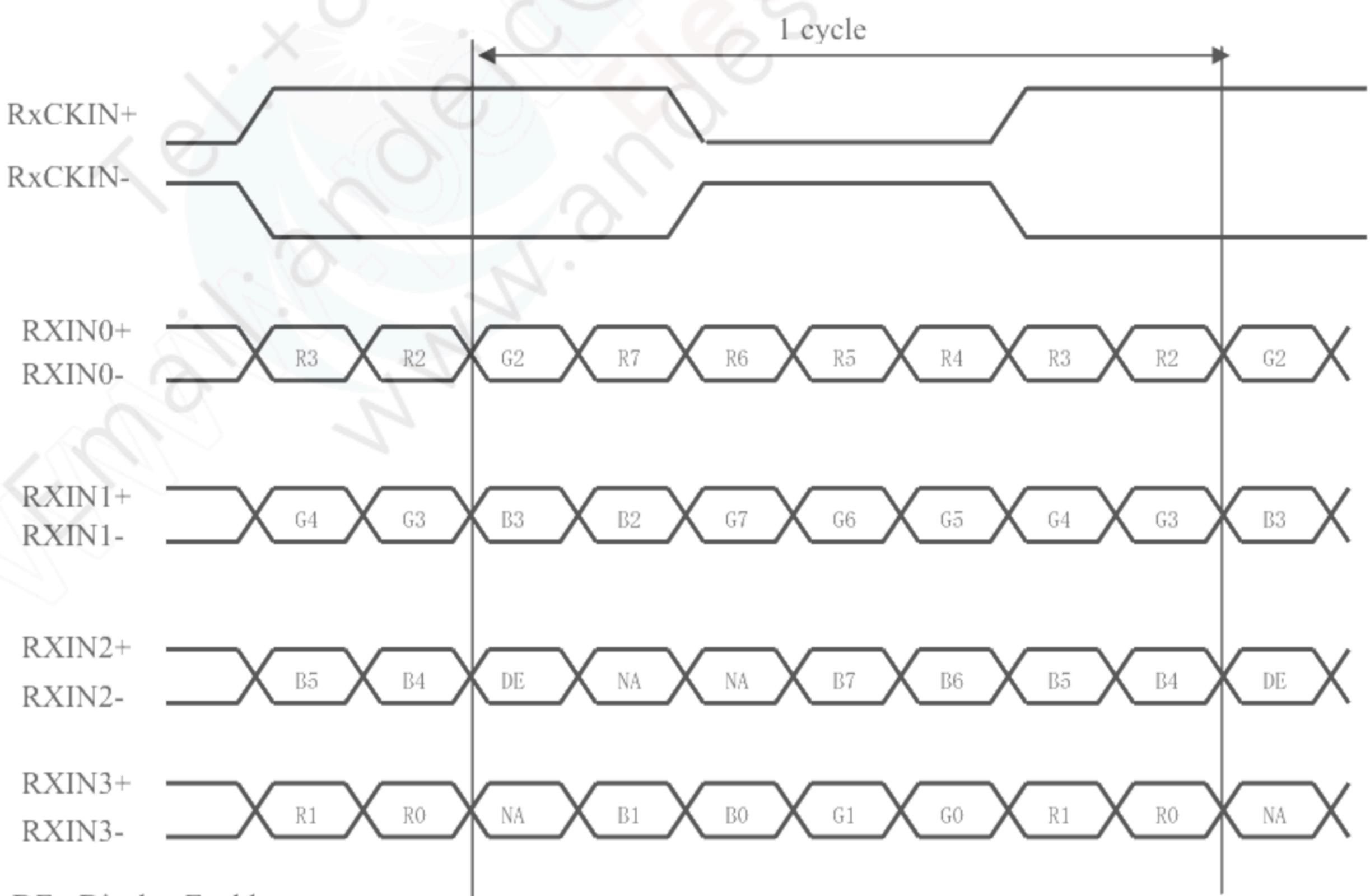
1) 8 bit input

[note1] pin assignment with LVDS_SET pin (Thine:THC63LVDM83R)

Tran	smitter	20pin LVDS_SET									
Pin No	Data	=L (GND) or Open	=H (3.3V)								
51	TA0	R0 (LSB)	R2								
52	TA1	R1	R3								
54	TA2	R2	R4								
55	TA3	R3	R5								
56	TA4	R4	R6								
3	TA5	R5	R7 (MSB)								
4	TA6	G0 (LSB)	G2								
6	TB0	G1	G3								
7	TB1	G2	G4								
11	TB2	G3	G5								
12	TB3	G4	G6								
14	TB4	G5	G7 (MSB)								
15	TB5	B0 (LSB)	B2								
19	TB6	B1	B3								
20	TC0	B2	B4								
22	TC1	B3	B5								
23	TC2	B4	B6								
24	TC3	B5	B7 (MSB)								
27	TC4	(NA)	(NA)								
28	TC5	(NA)	(NA)								
30	TC6	DE	DE								
50	TD0	R6	R0 (LSB)								
2	TD1	R7 (MSB)	R1								
8	TD2	G6	G0 (LSB)								
10	TD3	G7 (MSB)	G1								
16	TD4	B6	B0 (LSB)								
18	TD5	B7 (MSB)	B1								
25	TD6	(NA)	(NA)								



<LVDS_SET =H>



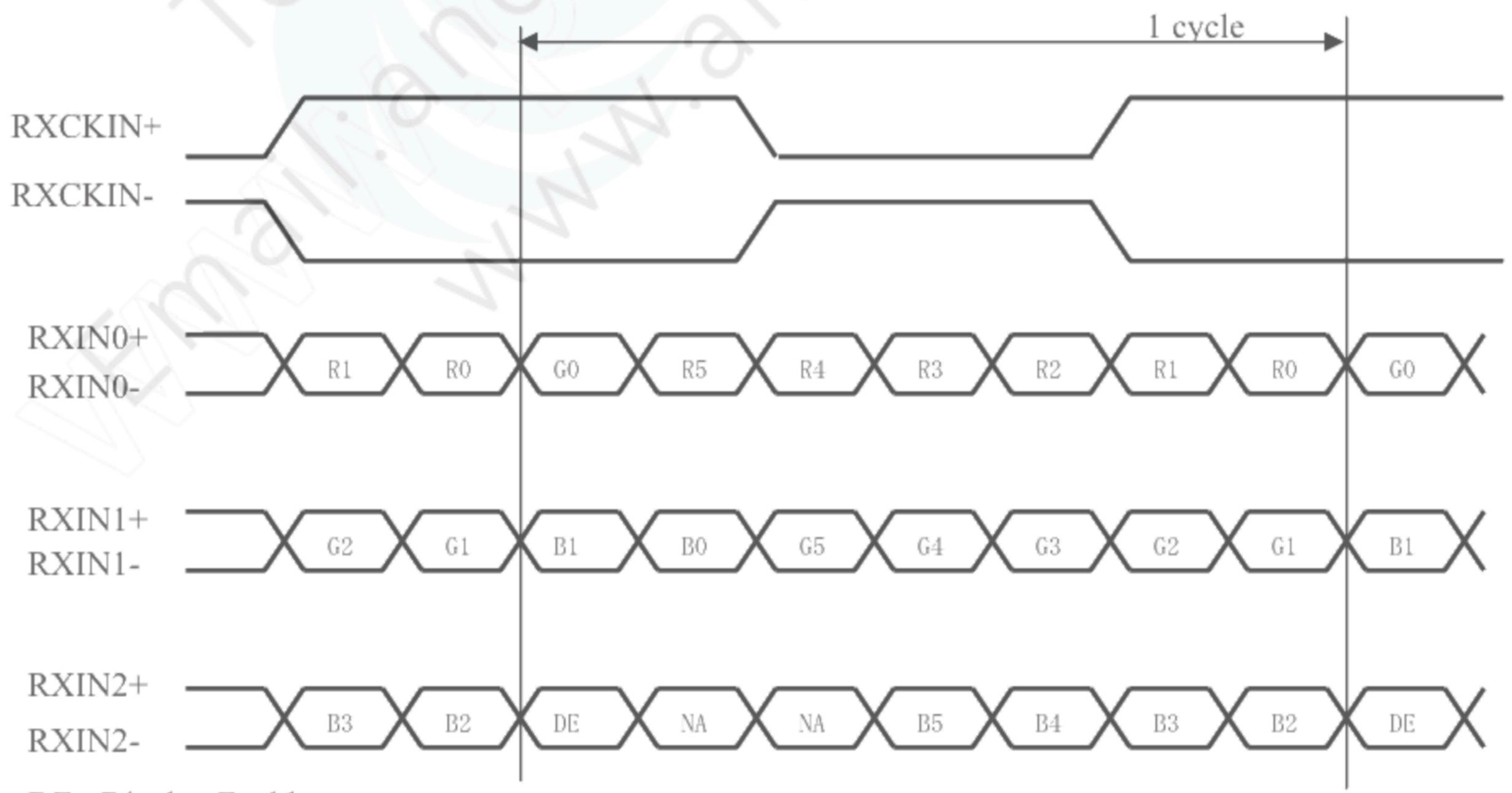
DE : Display Enable NA : Not Available

4-3 Data Mapping

2) 6 bit input

[note1] pin assignment with LVDS_SET pin (Thine:THC63LVDM83R)

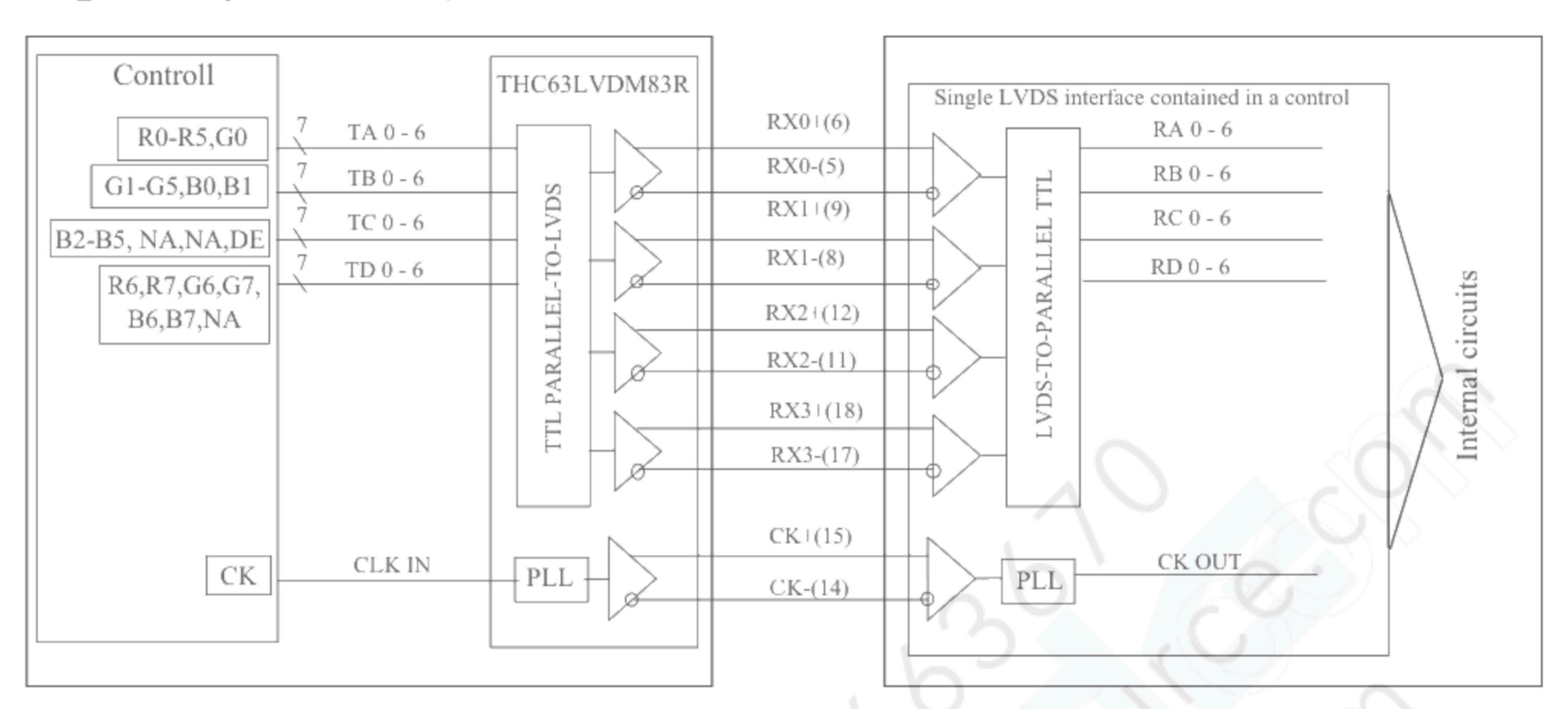
Tran	nsmitter	20pin LVDS_SET									
Pin No	Data	=L (GND) or Open	=H(3.3V)								
51	TA0		R0 (LSB)								
52	TA1	_	R1								
54	TA2	_	R2								
55	TA3		R3								
56	TA4	_	R4								
3	TA5	_	R5 (MSB)								
4	TA6		G0 (LSB)								
6	TB0		G1								
7	TB1		G2								
11	TB2		G3								
12	TB3		G4								
14	TB4		G5 (MSB)								
15	TB5		B0 (LSB)								
19	TB6		B1								
20	TC0		B2								
22	TC1		B3								
23	TC2		B4								
24	TC3		B5 (MSB)								
27	TC4		(NA)								
28	TC5		(NA)								
30	TC6		DE								
50	TD0		GND								
2	TD1		GND								
8	TD2		GND								
10	TD3		GND								
16	TD4		GND								
18	TD5		GND								
25	TD6		(NA)								



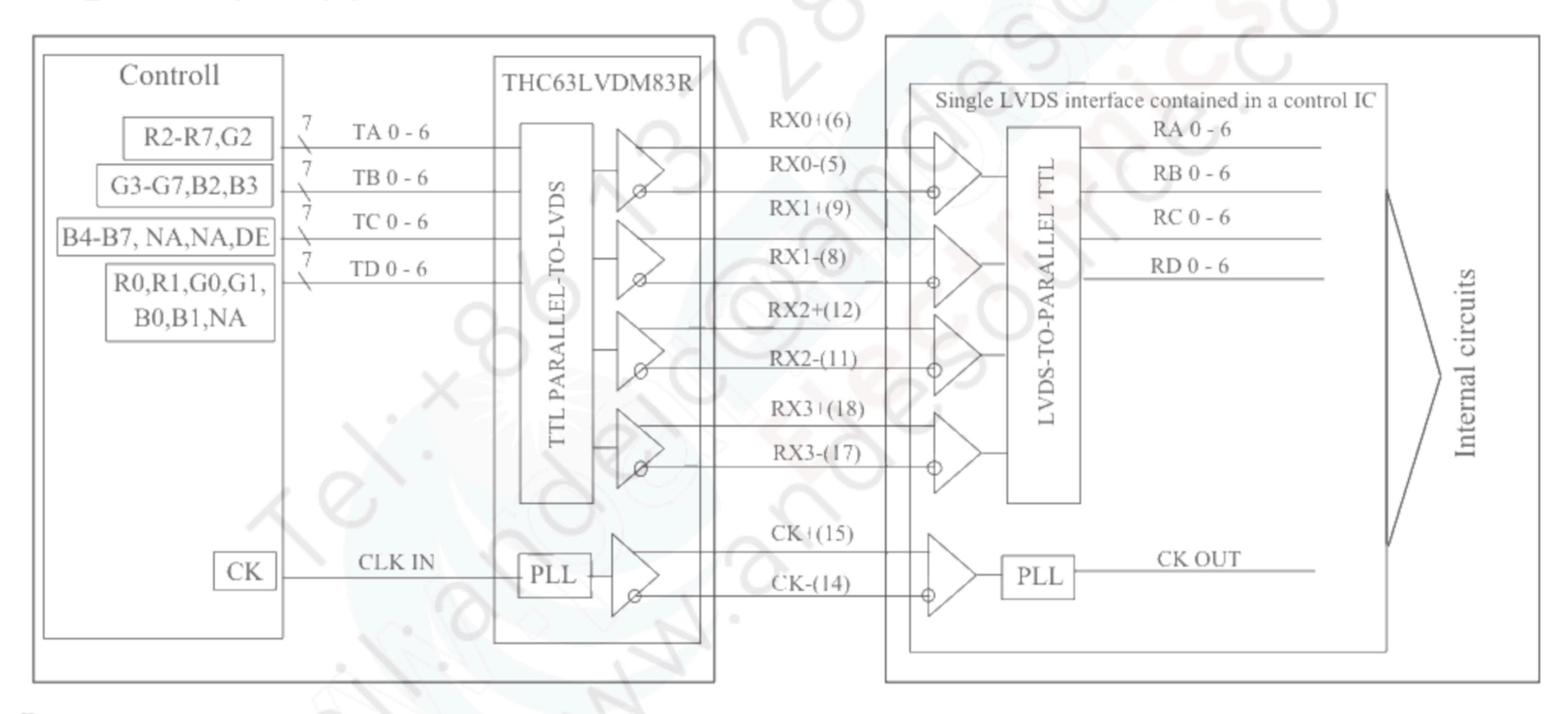
DE: Display Enable NA: Not Available (Computer Side)

(TFT-LCD side)

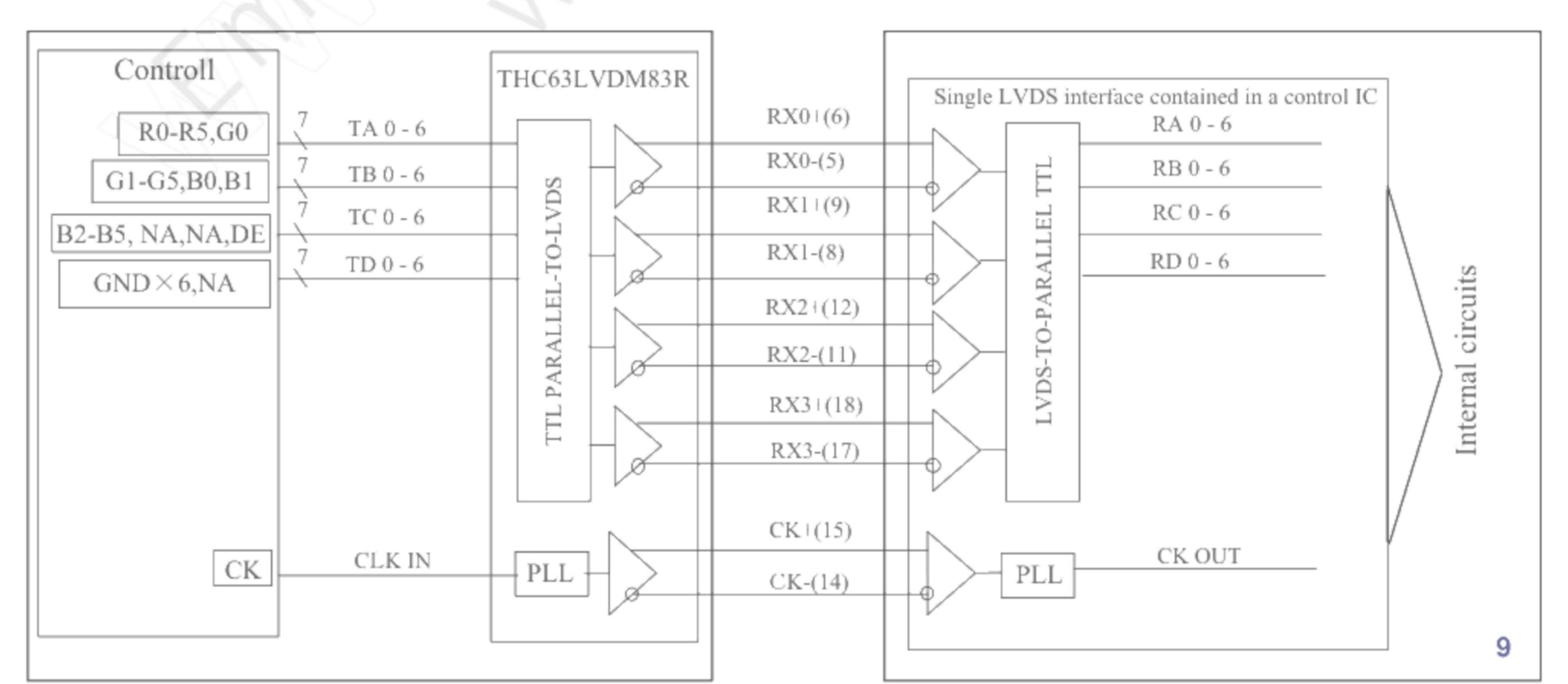
①8Bit Mode LVDS_SET=L (20 pin=GND or OPEN)



②8Bit Mode LVDS_SET=H (20 pin=3.3[V])



③6Bit Mode LVDS_SET=H (20 pin=3.3[V])

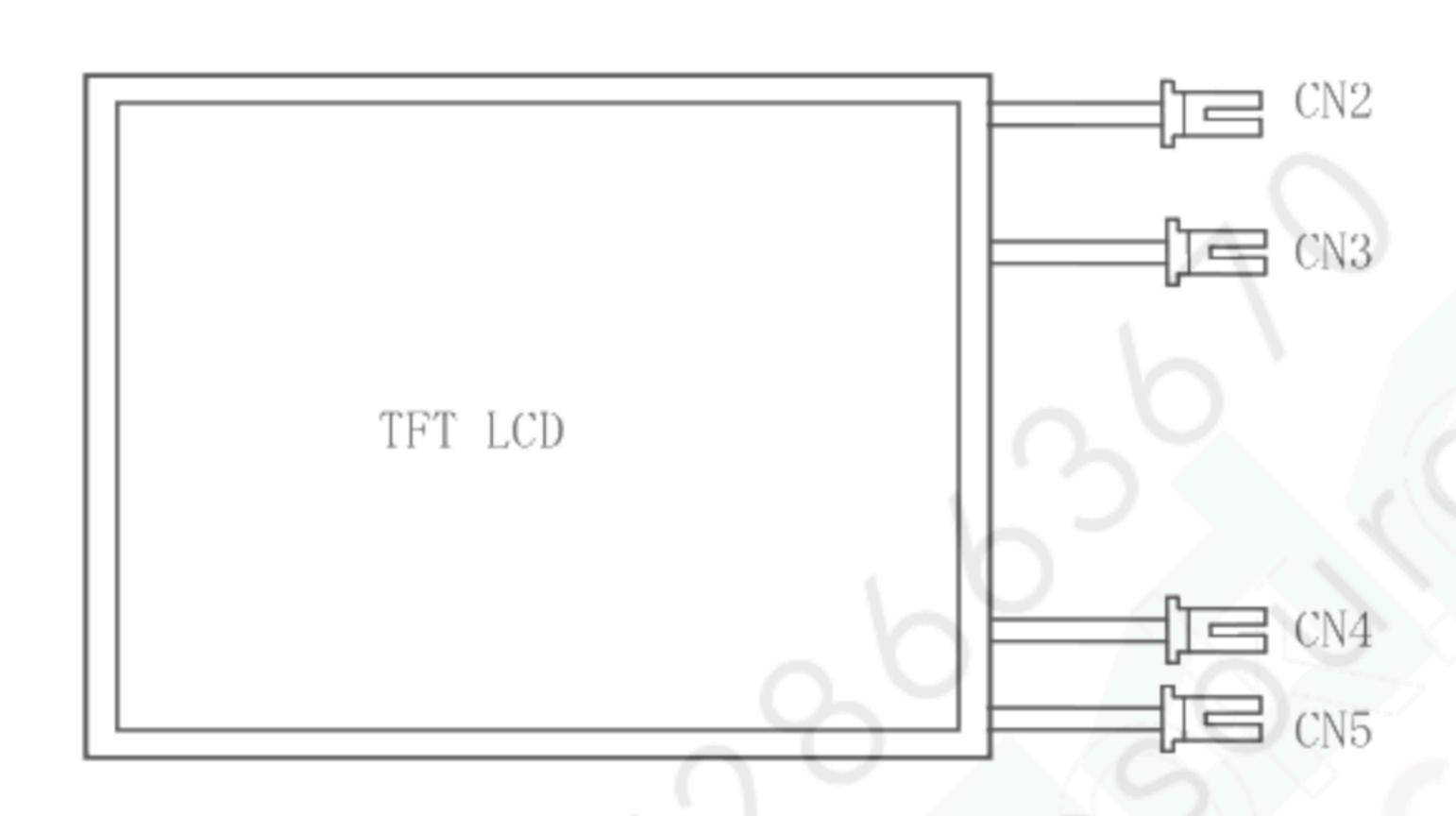


4-4. Backlight

CN 2, 3, 4, 5

The module-side connector: BHSR-02VS-1(N)(JST)

Pin no.	symbol	Funct	ion
1	V_{H}	Power supply for lamp	(High voltage side)
2	GND	Ground	



5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
Supply voltage	Vcc	Ta=25°C	0 ~ +4.0	V	
Storage temperature	T _{STG}		$-25 \sim +65$	°C	[Note1]
Operating temperature	T _{OPA}	Panel surface	0~+60	$^{\circ}\mathbb{C}$	

[Note1] Humidity: 95%RH Max. (Ta≤40°C)

Maximum wet-bulb temperature at 39°C or less. (Ta>40°C)

No condensation.

6. Electrical Characteristics

6-1. TFT-LCD panel driving

$Ta=25^{\circ}C$	
Remark	

	Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Power	Supply voltage	Vcc	+3.0	+3.3	+3.6	V	[Note1]
Supply	Current dissipation	Icc		400	700	mA	[Note2]
Permi	ssive input ripple voltage	V_{RF}	_	_	100	mVp-p	Vcc=+3.3V
Termi	nate Resister	R _T		100		Ω	
Input voltage (High)		V_{IH}			100	mA	
Input	voltage (Low)	V_{IL}	-100			mA	

[Note1]

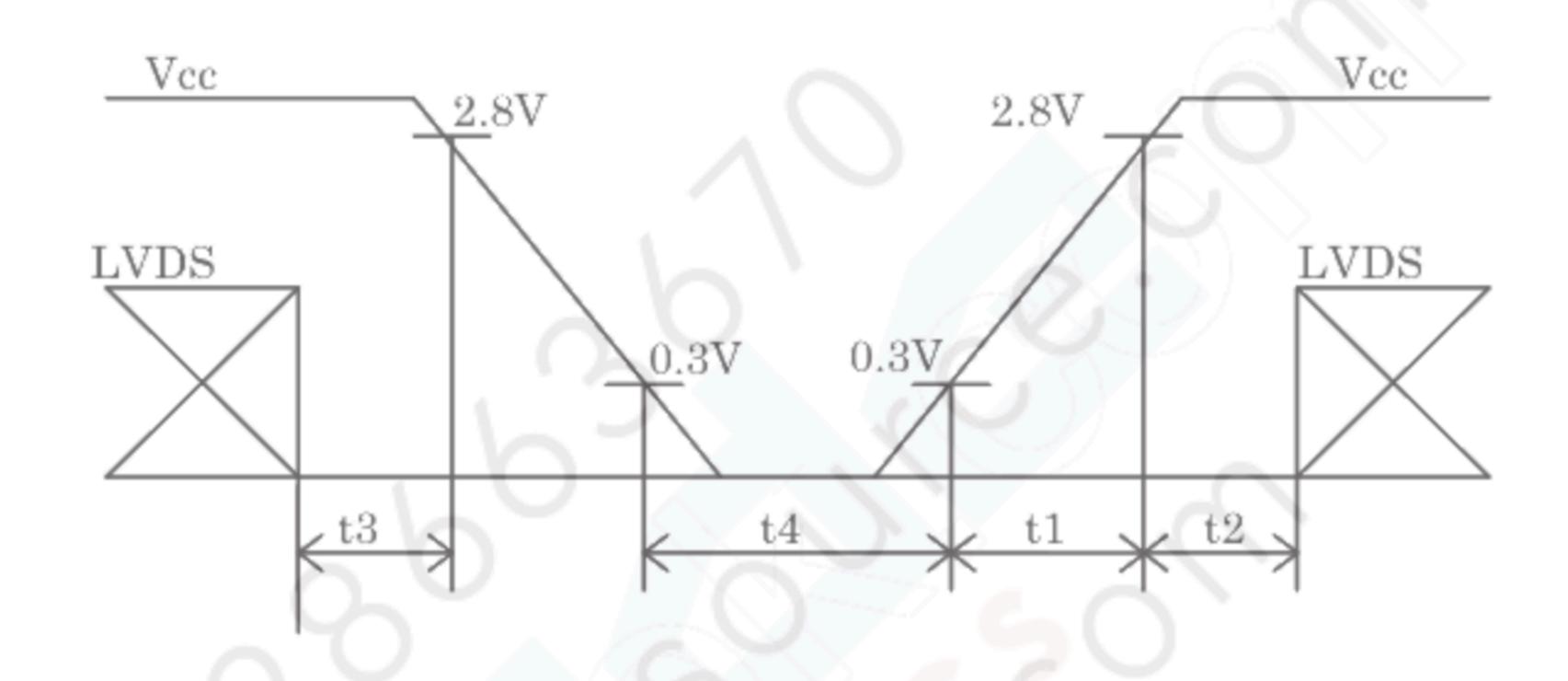
1) On-off sequences of Vcc and data

 $0 < t1 \le 60 \text{ms}$

 $0 < t2 \le 50 \text{ms}$

 $0 \le t3 \le 50 \text{ms}$

t4≧100ms

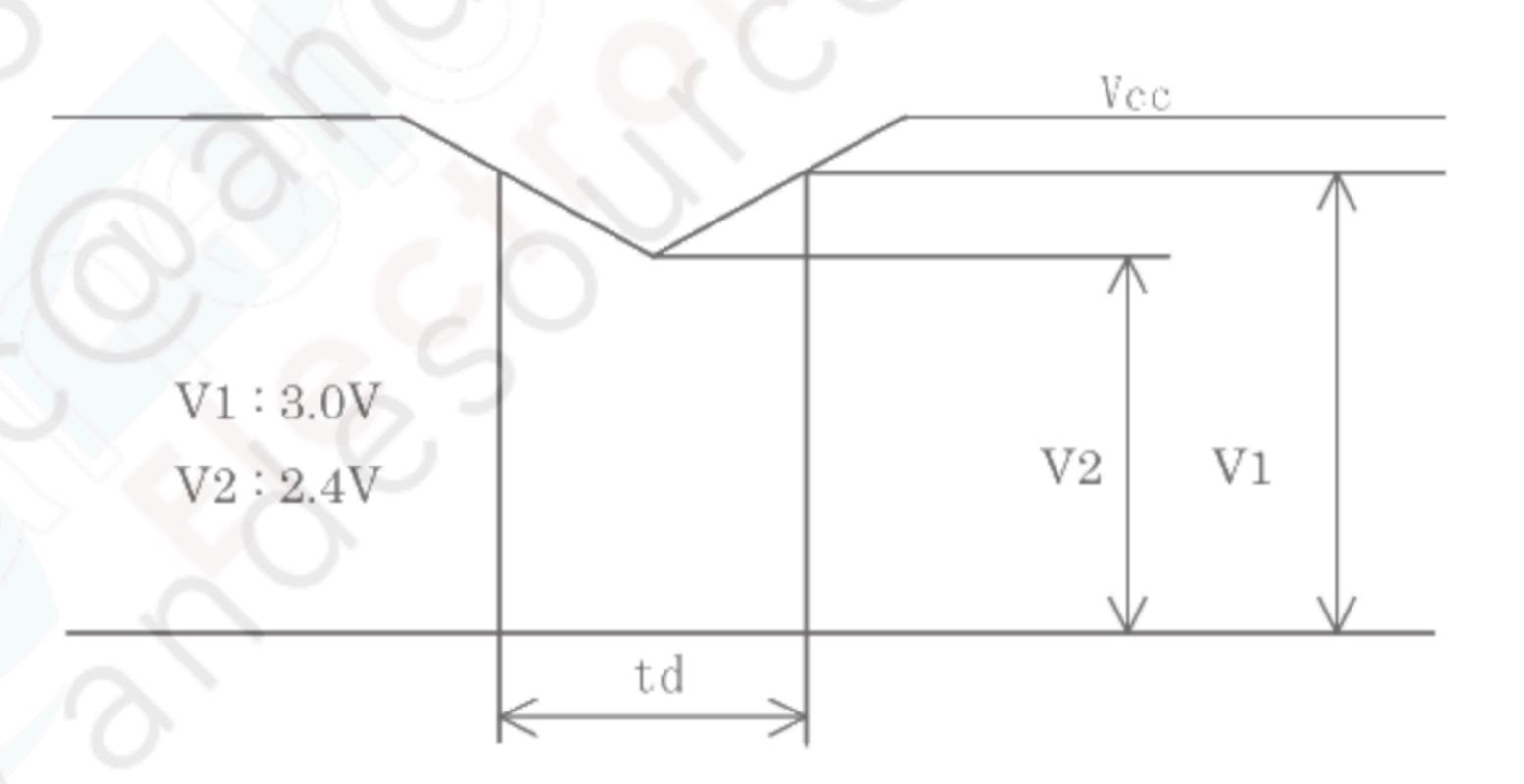


2) Dip conditions for supply voltage

1) V2 ≦Vcc< V1 td≦ 10ms

2) Vcc < V2

Vcc-dip conditions should also follow the on-off conditions.



[Note2] Typical current situation: 253-gray-bar pattern

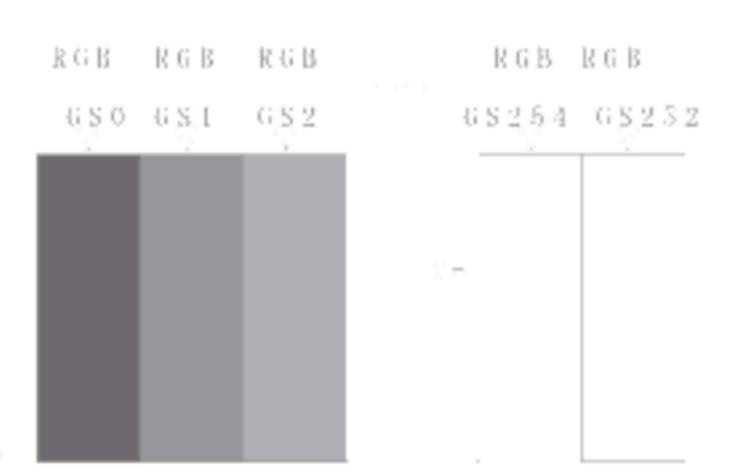
Vcc=+3.3V, CK=65MHz

Gray scale: GS(n)

 $n=0\sim 252$

The explanation of each gray scale, GS(n), is described

below section 8.



6-2. Backlight

The back light system is an edge-lighting type with 4 CCFTs (Cold Cathode Fluorescent Tube).

The characteristics of the lamp are shown in the following table.

The value mentioned below is at the case of one CCFT.

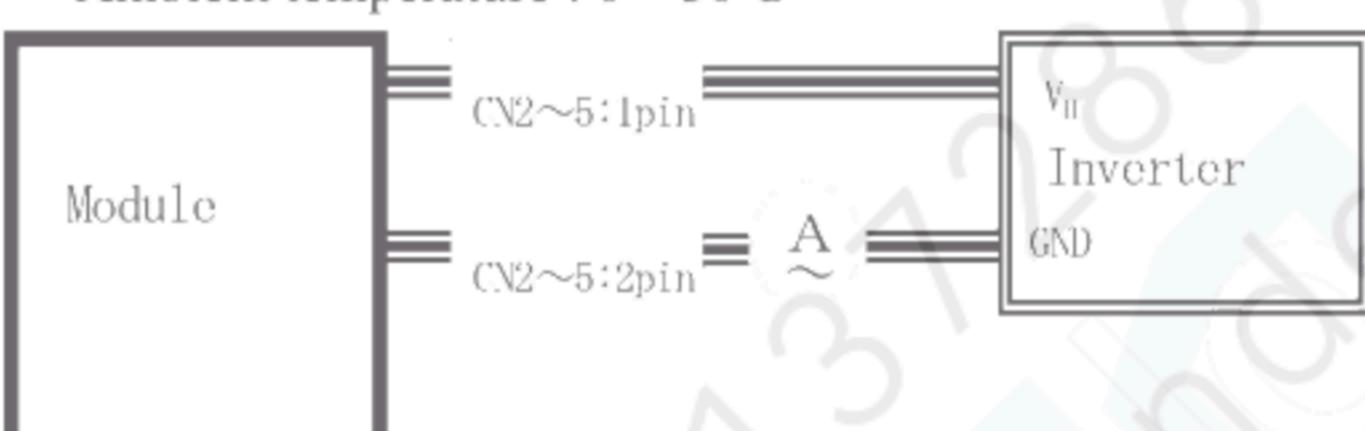
Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Lamp current range	I_L	3.5	6.5	7.5	mArms	[Note1]
Lamp voltage	V_{L}		615	700	Vrms	Ta=25°C,IL=6.5mArms
Lamp power consumption	PĹ		4.0	4.55	W	[Note2],IL=6.5mArms
Lamp frequency	FL	40	60	70	KHz	[Note3]
Kick-off voltage	Vs	_		1080	Vrms	Ta=25°C [Note4]
				1480	Vrms	Ta=0°C [Note4]
Lamp life time	LΤ	50,000	_	_	Hour	IL=6.5mArms [Note5]

[Note1] A lamp can be light in the range of lamp current shown above.

Maximum rating for current is measured by high frequency current measurement equipment connected to V_{LOW} at circuit showed below.

(Note: To keep enough kick-off voltage and necessary steady voltage for CCFT.)

Lamp frequency: $40 \sim 70 \text{kHz}$ Ambient temperature: $0 \sim 50 ^{\circ}\text{C}$



[Note2] Referential data per one CCFT by calculation (IL × VL). The data does not include loss at inverter.

[Note3] Lamp frequency of inverter may produce interference with horizontal synchronous frequency, and this may cause horizontal beat on the display. Therefore, adjust lamp frequency, and keep inverter as far as from module or use electronic shielding between inverter and module to avoid interference.

[Note4] Kick-off voltage value is described as the index in the state of lamp only.

The kick-off voltage is estimated to be risen up as approx. +200V in the state of module only, and the further rise up can be seen according to the assembling status of user cabinet. Please set the kick-off voltage of inverter to avoid the lighting failures in the state of operation. Please design the inverter so that its open output voltage can be connected for more than 1 second to startup. Otherwise, the lamp may not be turned on. But, please set as 100ms when the ambient luminance around the lamp is more than 1 lux.

[Note5] Lamp life time is defined as the time when either ① or ② occurs in the continuous operation under the condition of Ta=25°C and the lamp current value indicated to the Remark.

- ① Brightness becomes 50% of the original value under standard condition.
- ② Kick-off voltage at Ta=0°C exceeds maximum value,1480Vrms.

Note6

The performance of the backlight, for example lifetime or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. When you design or order the inverter, please make sure that a poor lighting caused by the mismatch of the backlight and the inverter (miss-lighting, flicker, etc.) never occurs. When you confirm it, the module should be operated in the same condition as it is installed in your instrument.

Use the lamp inverter power source incorporating such safeguard as overvoltage / overcurrent protective circuit or lamp voltage waveform detection circuit, which should have individual control of each lamp.

In case one circuit without such individual control is connected to more than two lamps, excessive current may flow into one lamp when the other one is not in operation.

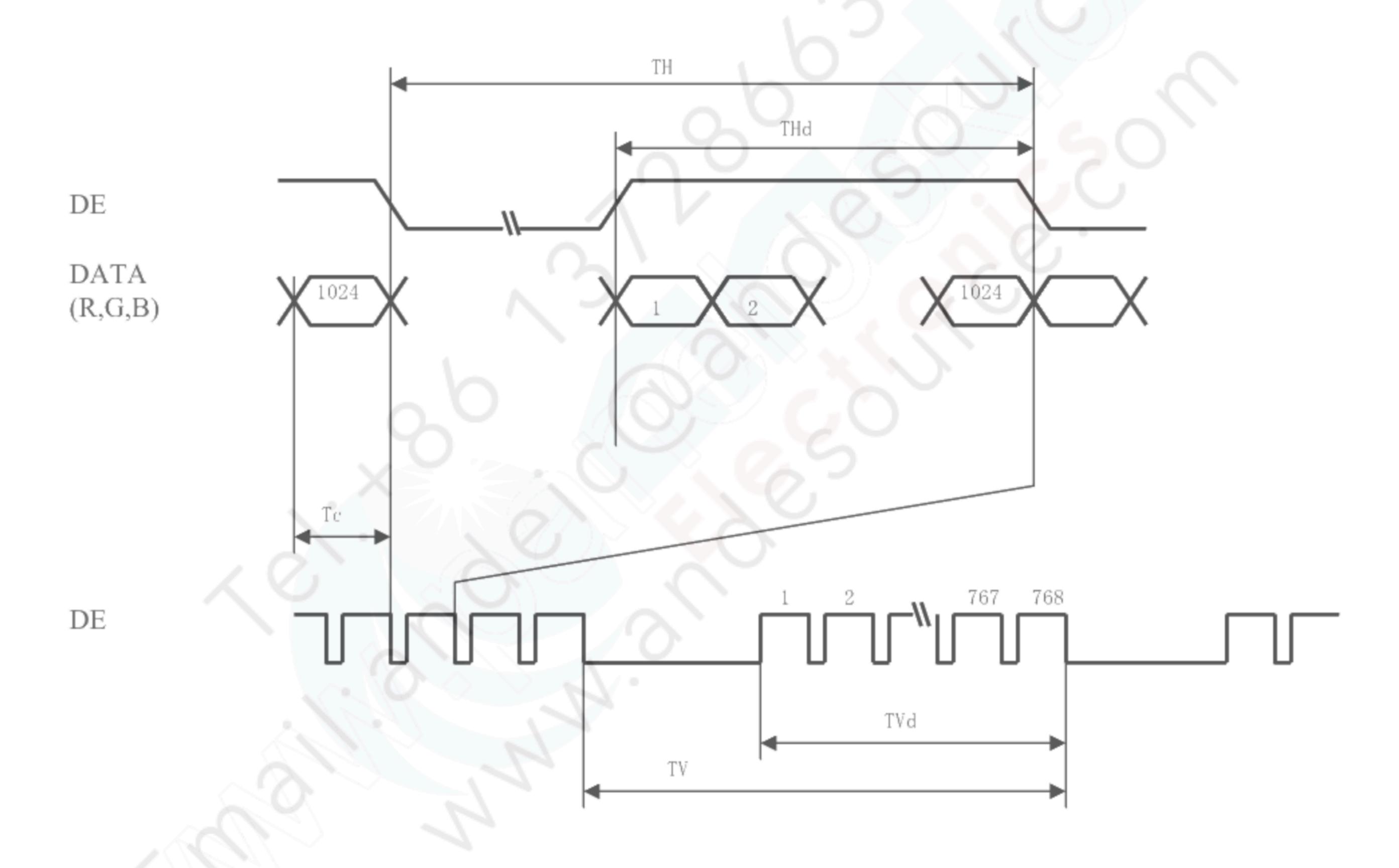
[Note7] Under the environment of 10lx or less, miss-lighting delay may occur.

7. Timing characteristics of input signals

7-1. Timing characteristics

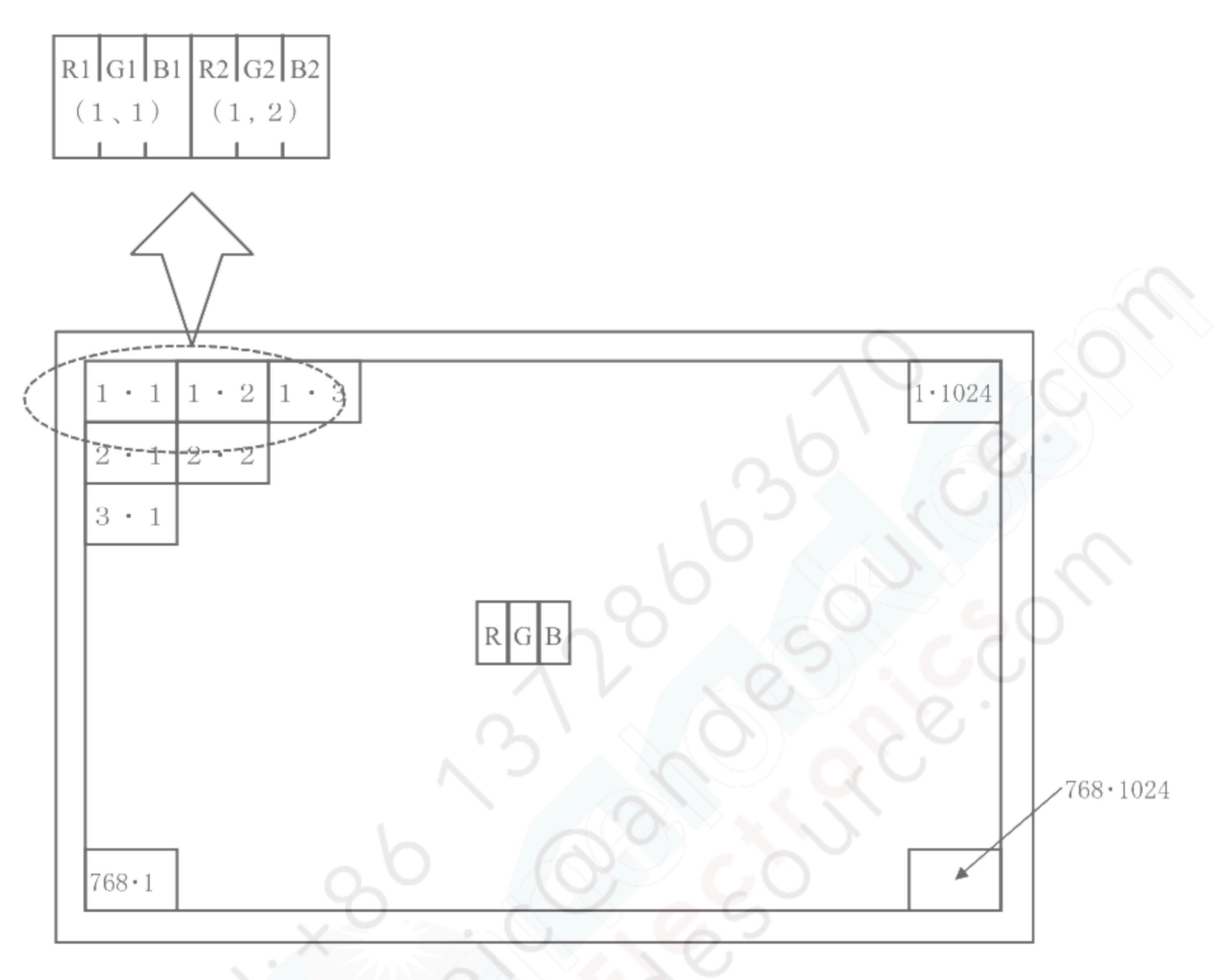
Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Frequency	1/Tc	60	65	85	MHz	
Horizontal period	TH	1056	1344	1720	clock	
		16.0	20.7	23.4	μs	
Horizontal period (High)	THd	1024		1024	clock	
Vertical period	TV	773	806	1008	line	[Note]
		13.3	16.7	18.1	ms	
Vertical period (High)	TVd	768		768	line	

[Note] In case of using the long vertical period, the deterioration of display quality, flicker etc. may occur.



7-2 Input Data Signals and Display Position on the screen

Graphics and texts can be displayed on a 1024 \times RGB \times 768 dots panel with 16M colors by supplying 24 bit data signal (8bit/color [253 gray scales] \times 3).



Display Position of Data (V, H)

8. Input Signals, Basic Display Colors and Gray Scale of Each Color

8-1 8bit input

8-1	8 DIU IN	րս I																								
													Data	sign	ıal											
	Colors &	Gray																								
	Gray scale	'	R0	RI	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	В0	В1	В2	ВЗ	В4	B5	В6	В7
	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1
	Green	_	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Cyan	_	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1
Color	Red	_	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
O.	Magenta	_	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0	Х	Х	1	1	1	>1	1	1
	Yellow	_	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1	0	0	0	0	\ ₀	0	0	0
	White	_	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1	Х	X	1	1	1	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Û	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale	Û	\downarrow				_	V					K)		L N	R	V.					,	l l			
le o	Û	\downarrow	↓							4						→										
of Red	Brighter	GS250	0	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
_	Û	GS251	1	1	0	1	1	I	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS252	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	Û	GS1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray S	Darker	GS2	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale	Û	\downarrow																↓								
of	Û	\downarrow	\ '				0	>/>				7	$\overline{}$		l							_	<u> </u>			
Green	Brighter	GS250	0	0	0	0	0	0	0	0	0	4	0	1	1	1	1	1	0	0	0	0	0	0	0	0
l ii	Û.	GS251	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0
	Green	GS252	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Û	GSI	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Gray	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
Scale	Û	4				1	L					V										l l				
요	Û	1	↓										V								V					
Blu	Brighter	GS250	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1
0	Û	GS251	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1
	Blue	GS252	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1
														$\overline{}$										$\overline{}$		

0 : Low level voltage, 1 : High level voltage. X :Don't care.

Each basic color can be displayed in 253 gray scales from 8 bit data signals. According to the combination of total 24 bit data signals, the 16-million-color display can be achieved on the screen.

8-2 6bit input

8-2 6bit input																				
		Data signal																		
	Colors &	Gray	R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	В0	В1	В2	В3	В4	В5
	Gray scale	Scale	100	10.1	102	10.5	104	IXJ		(,,,	02	(1)	()4	(1)	Do		172		D	100
Basic Color	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	_	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Green	_	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Cyan	_	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Red	_	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta		1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	_	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	_	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of Red	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Û	GS1	1	0	0	0	0	0	0	0	0	0	0	0	-0	0	0	0	0	0
	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Û	\	4						1					1						
	Û	\	↓					1												
	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Û	GS62	0	1	1	1	1	A l'	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
G	Û	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
Gray	Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
Scale	Û	4											↓							
of	Û	\	4										↓							
Green	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
n	.	GS62	0	0	0	0	0	0	0	Ĵ	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Gray Scale of Blue	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Û	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
	Û	1	1				\					↓								
	Û	1					\downarrow					↓								
	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	1
	Û	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

0 : Low level voltage, 1 : High level voltage.

Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.

9. Optical Characteristics

Ta=25°C, Vcc =+3.3V

Parameter		Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Viewing	Vertical	θ 11	CR ≥ 10	70	85	_	Deg.	[Note1,4]
angle		θ 12		70	85		Deg.	
range	Horizontal	θ 21, θ 22		70	85		Deg.	
Contrast ratio		CR	$\theta = 0^{\circ}$	400	600			[Note2,4]
Response Time		Td+Tr			35	55	ms	[Note3,4]
Chromaticity of		Wx		0.283	0.313	0.343		[Note4]
White		Wy		0.299	0.329	0.359	_	
Chromaticity of		Rx		0.608	0.638	0.668		
Red		Ry		0.309	0.339	0.369		
Chromaticity of		Gx		0.250	0.280	0.310	40	
Green		Gy		0.570	0.600	0.630		
Chromaticity of		Bx		0.114	0.144	0.174		
Blue		Ву		0.057	0.087	0.117		
Luminance of white		YL		280	350		cd/m ²	IL=6.5mA 60kHz ms [Note4]
White Uniformity		δw				1.25		[Note5]

^{*}The measurement shall be executed 30 minutes after lighting at rating.

The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig.2 below.

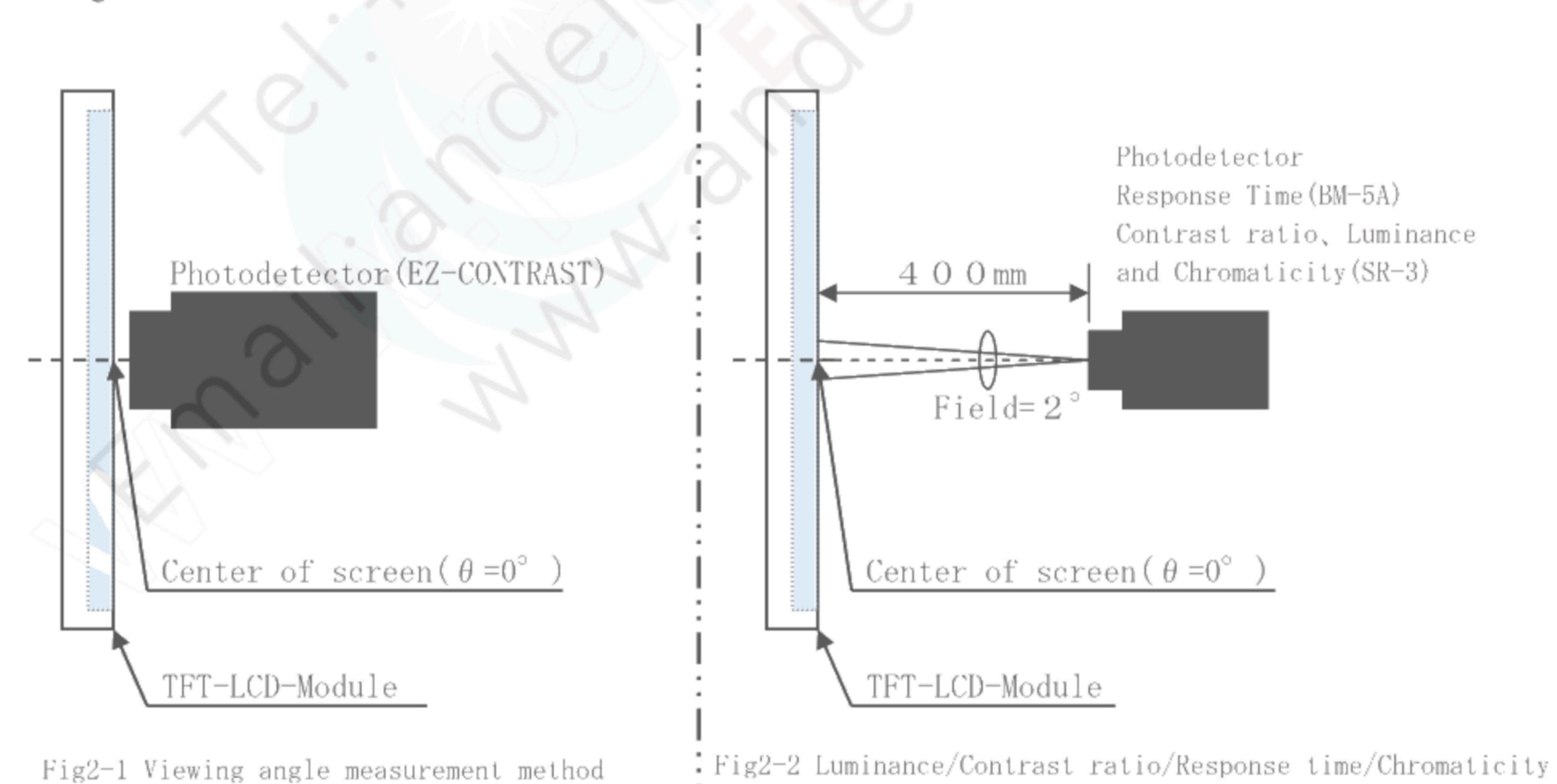
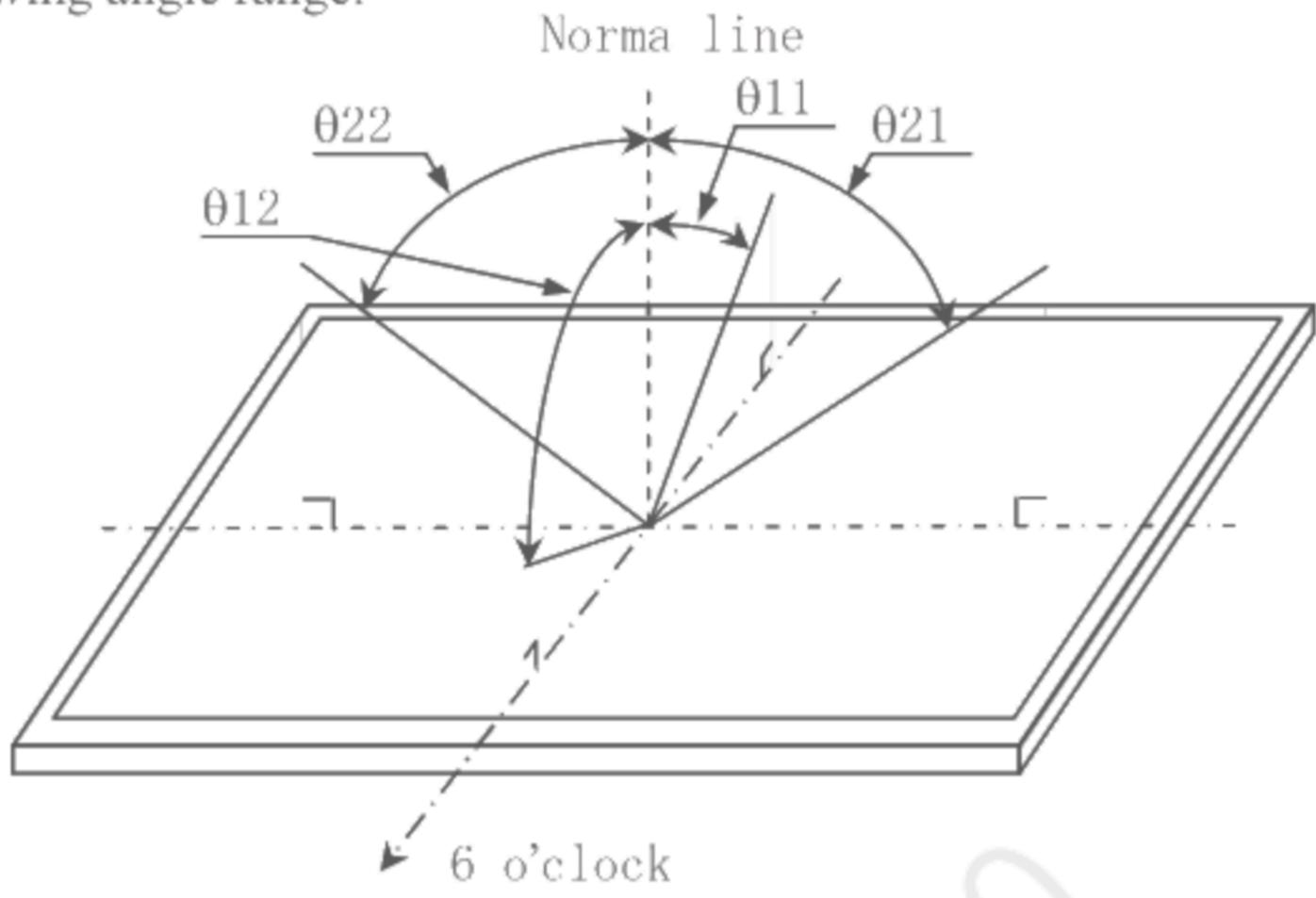


Fig2 Optical characteristics measurement method

measurement method

[Note1] Definitions of viewing angle range:



[Note2] Definition of contrast ratio:

The contrast ratio is defined as the following.

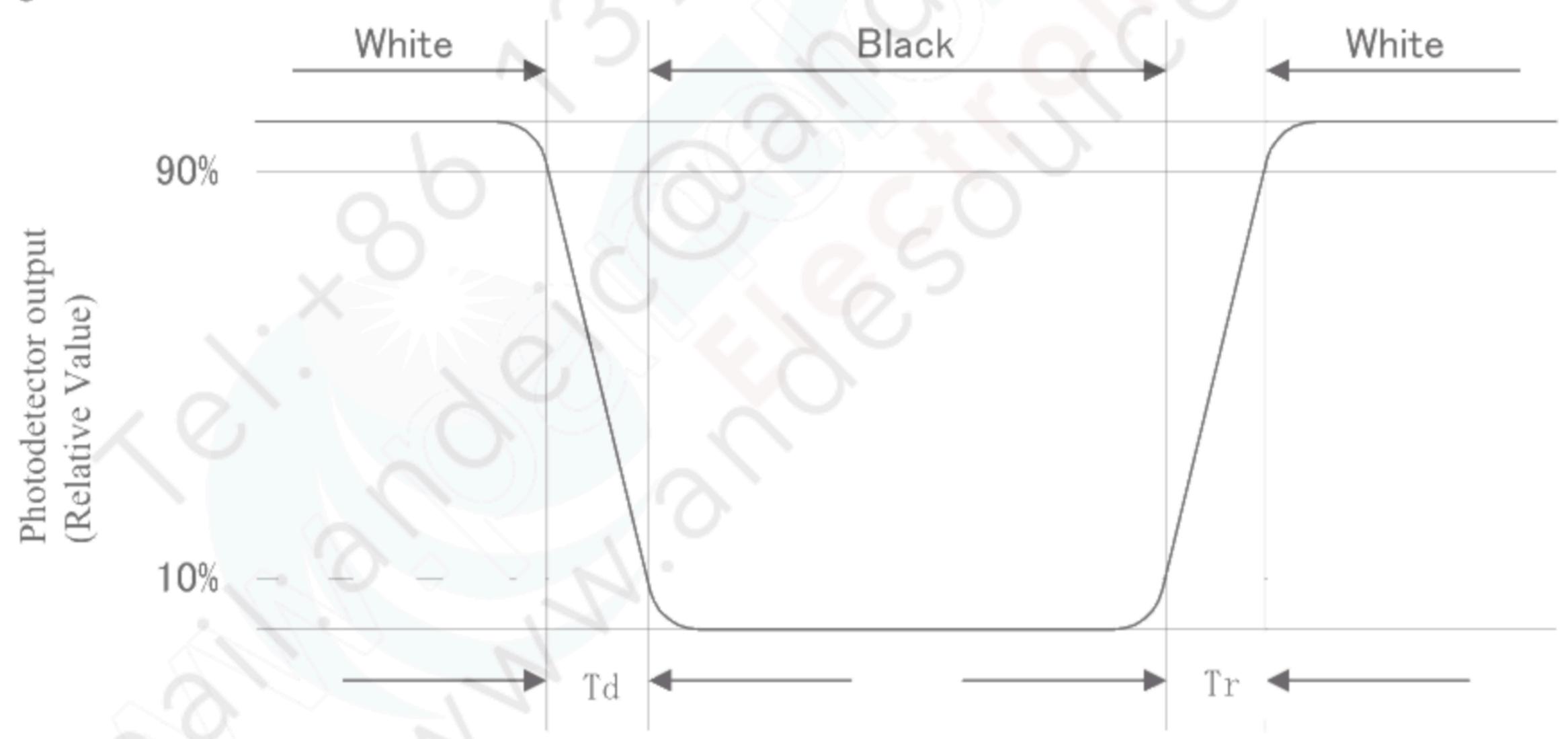
Contrast Ratio (CR) =

Luminance (brightness) with all pixels white

Luminance (brightness) with all pixels black

[Note3] Definition of response time:

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

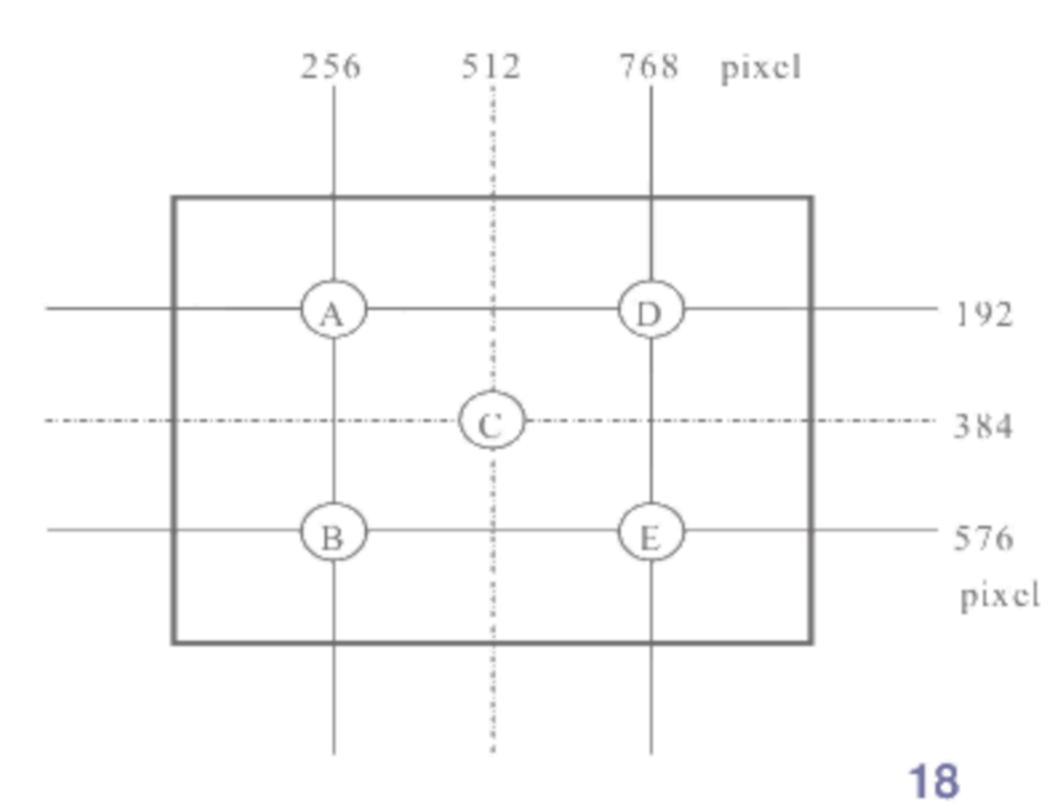


[Note4] This shall be measured at center of the screen.

[Note5] Definition of white uniformity:

White uniformity is defined as the following with five measurements

 $\delta w = \frac{\text{Maximum Luminance of five points (brightness)}}{\text{Minimum Luminance of five points (brightness)}}$



 $(A \sim E)$.

10. Handling Precautions

- a) Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.
- c) Since the front polarize is easily damaged, pay attention not to scratch it.
 - Blow away dust on the polarizer with antistatic N₂ blow. It is undesirable to wipe off because a polarizer is sensitive.
 - It is recommended to peel off softly using the adhesive tape when soil or finger oil is stuck to the polarizer.

 When unavoidable, wipe off carefully with a cloth for wiping lenses.
- d) Since long contact with water may cause discoloration or spots, wipe off water drop immediately.
- e) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- f) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
- g) Since CMOS LSI is used in this module, take care of static electricity and take the human earth into consideration when handling.
- h) Make sure the four mounting holes of the module are grounded sufficiently. Take electro-magnetic interference (EMI) into consideration.
- i) The module has some printed circuit boards (PCBs) on the back side. Take care to keep them form any stress or pressure when handling or installing the module; otherwise some of electronic parts on the PCBs may be damaged.
- j) Observe all other precautionary requirements in handling components.
- k) When some pressure is added onto the module from rear side constantly, it causes display non-uniformity issue, functional defect, etc. So, please avoid such design.
- When handling LCD modules and assembling them into cabinets, please be noted that long-term storage in the environment of oxidization or deoxidization gas and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion and discoloration of the LCD modules.
- m) If a minute particle enters in the module and adheres to an optical material, it may cause display nonuniformity issue, etc. Therefore, fine-pitch filters have to be installed to cooling and inhalation hole if you intend to install a fan.
- n) Protection film is attached to the module surface to prevent it from being scratched.
 Peel the film off slowly, just before the use, with strict attention to electrostatic charges.
 Blow off 'dust' on the polarizer by using an ionized nitrogen.
- o) The polarizer surface on the panel is treated with Anti-Glare for low reflection. In case of attaching protective board over the LCD, be careful about the optical interface fringe etc. which degrades display quality.
- p) Do not expose the LCD panel to direct sunlight. Lightproof shade etc. should be attached when LCD panel is used under such environment.
- q) There are high voltage portions on the backlight and very dangerous. Careless touch may lead to electrical shock. When exchange lamps or service, turn off the power without tail.
- r) Be careful of a back light lead not to pull by force at the time of the wiring to an inverter, or line processing.

- s) Liquid crystal contained in the panel may leak if the LCD is broken. Rinse it as soon as possible if it gets inside your eye or mouth by mistake.
- t) Never dismantle the module, because it will cause failure.
- adjusting volume have been set optimally before shipment, so do not change any adjusted value.
 If adjusted value is changed, the specification may not be satisfied.
- v) The lamp used for this product is very sensitive to the temperature.
 Luminance decreases rapidly when it is used for a long time or repeatedly under the environment of the low temperature or the module is being cooled.

Please avoid the continuous or repeating use of it under such an environment.

It may decrease up to 50% of the initial luminance in about one month under the low temperature environment. Please consult our company when it is used under the environment like the above mentioned.

11. Packing form

THE POTEST	
Product countries / Areas	TBD
Piling number of cartons	TBD
Package quantity in one carton	TBD
Carton size(TYP)	TBD
Total mass of one carton filled with full modules	TBD
Packing form is shown	TBD

12. Reliability test items

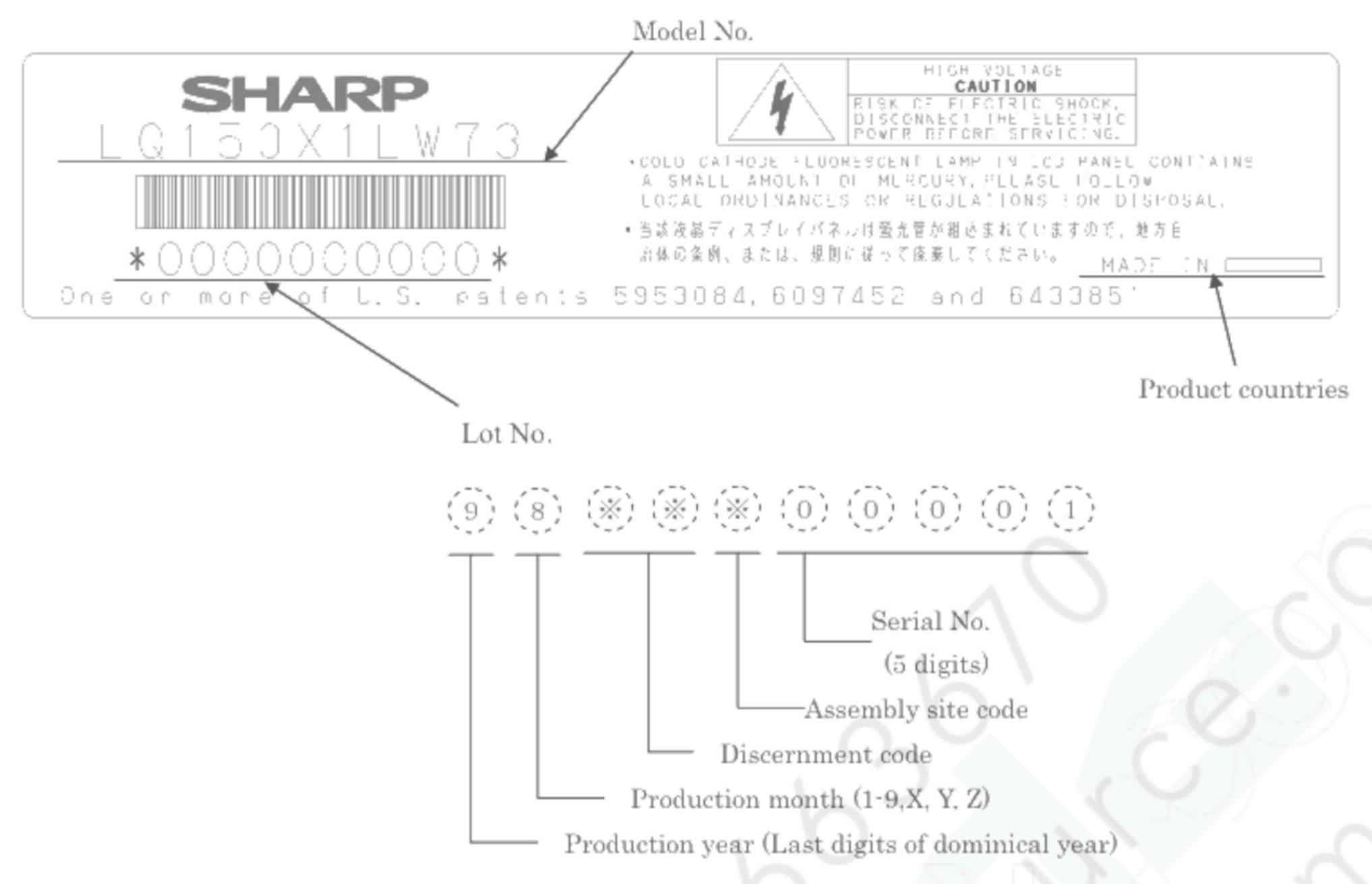
No.	Test item	Conditions
1	High temperature storage test	$Ta = 65^{\circ}C$ 240h
2	Low temperature storage test	Ta = -25°C 240h
3	High temperature	Ta = 40°C ; 95%RH 240h
	& high humidity operation test	(No condensation)
4	High temperature operation test	Tp = 60°C (panel surface) 240h
5	Low temperature operation test	$Ta = 0^{\circ}C$ 240H
6	Vibration test	Frequency: 10~57Hz/Vibration width (one side): 0.075mm
	(non- operating)	: 57~500Hz/Gravity : 9.8m/s ²
		Wave form: sin wave
		Sweep time: 11minutes
		Test period: 3 hours
		(1 hour for each direction of X,Y,Z)
7	Shock test	Max. gravity : 490m/s ²
	(non- operating)	Pulse width: 11ms, half-sine wave
		Direction: $\pm X$, $\pm Y$, $\pm Z$,
		once for each direction.

[Result Evaluation Criteria]

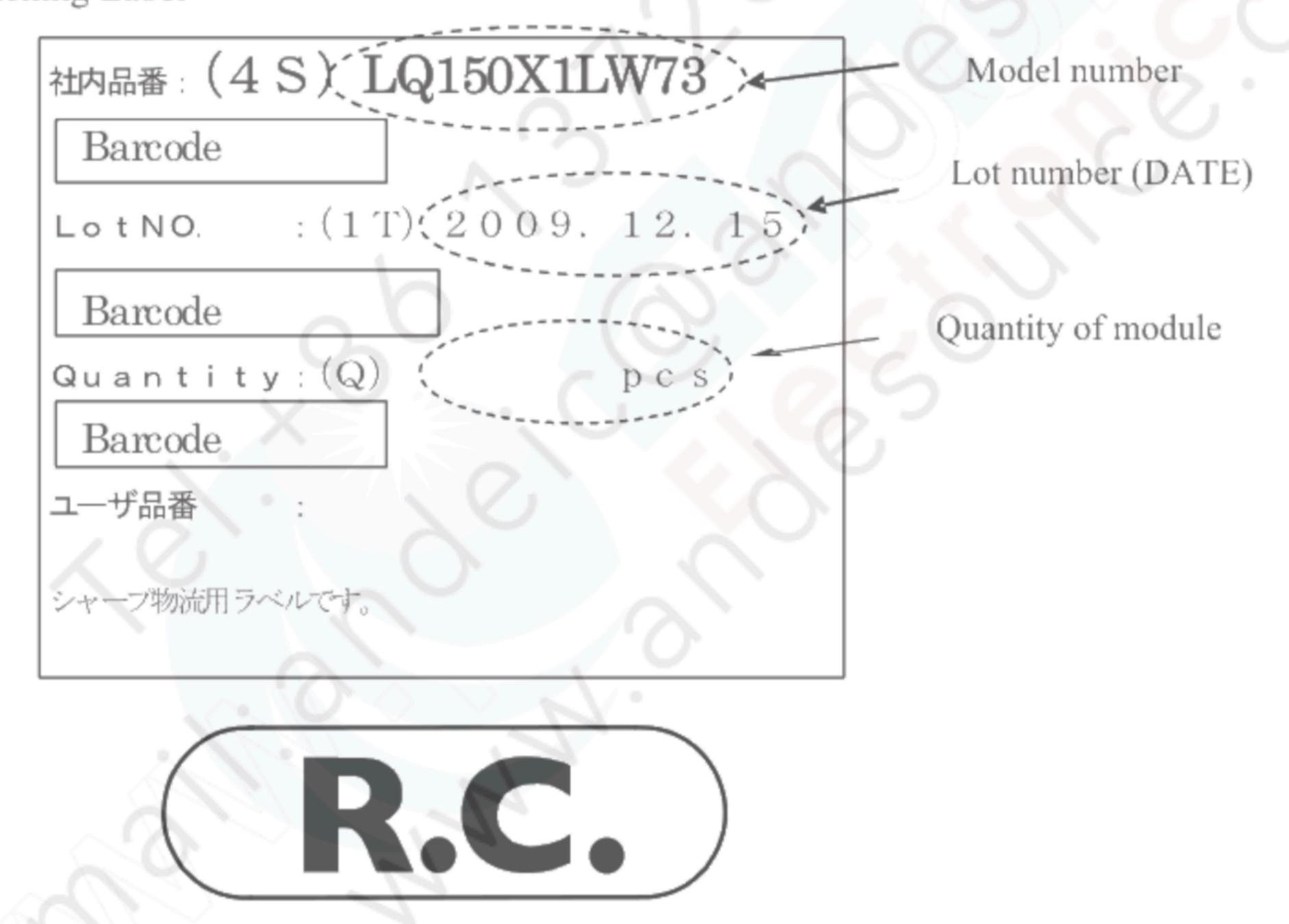
Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.

13. Others

1) Lot No. and indication Bar Code Label:



2) Packing Label



※R.C. (RoHs Compliance) means these parts have corresponded with the RoHs directive.

- 3) Adjusting volume have been set optimally before shipment, so do not change any adjusted value.
 - If adjusted value is changed, the technical literature may not be satisfied.
 - 4) Disassembling the module can cause permanent damage and should be strictly avoided.
 - 5) Please be careful since image retention may occur when a fixed pattern is displayed for a long time.
 - 6) The chemical compound which causes the destruction of ozone layer is not being used.

7)Cold cathode fluorescent lamp in LCD PANEL contains a small amount of mercury, Please follow local ordinances or regulations for disposal. (It describes in the label.)

COLD CATHOED FLUORESCENT LAMP IN LCD PANEL CONTAINS A SMALL AMOUNT OF MERCURY, PLEASE FOLLOW LOCAL ORDINANCES OR REGULATIONS FOR DISPOSAL 当該液晶ディスフレル ねは蛍光管が組込まれていますので、地方自治体の条例、または、規制に従って廃棄して下さい。

8) When any question or issue occurs, it shall be solved by mutual discussion.

14. Storage conditions

Temperature 0°C to 40°C
Humidity 95%RH or less

Reference condition: 20°C to 35°C, 85%RH or less (summer)

: 5°C to 15°C, 85%RH or less (winter)

• the total storage time (40°C,95%RH): 240H or less

Sunlight Be sure to shelter a product from the direct sunlight.

Atmosphere Harmful gas, such as acid and alkali which bites electronic components and/or

wires must not be detected.

Notes Be sure to put cartons on palette or base, don't put it on floor, and store them with

removing from wall

Please take care of ventilation in storehouse and around cartons, and control

changing temperature is within limits of natural environment

Storage period Within above mentioned conditions, maximum storage period should be one year.

