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SHARP

LIQUID CRYSTAL DISPLAY GROUP SHARP CORPORATION

SPECIFICATION

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REVISION: 20-Dec-11

DEVICE SPECIFICATION FOR

TFT-LCD Module

MODEL No.

LQ150X1LW94

These parts have corresponded with the RoHS

☐ CUSTOMER'S APPROVAL BY

BY For Ohnishi

GENERAL MANAGER

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DISPLAY DISPLAY DIVISION III
DISPLAY DEVICE BUSINESS GROUP

SHARP CORPORATION

RECORDS OF REVISION

LQ150X1LW94

		REVI			
SPEC No.	DATE	SED No	PAGE	SUMMARY	NOTE
LD-23Z04A	2011/12/20			_	

No.			page
1	Application		1
2	Overview		2
3	Mechanical Specifications		2
4	Input Terminals		3
5	Absolute Maximum Ratings	s	9
6	Electrical Characteristics		10
7	Timing characteristics of i	nput signals	12
8	Input Signals, Basic Displa	y Colors and Gray Scale of Each Color · ·	13
9	Optical Characteristics		15
10	Handling Precautions		17
11	Packing form		18
12	Reliability test items		18
13	Others		19
14	Storage conditions		20
Fig.1	OUTLINE DIMENSIONS		21
Fig.3	PACKING FORM		22

1. Application

This specification applies to the color TFT-LCD module LQ150X1LW94.

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In case of using the device for applications such as control and safety equipment for transportation (controls of aircraft, trains, automobiles, etc.), rescue and security equipment and various safety related equipment which require higher reliability and safety, take into consideration that appropriate measures such as fail—safe functions and redundant system design should be taken.

Do not use the device for equipment that requires an extreme level of reliability, such as aerospace applications, telecommunication equipment (trunk lines), nuclear power control equipment and medical or other equipment for life support.

SHARP assumes no responsibility for any damage resulting from the use of the device which does not comply with the instructions and the precautions specified in this specification.

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2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, control circuit, power supply circuit and a White-LED Backlight unit. Graphics and texts can be displayed on a 1024 × RGB × 768dots panel with about 16 million colors by using LVDS (Low Voltage Differential Signaling) and supplying +3.3V DC supply voltages for TFT-LCD panel driving and supply voltage for backlight.

The TFT-LCD panel used for this module is a high-brightness and high-contrast image.

The LED driver circuit is built into the module.

3. Mechanical technical literatures

Parameter	technical literatures	Unit
Display size	38 (15inch) Diagonal	cm
Active area	304.1 (H) × 228.1 (V)	mm
Divisit forms at A	1024(H) × 768(V)	
Pixel format	(1pixel=R+G+B dot)	pixel
Aspect ratio	4:3	
Pixel pitch	0.297(H) × 0.297(V)	mm
Pixel configuration	R,G,B vertical stripe	
Display mode	Normally black	
Unit outline dimensions [*1]	326.5 (W) × 253.5 (H) × 9.0(D)	mm
Mass	950 (Max.)	g
Surface treatment	Anti-glare and hard-coating 3H	

[*1] Excluding the protrusion of the connector cover from thickness.

Outline dimensions are shown in Fig.1.

4. Input Terminals

4-1. TFT-LCD panel driving

CN1 (Interface signals and +3.3V power supply)

Using connectors: DF14-30P-1.25(56) (Hirose Electric Co., Ltd.)

Corresponding connectors: DF14-30S-1.25C (Hirose Electric Co., Ltd.)

:DF14-2628SCF (Hirose Electric Co., Ltd.)

Using LVDS receiver: Building into cotroll IC(THC63LVDF84B(Thine electronics) or Compatible product)

Corresponding LVDS transmitter: THC63LVDM83R(Thine electronics) or Compatible product

CN1

Pin	Symbol	Function	Remark
1	GND	GND	
2	GND	GND	
3	VDD	+12V Power supply	_ Z-2 N
4	VDD	+12V Power supply	
5	GND	GND	
6	XSTABY	Backlight ON/OFF signal	【*1】
7	VBR	PWM signal	*1]
8	GND	GND	
9	VCC	+3.3V Power supply	
10	VCC	+3.3V Power supply	
11	GND	GND	
12	GND	GND	
13	RxIN0-	LVDS receiver signal CH0 (-)	LVDS
14	RxIN0+	LVDS receiver signal CH0 (+)	LVDS
15	GND	GND	
16	RxIN1-	LVDS receiver signal CH1 (-)	LVDS
17	RxIN1+	LVDS receiver signal CH1 (+)	LVDS
18	GND	GND	
19	RxIN2-	LVDS receiver signal CH2 (-)	LVDS
20	RxIN2+	LVDS receiver signal CH2 (+)	LVDS
21	GND	GND	
22	CK IN-	LVDS receiver signal CK (-)	LVDS
23	CK IN+	LVDS receiver signal CK (+)	LVDS
24	GND (GND	
25	RxIN3-	LVDS receiver signal CH3 (-)	LVDS
26	RxIN3+	LVDS receiver signal CH3 (+)	LVDS
27	GND	GND	
28	RL/UD	Horizontal/Vertical display mode select signal	【*2】
29	SELLVDS	LVDS SET	[*3]
30	GND	GND	

[*1] XSTABY, VBR is shown in 6-2.

[*2] RL/UD = LOW

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RL/UD = HIGH

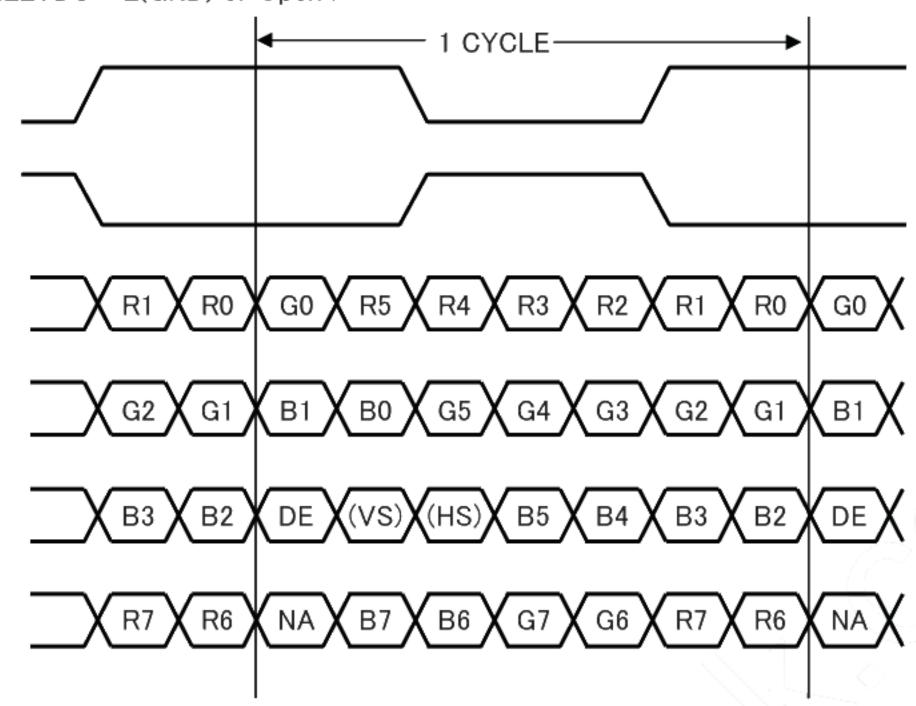


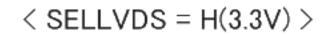
4-2. Data Mapping

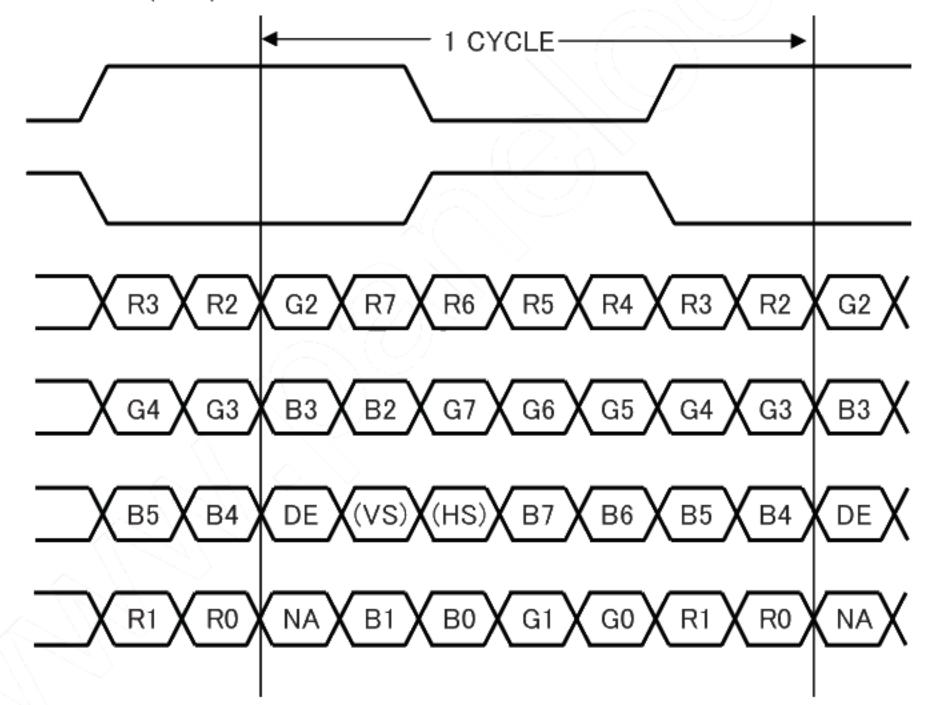
1) 8 bit input

pin assignment with SELLVDS pin (THC63LVDM83R(Thine electronics) or Compatible product)

Transmitter		20Pin SELLVDS								
Pin No	Data	= L(GND) or Open	= H(3.3V)							
51	TA0	R0 (LSB)	R2							
52	TA1	R1	R3							
54	TA2	R2	R4							
55	TA3	R3	R5							
56	TA4	R4	R6							
3	TA5	R5	R7 (MSB)							
4	TA6	G0 (LSB)	G2							
6	ТВ0	G1	G3							
7	TB1	G2	G4							
11	TB2	G3	G5							
12	ТВ3	G4	G6							
14	TB4	G5	G7 (MSB)							
15	TB5	B0 (LSB)	B2							
19	TB6	B1	В3							
20	TC0	B2	B4							
22	TC1	В3	B5							
23	TC2	B4	B6							
24	TC3	B5	B7 (MSB)							
27	TC4	(HS)	(HS)							
28	TC5	(VS)	(VS)							
30	TC6	DE	DE							
50	TD0	R6	R0 (LSB)							
2	TD1	R7 (MSB)	R1							
8	TD2	G6	G0 (LSB)							
10	TD3	G7 (MSB)	G1							
16	TD4	B6	B0 (LSB)							
18	TD5	B7 (MSB)	B1							
25	TD6	(NA)	(NA)							







DE: DATA ENABLE

HS: Hsync

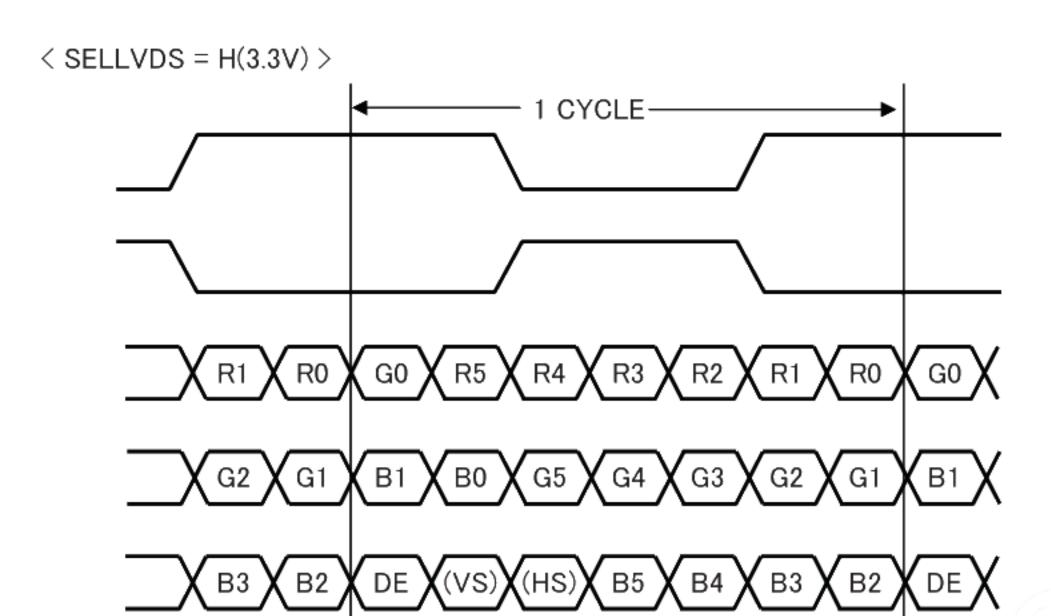
VS:Vsync

NA: Non Available

2) 6bit input

pin assignment with SELLVDS (THC63LVDM83R(Thine electronics) or Compatible product)

Transmitter		20Pin S	ELLVDS
Pin No	Data	= L(GND) or Open	= H(3.3V)
51	TA0	_	R0 (LSB)
52	TA1	_	R1
54	TA2	_	R2
55	TA3	_	R3
56	TA4	_	R4
3	TA5	_	R5 (MSB)
4	TA6	_	G0 (LSB)
6	ТВ0	_	G1
7	TB1	_	G2
11	TB2	_	G3
12	ТВ3	_	G4
14	TB4		G5 (MSB)
15	TB5	- < ((B0 (LSB)
19	ТВ6		B1
20	TC0	((//))	B2
22	TC1		В3
23	TC2		B4
24	тс3	5/A>>-	B5 (MSB)
27	TC4	_	(HS)
28	TC5	<u>-</u>	(VS)
30	TC6	> -	DE
50	TD0	_	GND
2	TD1	_	GND
8	TD2	_	GND
10	TD3	_	GND
16	TD4	_	GND
18	TD5	_	GND
25	TD6	_	(NA)



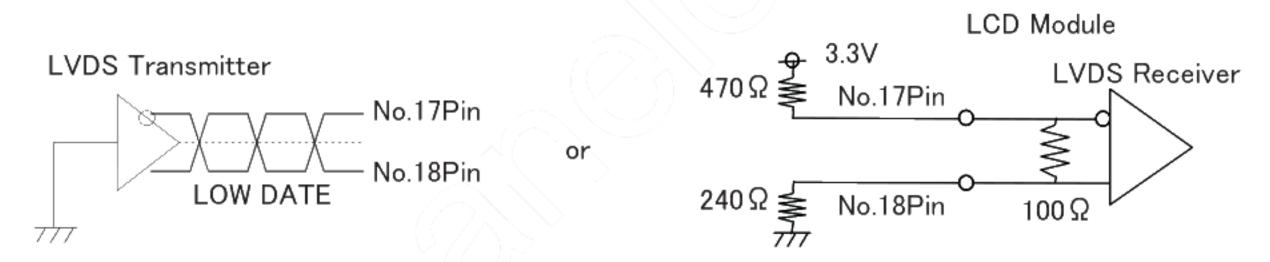
DE: DATA ENABLE

HS: Hsync

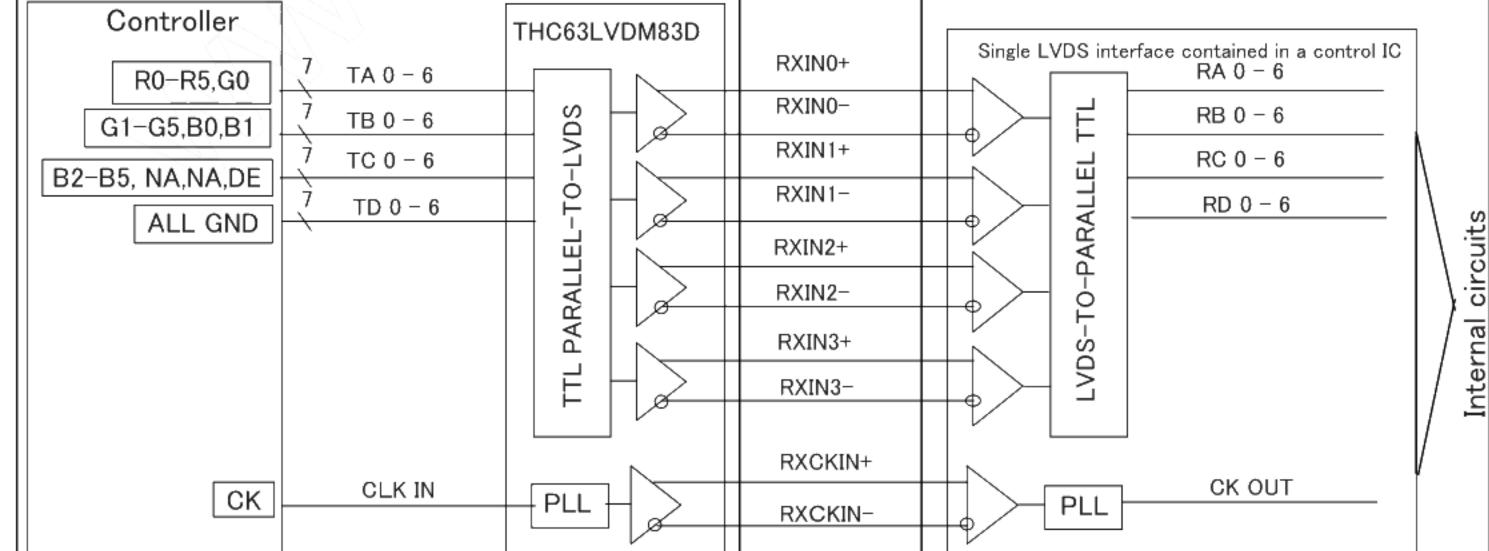
VS:Vsync

NA: Non Available

Recommended input (17pin, 18pin at 6bit)



4-4. Interface block diagram (Computer Side) (TFT-LCD side) 18 bit input SELLVDS = L(GND) or Open Controller THC63LVDM83D Single LVDS interface contained in a control IC RXIN0+ TA 0 - 6 RA0 - 6R0-R5,G0 RXIN0-PARALLEL-TO-LVDS RB 0 - 6 TB 0 - 6 G1-G5,B0,B1 RXIN1+ RC 0 - 6 TC 0 - 6 LVDS-TO-PARALLEL B2-B5, NA,NA,DE RXIN1-RD 0 - 6TD 0 - 6 Internal circuits R6,R7,G6,G7, RXIN2+ B6,B7,NA RXIN2-RXIN3+ RXIN3-RXCKIN+ CK OUT CLK IN CK PLL RXCKIN-28 bit input SELLVDS = H(3.3V)Controller THC63LVDM83D Single LVDS interface contained in a control IC RXIN0+ RA 0 - 6TA 0 - 6 R2-R7,G2 RXIN0--TO-LVDS RB 0 - 6 TB 0 - 6 G3-G7,B2,B3 RXIN1+ RC 0 - 6 TC 0 - 6 S-TO-PARALLEL B4-B7, NA,NA,DE RXIN1-RD 0 - 6 TD 0 - 6 R0,R1,G0,G1, nal circuits PARALLEL RXIN2+ B0,B1,NA RXIN2-RXIN3+ RXIN3-RXCKIN+ CK OUT CLK IN CK PLL **PLL** RXCKIN-36 bit input SELLVDS = H(3.3V)Controller THC63LVDM83D Single LVDS interface contained in a control IC RXIN0+ RA0 - 6TA 0 - 6 R0-R5,G0 RXIN0-RB 0 - 6 TB 0 - 6 G1-G5,B0,B1



5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Pin	Ratings	Unit	Remark
Supply voltage	Vcc	Ta=25°C	VCC	−0.3 ~ +4.0	٧	【*1,2】
	VDD	Ta=25°C	VDD	−0.3 ~ +15.0	V	【*1,2】
Tonovit violetorio	V	Ta=25°C	RxINi-/+	-0.2\/ +0.2	V	:-0.1.0.0
	V _{I 1}	1a-25 C	CK IN-/+	-0.3∼Vcc+0.3	V	i=0,1,2,3
Input voltage	V _{I 2}	Ta=25°C	RL/UD,SELLVDS	-0.3∼Vcc+0.3	٧	
	V _{I 4}	Ta=25°C	XSTABY, VBR	-0.3∼V _{DD}	٧	
Storage temperature	T _{STG}	_	-	−25 ~ +65	°C	[*1]
Operating temperature	T _{OPA}	_	_	0 ~ +60	°C	[*1,3,4]

[*1] Humidity:95%RH Max.(Ta≤40°C) Note static electricity.

Maximum wet-bulb temperature at 39°C or less. (Ta>40°C) No condensation.

[*2] The Vcc power supply capacity must use the one of 2A or more.

The Vcc power supply capacity must use the one of 3A or more.

[*3] There is a possibility of causing deterioration in the irregularity and others of the screen and the display fineness though the liquid crystal module doesn't arrive at destruction when using it at 60°C.

There is a possibility of causing the fineness deterioration by the prolonged use in the (high temperature) humidity environment (60% or more).

[*4] In the operating temperature item, the low temperature side is the ambient temperature regulations.

The high temperature side is the panel surface temperature regulations.

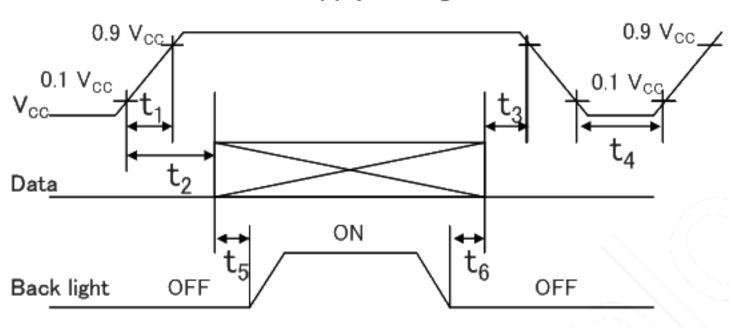
6. Electrical Characteristics

6-1. TFT-LCD panel driving

$T_a = +25$ °(

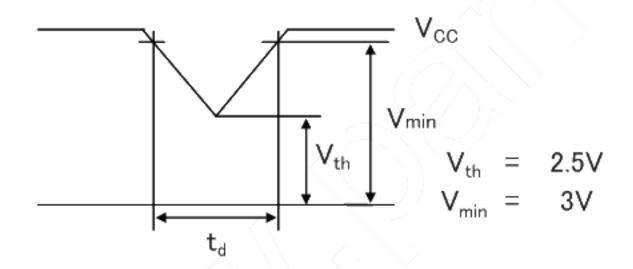
Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark	
Supply voltag	е	V _{cc}		3.0	3.3	3.6	٧	[*1]
Current dissipat	tion	I_{CC}	Vcc=3.3V	_	480	900	mA	[*2]
Input voltage width for LVDS	receiver	V _L		0	_	2.4	٧	
Permissive input ripple voltage		V_{RP}		_	_	200	mV _{P-P}	Vcc = 3.3V
Differential input	High	V _{TH}		_	_	V _{CM} +100	mV	V _{CM} =+1.2V
Threshold voltage	Low	V _{TL}		V _{CM} -100	_	_	mV	[*3]
Innut valtage		V _{IH}		2.1	_	_	٧	[*4]
Input voltage	;	V _{IL}		_	_	0.8	٧	
Innut wook ouws	o.n.t	I _{OH}		_	_	400	μА	V ₁₂ = +3.3V[*4]
Input reak current		I _{OL}		-10	_	+10	μА	V ₁₂ =0V [*4]
Terminal resist	or	R_T			100	_	Ω	Differential input

[*1] On-off conditions for supply voltage



 $20 \, \mu \, \text{s} < t_1 \leq 10 \, \text{ms}$ $0 < t_2 \leq 20 \, \text{ms}$ $0 < t_3 \leq 1 \, \text{s}$ $1 \, \text{s} \leq t_4$ $300 \, \text{ms} \leq t_5$ $200 \, \text{ms} \leq t_6$

Vcc-dip conditions



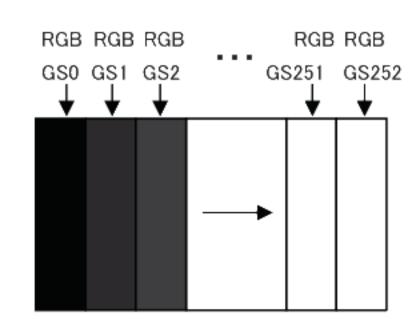
- $\begin{array}{cccc} \cdot & V_{\text{th}} & < V_{\text{CC}} \leqq & V_{\text{min}} \\ & & t_{\text{d}} & \leqq & 10 \text{ms} \end{array}$
- \cdot V_{CC} < V_{th}

Vcc-dip conditions should also follow the On-off conditions for supply voltage

- Hsync/Vsync need not be input so that this model may drive only by the ENAB signal.
 Even if Hsync/Vsync is input, it doesn't become a malfunction.
- The relation between the data input and the backlight lighting will recommend the above-mentioned input sequence.
 When the backlight is turned on before the panel operates, there is a possibility of abnormally displaying.
 The liquid crystal module is not damaged.

[*2] Current dissipation

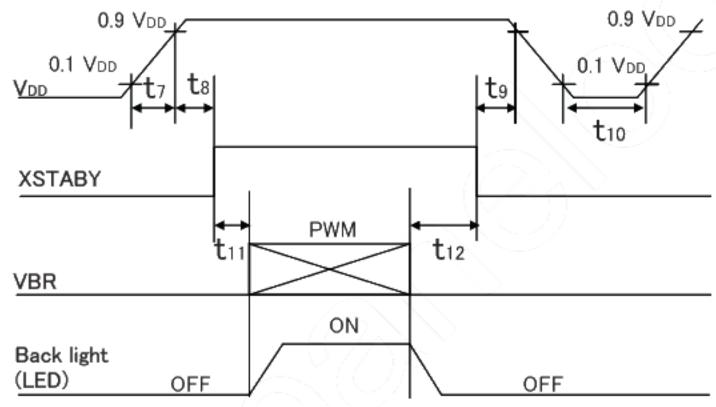
Typical current situation : 253-gray-bar pattern (Vcc=+3.3V, fck = 65MHz, Ta=25°C)



- [*3] V_{CM}: LVDS common mode voltage
- [*4] RL/UD, SELLVDS

Para	meter	Symbol	Min.	Тур.	Max.	Unit	Remark
Supply	voltage	V _{DD}	10.2	12.0	13.8	٧	【*1】
Commont	lianimation	I _{DD1}	_	680	1000	mA	【*2】
Current dissipation		I _{DD2}	_	_	10.0	μΑ	
Permissive inpu	ut ripple voltage	VRP_BL	_	_	200.0	mV _{P-P}	VDD=+12.0V
VCTADV	High voltage	VIH_BL1	2.4	_	VDD	٧	[*3]
XSTABY	Low voltage	VIL_BL1	_	_	0.4	٧	[*3]
VDD	High voltage	VIH_BL2	2.1	_	VDD	٧	[*4]
VBR	Low voltage	VIL_BL2	_	_	0.4	٧	【*4】
PWM fr	equency	fрwм	50.0	_	1k	Hz	【*4,5】
PWM	l duty	Dрwм	1.0	_	100.0	%	【*4,5】
Life time		L	_	(50,000) (Module)	_ (h	【Reference】 【*6】

[*1] On-off conditions for supply voltage



$$20 \,\mu\,\mathrm{s} \leq t_7 \leq 200 \mathrm{ms}$$

 $0ms \le t8$

 $0ms \le t_9$

200ms ≤ t10

10ms ≦ t₁₁

 $0ms \leq t_{12}$

t₁₄

[*2] Current dissipation

Typ. value: VDD= +12V, Duty=100%

Max. value: VDD= +10.2V, Duty=100%

- [*3] XSTABY is connected by the pull-down resistor of $10k\Omega$.
- [*4] VBR is connected by the pull-down resistor of $10k\Omega$.

[*5] PWM

 $fPWM = 1/t_{14}$

Duty 1%: Min. Luminance

Duty 100%: Max. Luminance

Luminance changes in proportion to the duty ratio. (t13 \geq 200 μ s)

VBR

When the frequency slows, the display fineness might decrease.

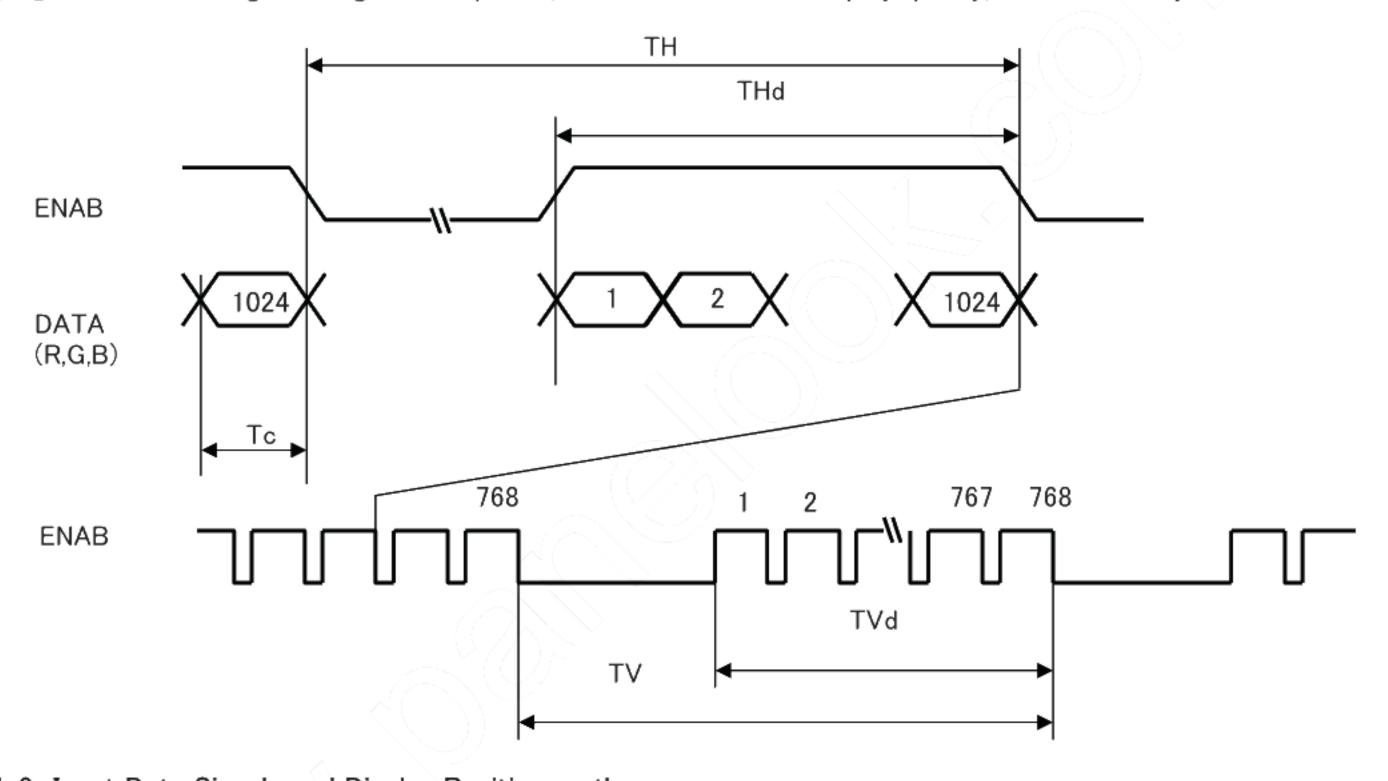
[*6] Luminance becomes 50% of an initial value. (Ta=25°C, PWM=100%)

7. Timing characteristics of input signals

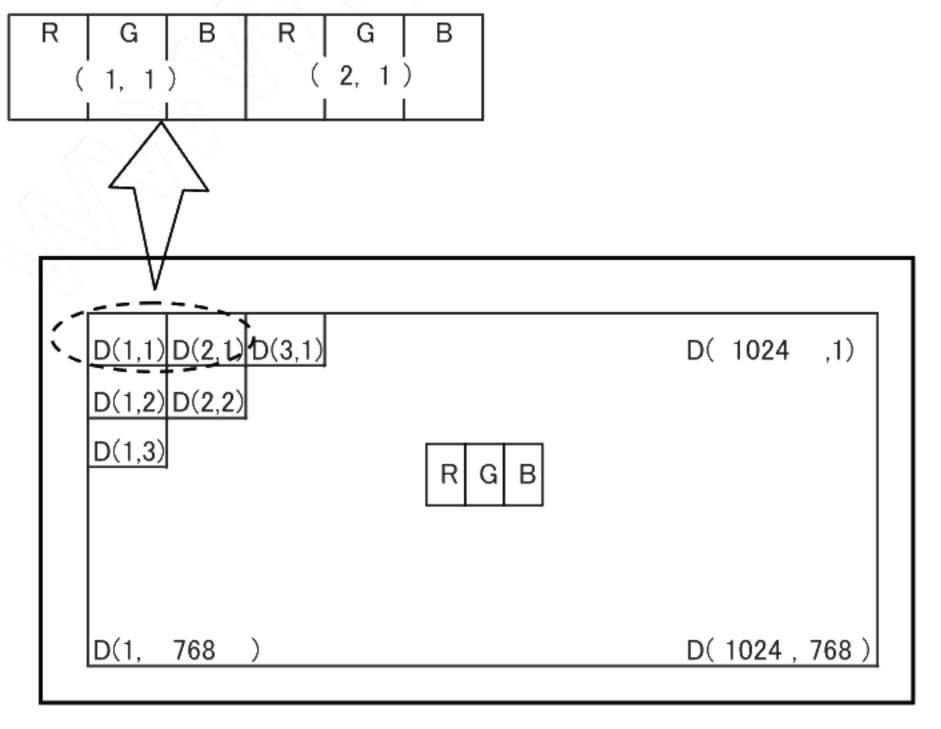
7-1. Timing characteristics

P	arameter	Symbol	Min.	Тур.	Max.	Unit	Remark		
Clock	Frequency	1/Tc	50.0	65	80.0	MHz			
	Harizantal nariad	TH	1094	1344	1720	clock			
	Horizontal period	III	16.0	20.7	26.4	μs			
ENAB	Horizontal period (High)	THd	1024	1024	1024	clock			
LINAD	Vartical Evanuaria	TV	776	806	990	line	[*1]		
	Vertical Frequency	1 V	13.3	16.7	20.5	ms	L*11		
	Vertical period (High)	TVd	768	768	768	line			

[*1] In case of using the long vertical period, the deterioration of display quality, flicker etc. may occur.



7-2. Input Data Signals and Display Position on the screen



8. Input Signals, Basic Display Colors and Gray Scale of Each Color

8-1. 8 bit input

		Data signal																								
	Colors & Gray scale	Gray Scale	R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	В0	В1	B2	ВЗ	В4	B5	В6	В7
	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1
<u> </u>	Green	_	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Color	Cyan	-	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1
asic	Red	_	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
m	Magenta	_	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0	Х	Х	1	1-	<u>(1</u>	1	1	1
	Yellow	_	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1	0	0	0(0	0	0	0	0
	White	-	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1	Х	Х	\overline{A}	1	্ৰ	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ہ ا	1	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
f Red	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0 ,	0	0	0	0	0	0	0	0	0	0
le of	1	1					1								1				>				1			
Scale	Į.	1	1								4								1							
Gray	Brighter	GS250	0	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	GS251	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS252	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
en	1	GS1	0	0	0	0	0	0	0	0	4.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Green	Darker	GS2	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
e of	1	1													1								Î			
Scale	ţ	1	L		_/				<u> </u>						ļ								ļ			
Gray (Brighter	GS250	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0
g	ţ	GS251	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0
Ш	Green	GS252	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
e e	1	GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
of Blue	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
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l o		1	L,				ļ				L				ļ				<u> </u>				ļ			\square
Gray	Brighter	GS250	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1
	1	GS251	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1
	Blue	GS252	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1

0 :Low level voltage 1 :High level voltage X :Don't care

Each basic color can be displayed in 253 gray scales from 8 bit data signals. According to the combination of, total 24 bit data signals, the 16-million-color display can be achieved on the screen.

	Colors &	Data signal																		
	Gray scale	GrayScale	R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	В0	В1	B2	ВЗ	В4	B5
Basic Color	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	_	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Green	_	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Cyan	_	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Red	-	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	-	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of Red	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Î	1	1					1												
	ļ	ļ	1					1					1							
	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	1	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
eu	1	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
Green	Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
of of	Î	1	4					↓						1						
Scale	ļ	\downarrow						↓						ļ						
Gray S	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
	ļ	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Gray Scale of Blue	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
	1	1	1					1						1						
	l l	Ţ	<u> </u>					↓					↓							
	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
	<u></u>	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.

Parameter		Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Viewing	Horizontal	θ 21, θ 22		70	85	_	Deg.	
angle	Vautiaal	θ11	CR>10	70	85	_	Deg.	【*1,2,4】
range	Vertical	θ 12		70	85	_	Deg.	
Contra	ist ratio	CR	optimized angle	900	1500	-		【*2,4】
Response Time	White Black	τr+τd		_	35	-	ms	【*3,4】
Chromaticity of White		Wx] [0.250	0.300	0.350		
		Wy] [0.275	0.325	0.375		
Chromaticity of		Rx] [_	0.635	-	6	
	ed	Ry] [_	0.345	-)) _[104]
Chroma	aticity of	Gx	θ =0°	_	0.318	-		【*4】
Green		Gy	0 -0	_	0.625	<u>7</u>		
Chromaticity of Blue		Bx] [_	0.150	\ \ <u></u>	<i>></i>	
		Ву] [_	0.069	7/-/		
Luminand	e of white	Y _{L1}		240	300	<u> </u>	cd/m²	[*4]
White Uniformity				-		1.33		【*5】

*The measurement shall be executed 30 minutes after lighting at rating.

The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig.2 below.

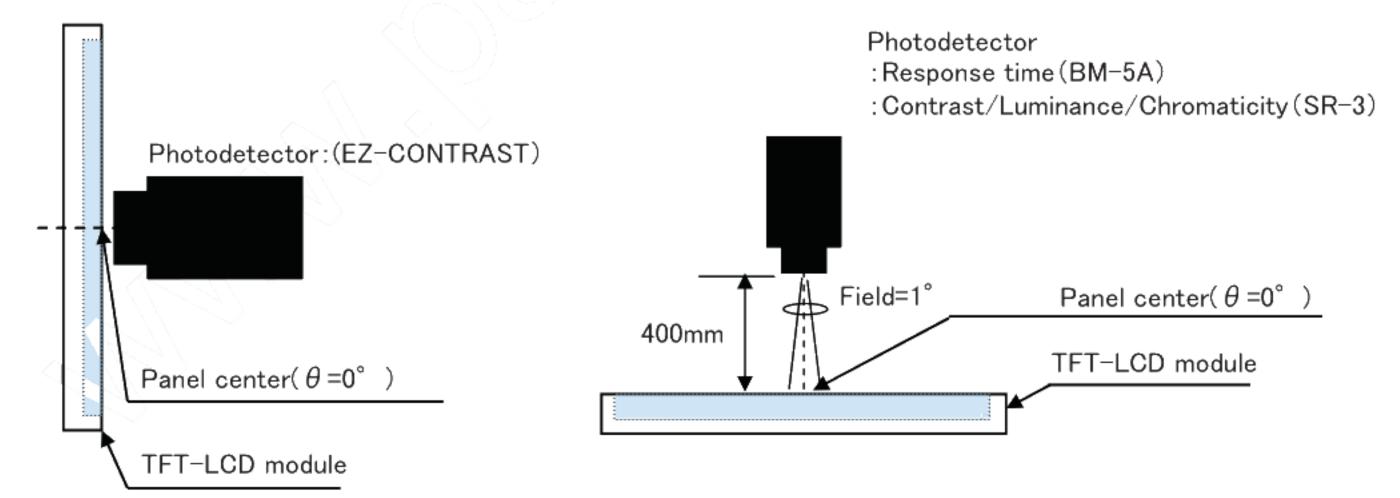
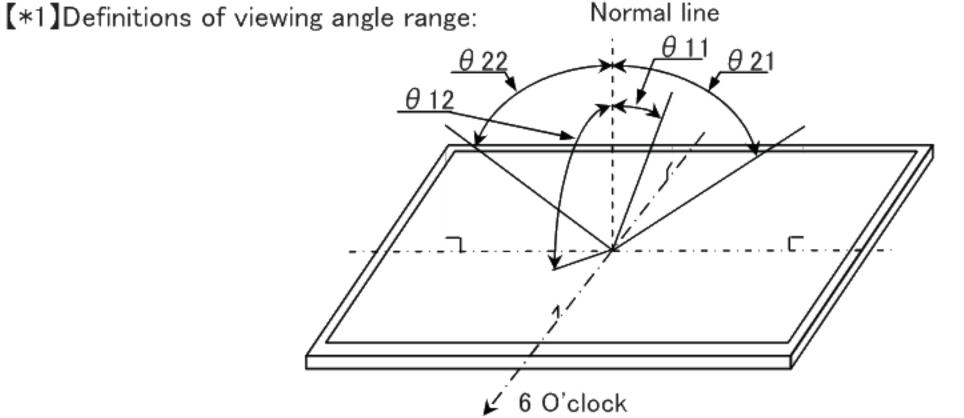


fig.2-1 Measuring method of Viewing angle range.

fig.2-2 Measuring method of contrast, luminance, response time, and Chromaticity.

Fig.2 Optical characteristics measurement method

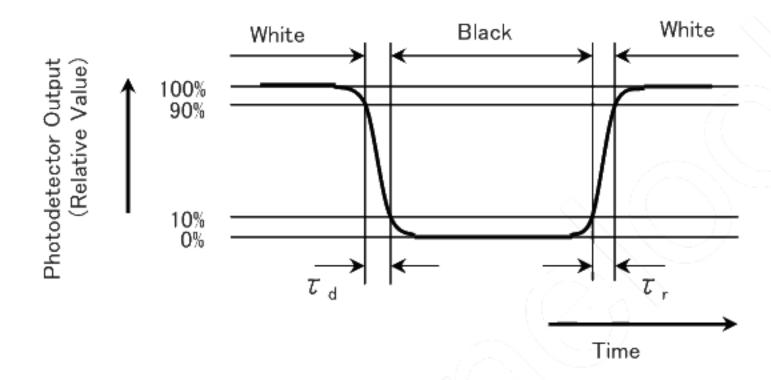


[*2]Definition of contrast ratio:

The contrast ratio is defined as the following. Contrast (CR) = Luminance with all pixels white Luminance with all pixels black

[*3]Definition of response time:

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

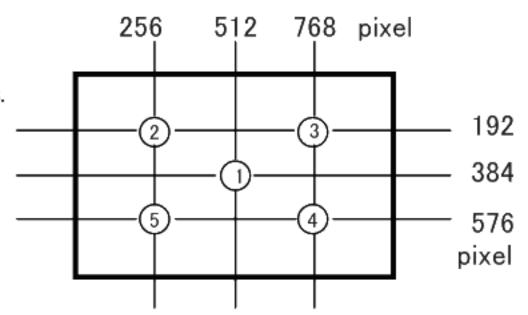


[*4] This shall be measured at center of the screen.

[*5] Definition of white uniformity:

White uniformity is defined as the following with five measurements. $(1\sim5)$

 $\delta_{\rm w} = \frac{{\rm Maximum\ luminance\ of\ 5\ points(1) \sim 5)}}{{\rm Minimum\ luminance\ of\ 5\ points(1) \sim 5)}}.$



10. Handling Precautions

- a) Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Since the front polarizer is easily damaged, pay attention not to scratch it.
- c)Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- d) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- e) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
- f) Since CMOS LSI is used in this module, take care of static electricity and injure the human earth when handling. Observe all other precautionary requirements in handling components.
- g) Since there is a circuit board in the module back, stress is not added at the time of a design assembly. Please make it like. If stress is added, there is a possibility that circuit parts may be damaged.
- h) It causes an irregular display and the defective indication, etc., when always put constant pressure on the back of the module.
 - Please do not make the structure to press the back of the module.
- i) Do not expose the LCD panel to direct sunlight. Lightproof shade etc. should be attached when LCD panel is used under such environment.
- j) Connect GND to stabilize against EMI and external noise.
- k) When handling LCD modules and assembling them into cabinets, please avoid that long-terms storage in the environment of oxidization or deoxidization gas and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion and discoloration of the modules. Do not use the LCD module under such environment.
- Liquid crystal contained in the panel may leak if the LCD is broken. Rinse it as soon as possible
 if it gets inside your eye or mouth by mistake.
- m) Be careful when using it for long time with fixed pattern display as it may cause accidential image.
- n) Adjusting volume have been set optimally before shipment, so do not change any adjusted value.

 If adjusted value is changed, the specification may not be satisfied.
- o) If a minute particle enters in the module and adheres to an optical material, it may cause display non-uniformity issue, etc. Therefore, fine-pitch filters have to be installed to cooling and inhalation hole if you intend to install a fan.
- p) The polarizer surface on the panel is treated with Anti-Glare for low reflection. In case of attaching protective board over the LCD, be careful about the optical interface fringe etc. which degrades display quality.
- q) Notice: Never take to pieces the module, because it will cause failure.
 Please do not peel off the Black tape pasted to the product.
- r) An abnormal display by changing in quality of the polarizing plate might occur regardless of contact or no contact to the polarizing plate, because of epoxy resin (amine system curing agent) that comes out from the material and the packaging material used for the set side, the silicon adhesive (dealcoholization system and oxime system), and the tray blowing agents (azo-compound), etc. Please confirm adaptability with your employed material.

11. Packing form

a) Piling number of cartons : MAX. 6

b) Package quantity in one carton: 10pcs

c) Carton size(TYP): $450 \text{mm}(W) \times 312 \text{mm}(D) \times 400 \text{mm}(H)$

d) Total mass of one carton filled with full modules(10pcs): 9.8kg

12. Reliability test items

No.	Test item	Conditions	Remark
1	High temperature storage test	Ambient temperature 65°C 240H	[Note1]
2	Low temperature strage test	Ambient temperature -25°C 240H	[Note1]
3	High temperature & high humidity operation test	Ambient temperature 40°C, Humidity 95% RH 240H (No condensation.)	[Note1]
4	High temperature operation test	Panel surface 60°C 240H	[Note1]
5	Low temperature operation test	Ambient temperature 0°C 240H	【Note1】
6	Vibration test (non-operating)	<pre> <sin wave=""> Frequency : 10~57Hz/Vibration width (one side) : 0.076mm</sin></pre>	【Note1】
7	Shock test (non-operating)	Max. gravity: 490m/s2 Pulse width: 11ms Direction: ±X,±Y,±Z Test period: 1time/1direction	【Note1】
8	Thermal shock test (non-operating)	-25°C[0.5h]~65°C[0.5h]∕50cycles	【Note1】

[Note1] Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function. (normal operation state: Temperature:15~35°C, Humidity:45~75%, Atmospheric pressure:86~106kpa)

13. Others

13-1. Lot No Label:

A) Module serial label

The label that displays SHARP·Model No.(LQ150X1LW94)·Lot No. is stuck on the back of the module.

SHARP
LQ150X1LW94

Bar Code(Lot No.)

Lot No display method(Figure and alphabet)

Production year
(Last digit of dominical year)

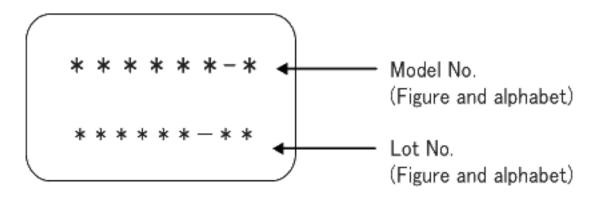
Serial No.

Production month (1-9X, Y, Z)

Discernment code

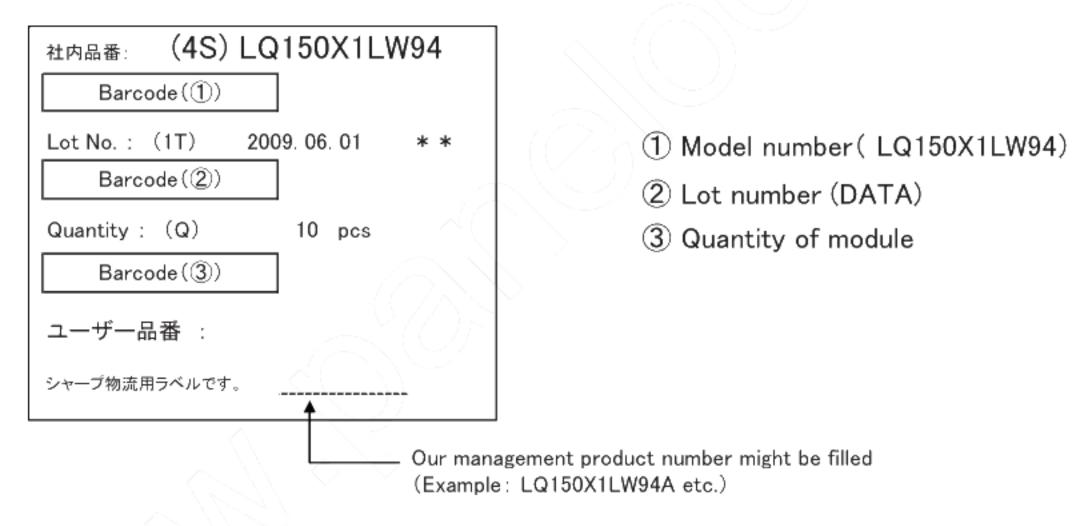
B) Backlight serial label

The label that displays the model No. and lot No. for the backlight is stuck on the back of the module.



13-2. Packing box Label:

The label that displays ①Model number(LQ150X1LW94) ②Lot number ③Quantity of module is stuck on the packing box. Moreover, the display of bar code also applies to this.

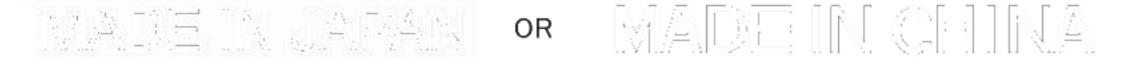


A right picture is written to the packing box of module for the RoHS restriction.

* R.C. (RoHs Compliance) means these parts have corresponded with the RoHs directive. This module corresponds from the first sample to RoHS Directive.



The figure below is written under the SHARP logo of the packing box about the production country.



- 13-3. The ozone-depleting substances is not used.
- 13-4. If any problem occurs in relation to the description of this specification, it shall be resolved through discussion with spirit of cooperation.

14. Storage conditions

Environmental condition range of storage temperature and humidity

Temperature 0 to 40 degrees Celsius

Relative humidity 95% and below

[Note] Please refer below as a mean value of the environmental conditions.

Summer time temperature 20 to 35 degrees Celsius humidity, 85% and below

Winter time temperature 5 to 15 degrees Celsius humidity, 85% and below

Please maintain within 240 hours of accumulated length of storage time, with conditions of 40 degrees Celsius and room humidity of 95%.

Direct sun light

Please keep the product in a dark room or cover the product to protect from direct sun light.

Atmospheric condition

Please refrain from keeping the product with possible corrosive gas or volatile flux.

Prevention of dew

Please store the product carton either on a wooden pallet or a stand / rack to prevent dew.

Do not place directly on the floor. In addition, to obtain moderate ventilation in between the pallet's top and bottom surfaces, pile the cartons up in a single direction and in order.

Please place the product cartons away from the storage wall.

Storage period

Within above mentioned conditions, maximum storage period should be one year.

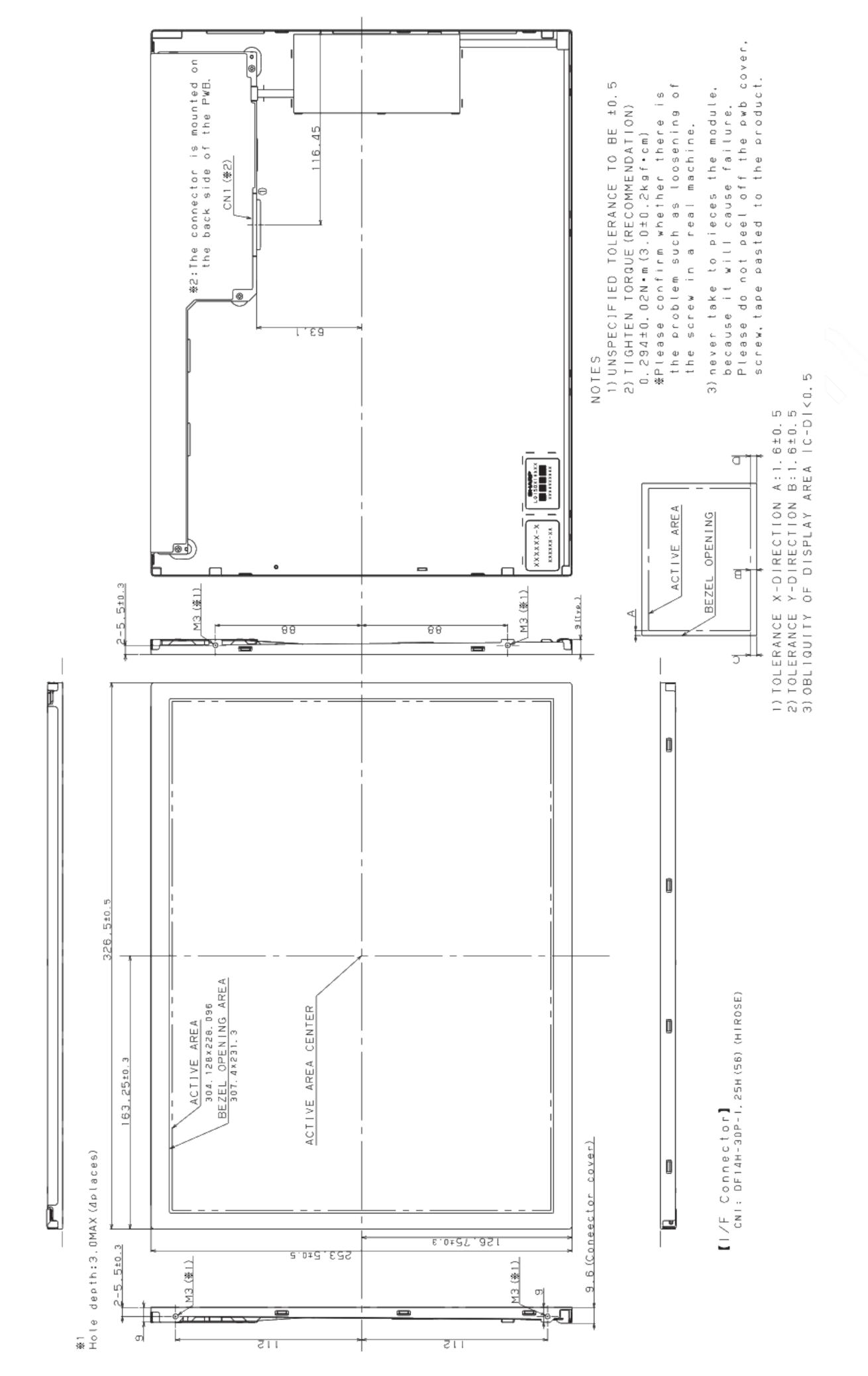


Fig. 1 : LQ150X1LW94 OUTLINE DIMENSIONS

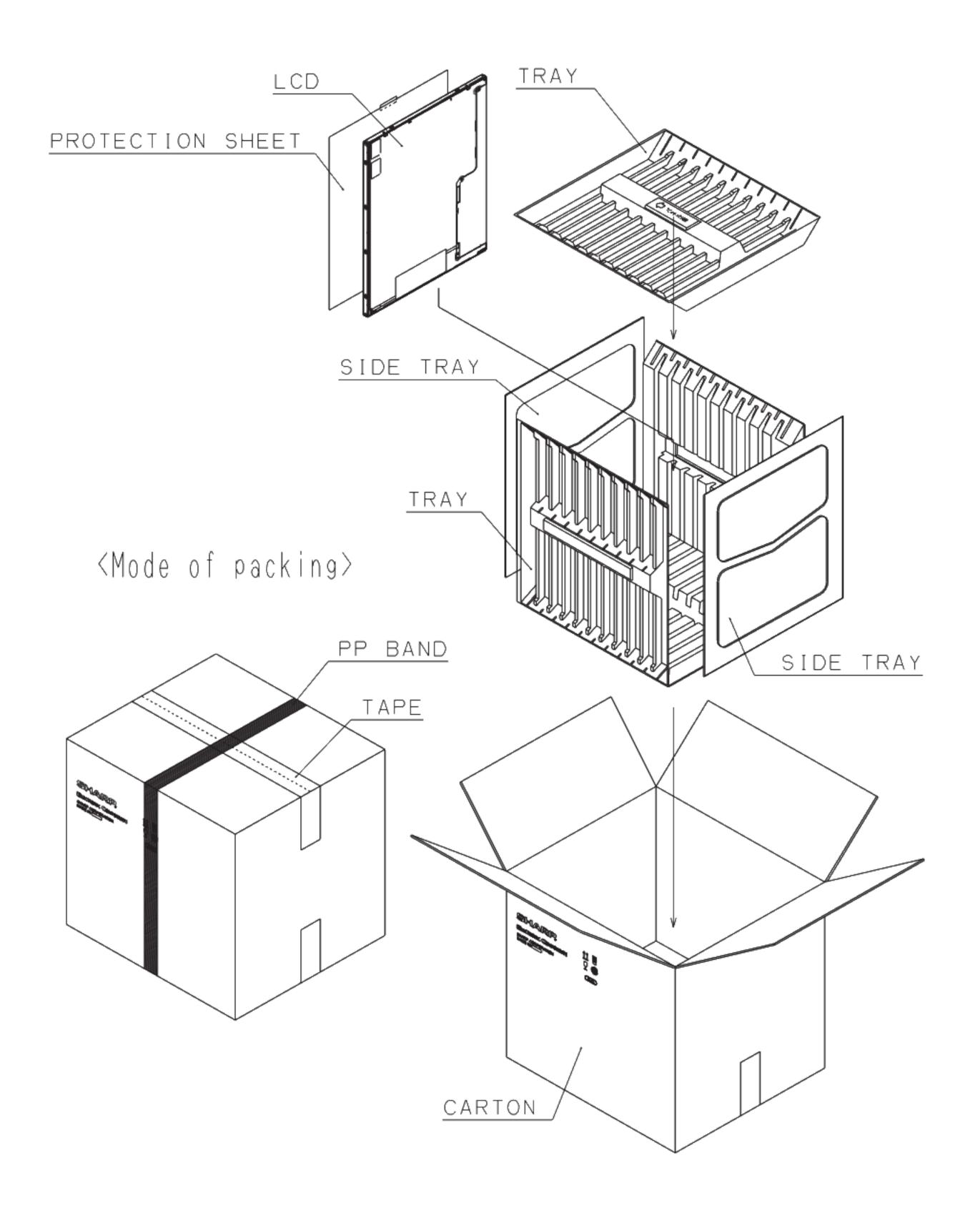


Fig.3 : PACKING FORM