PREPARED BY: DATE SPEC No. LD-21810 SHARP FILE No. APPROVED BY: DATE ISSUE: Sep. 01. 2009 PAGE : 22 pages MOBILE LIQUID CRYSTAL DISPLAY GROUP APPLICABLE GROUP SHARP CORPORATION MOBILE LIQUID CRYSTAL DISPLAY SPECIFICATION GROUP DEVICE SPECIFICATION FOR TFT-LCD Module MODEL No. LQ231U1LW31

These parts have corresponded with the RoHS directive.

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DIVISION DEPUTY GENERAL MANAGER
AND DEPARTMENT GENERAL MANAGER
ENGINEERING DEPT. I
MOBILE LIQUID CRYSTAL DISPLAY DIVISION III
MOBILE LIQUID CRYSTAL DISPLAY GROUP
SHARP CORPORATION

RECORDS OF REVISION

LQ231U1LW31

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1. Application

This specification sheets applies to the color TFT-LCD module LQ231U1LW31.

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Do not use the device for equipment that requires an extreme level of reliability, such as aerospace applications, telecommunication equipment (trunk lines), nuclear power control equipment and medical or other equipment for life support.

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Confirm "11. Handling Precautions " item when you use the device.

Contact and consult with a SHARP sales representative for any questions about this device.

2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (<u>Thin Film Transistor</u>). It is composed of a color TFT-LCD panel, driver ICs, control circuit, power supply circuit and a back light unit. Graphics and texts can be displayed on a $1600 \times RGB \times 1200$ dots panel with about 16,777,216 colors by using LDI (<u>LVDS Display Interface</u>) and supplying +5.0V DC supply voltages for TFT-LCD panel driving and applying +12.0V DC supply voltage for LED backlight-driving DC/DC converter.

It is a wide viewing-angle-module, high brightness(500cd/m) and high speed response specification. These LCD modules have corresponded with the RoHS directive.

3. Mechanical Specifications

Parameter	Specifications	Unit
Display size	59 (23.1") Diagonal	cm
Active area	470.4 (H)×352.8 (V)	mm
Pixel format	1600 (H)×1200 (V)	Pixel
	(1 pixel=R+G+B dots)	
Aspect ratio	4:3	
Pixel pitch	0.294 (H)×0.294 (V)	mm
Pixel configuration	R, G, B vertical stripe	
Display mode	Normally black	
Unit outline dimensions *1	530.0(W)×431.5(H) ×23.9(D)TYP	mm
Mass	4,500 (max)	g
Surface treatment (Haze value)	Anti-glare coating :	
	(Haze value 40%, Hardness 2H)	

^{*1.} Note: excluding back light cables and connecters.

The thickness of module (D) doesn't contain the projection.

Outline dimensions are shown in Fig.1.

4. Input Terminals

4-1. TFT-LCD panel driving

CN1 (Interface signals and +5.0V power supply)

Using connectors : FI-WE31P-HFE (Japan Aviation Electronics Industry, Limited)

Corresponding connectors : FI-W31M (Japan Aviation Electronics Industry, Limited)

: FI-C3-A3-15000(Contact)

Using LVDS receiver : Type contained in a control IC

(DS90CF386 (NS Corporation) or equivalent)

Corresponding LDI Transmitter: DS90C387 (NS Corporation) or equivalent)

Non-DC Balanced Mode

Table 4-1-1 LDI Interface signals

Pin No.	Symbol	Function	Remark
1	Vec	+5V power supply	
2	Vcc	+5V power supply	
3	Vec	+5V power supply	
4	Vec	+5V power supply	
5	GND	GND	
6	GND	GND	
7	GND	GND	
8	NC	NC	
9	GMCH	Gamma control(High)	
10	GMCL	Gamma control(Low)	
11	RxO0-	Receiver signal of LDI (O0-)	LDI
12	RxO0+	Receiver signal of LDI (O0+)	LDI
13	RxO1-	Receiver signal of LDI (O1-)	LDI
14	RxO1+	Receiver signal of LDI (O1+)	LDI
15	RxO2-	Receiver signal of LDI (O2-)	LDI
16	RxO2+	Receiver signal of LDI (O2+)	LDI
17	RxO-	Receiver CLK of LDI(OC-)	LDI
18	RxO+	Receiver CLK of LDI(OC+)	LDI
19	RxO3-	Receiver signal of LDI (O3-)	LDI
20	RxO3+	Receiver signal of LDI (O3+)	LDI
21	RxE0-	Receiver signal of LDI (E0-)	LDI
22	RxE0+	Receiver signal of LDI (E0+)	LDI
23	RxE1-	Receiver signal of LVDS (E1-)	LDI
24	RxE1+	Receiver signal of LDI (E1+)	LDI
25	RxE2-	Receiver signal of LDI (E2-)	LDI
26	RxE2+	Receiver signal of LDI (E2+)	LDI
27	RxE3-	Receiver signal of LDI (E3-)	LDI
28	RxE3+	Receiver signal of LDI (E3+)	LDI
29	NC	NC	
30	LVDSGND	LVDSGND	
31	LVDSGND	LVDSGND	

Note1: Resistance (RVR) between GMCL and GMCH enables Gamma adjustment.

(See Fig.4-5-1: RVR=OPEN recommended)

Note2: There is a possibility that trouble occurs in initial and long-term reliability when using it besides corresponding connector.

4-2. Data Mapping

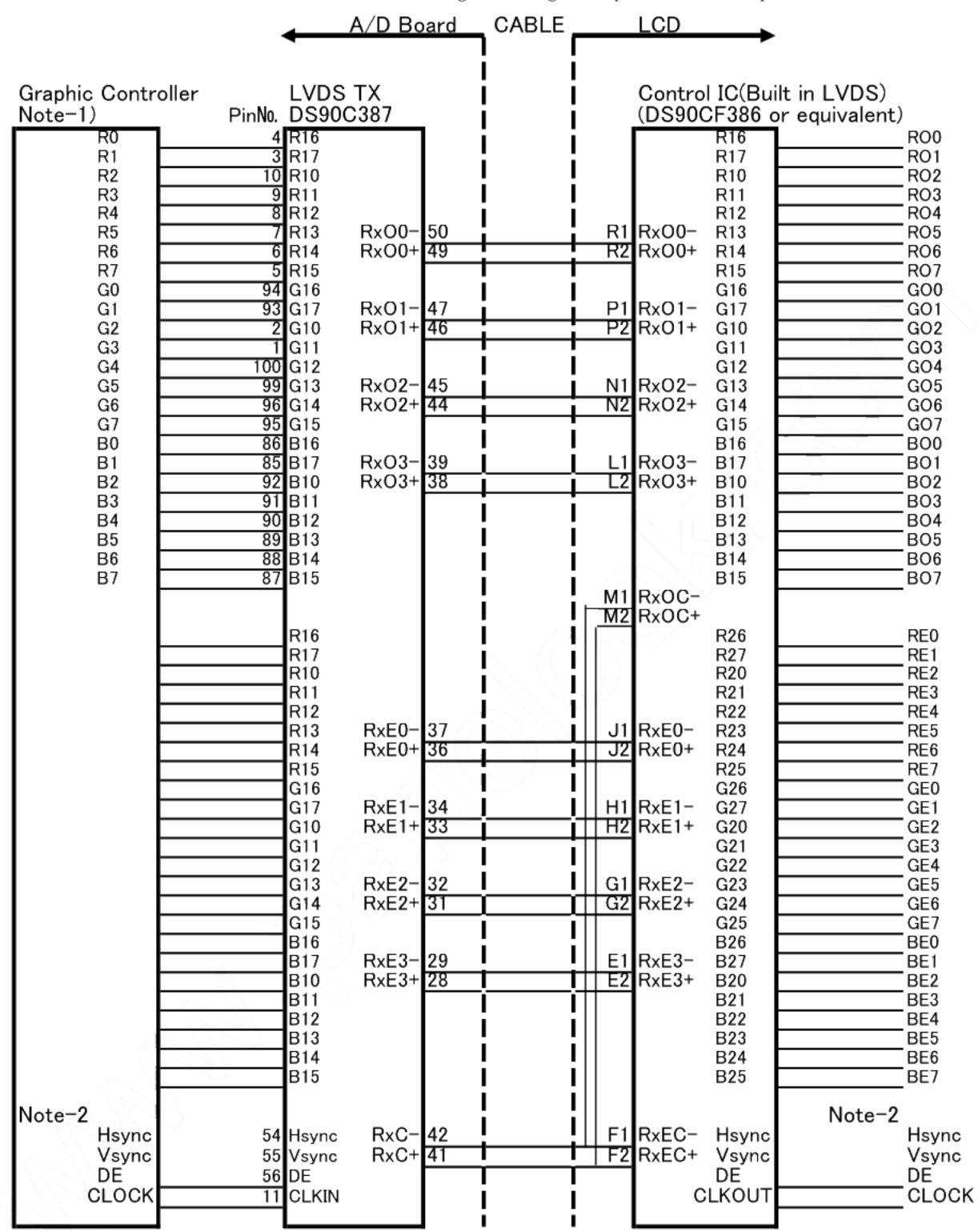
1) 8 bit input

Table 4-2-1 and Fig4-2-2 shows the pin assignment of the input signals.

Table 4

VGA-TF1	Data Signal	Input Data Pin (DS90C387)	Output Data Pin (DS90CF386)	LCD Unit Data Signal
LSB	RO	R16	R16	ROO
LOD	R1	R17	R17	RO1
	R2	R10	R10	RO2
	R3	R11	R11	R03
	R4	R12	R12	RO4
	R5	R13	R13	R05
	R6	R14	R14	R06
MSB	R7	R15	R15	R07
LSB	GO	G16	G16	G00
LJD	G1	G17	G17	G00 G01
	G2	G10	G17	G01 G02
	G2 G3	G10 G11	G10 G11	G02 G03
	G4	G11 G12	G11 G12	G03 G04
				
	G5	G13	G13	G05
MSB	G6	G14	G14	G06
	G7	G15	G15	G07
LSB	BO	B16	B16	B00
	B1	B17	B17	B01
	B2	B10	B10	B02
	B3	B11	B11	B03
	B4	B12	B12	B04
	B5	B13	B13	B05
MSB	B6	B14	B14	B06
	В7	B15	B15	B07
LSB		R16	R26	REO
		R17	R27	RE1
	/	R10	R20	RE2
		R11	R21	RE3
		R12	R22	RE4
		R13	R23	RE5
Wab		R14	R24	RE6
MSB		R15	R25	RE7
LSB		G16	G26	GEO
		G17	G27	GE1
		G10	G20	GE2
		G11	G21	GE3
		G12	G22	GE4
		G13	G23	GE5
		G14	G24	GE6
MSB		G15	G25	GE7
LSB		B16	B26	BEO
_		B17	B27	BE1
		B10	B20	BE2
		B11	B21	BE3
		B12	B22	BE4
		B13	B23	BE5
		B13	B23	BE6
MSB		B15	B24 B25	BE7

LDI Interface block diagram(Single input→Dual output)



Note-1) Signal configuration

EVEN DATA(ROx,BOx,GOx: 1'st,3'rd,5th······data)
ODD DATA(REx,GEx,BEx : 2'nd,4'th,6'th·····data)

Note-2) Hsync, Vsync Not use.

Note-3) Use twisted differential cable (impedance= 100Ω)

Note-4) DS90C387: Non-DC Balanced Mode

Figure 4-2-2 Interface pin assignment

<u>Table 4-2-3 LDI Input signals(Dual input)</u>

	Data Signal	Input Data Pin (DS90C387)	Output Data Pin (DS90CF386)	LCD Unit Data Signal
LSB	REO	R16	R16	R00
Lob	RE1	R17	R17	RO1
	RE2	R10	R10	R02
	RE3	R11	R11	R03
	RE4	R12	R12	RO4
	RE5	R13	R13	R05
	RE6	R14	R14	R06
MSB	RE7	R15	R15	R07
LSB	GEO	G16	G16	G00
LOD	GE1	G17	G17	G01
	GE2	G10	G10	G02
i	GE3	G11	G11	G03
	GE4	G12	G12	G04
	GE5	G13	G13	G05
	GE6	G14	G14	G06
MSB	GE7	G15	G15	G07
LSB	BEO	B16	B16	B00
LOD	BE1	B17	B17	B01
ĺ	BE2	B10	B10	B02
	BE3	B11	B11	B03
	BE4	B12	B12	B04
	BE5	B13	B13	B05
	BE6	B14	B14	B06
MSB	BE7	B15	B15	B07
LSB	ROO	R26	R26	REO
202	RO1	R27	R27	RE1
i	R02	R20	R20	RE2
i	R03	R21	R21	RE3
i	R04	R22	R22	RE4
i	R05	R23	R23	RE5
İ	R06	R24	R24	RE6
MSB	R07	R25	R25	RE7
LSB	G00	G26	G26	GEO
	G01	G27	G27	GE1
İ	G02	G20	G20	GE2
	G03	G21	G21	GE3
	G04	G22	G22	GE4
	G05	G23	G23	GE5
	G06	G24	G24	GE6
MSB	G07	G25	G25	GE7
LSB	B00	B26	B26	BEO
	B01	B27	B27	BE1
	B02	B20	B20	BE2
	B03	B21	B21	BE3
	B04	B22	B22	BE4
	B05	B23	B23	BE5
	B06	B24	B24	BE6
MSB	B07	B25	B25	BE7

LDI Interface block diagram(Dual input→Dual output) A/D Board CABLE LCD LVDS TX Control IC(Built in LVDS) (DS90CF388 or equivalent) PinNo. DS90C387 Graphic controller 4 R16 RO0 RE0 R16 3 R17 RE1 RE2 RO1 R17 10 R10 R10 RO2 RE3 9 R11 RO3 R11 RE4 8 R12 R12 RO4 R1 Rx00-RE5 RxO0-50 RO5 7 R13 R13 RxO0+ 49 R2 RxO0+ RE6 6 R14 R14 RO6 RE7 5 R15 RO7 R15 GE0 94 G16 GO0 G16 RxO1- 47 RxO1+ 46 GE1 93 G17 P1 Rx01-G17 GO1 2 G10 P2 RxO1+ GO₂ GE2 G10 GE3 G11 G11 GO3 GE4 100 G12 G12 G₀₄ GE5 RxO2- 45 RxO2+ 44 99 G13 **GO5** N1 RxO2-G13 N2 RxO2+ GE6 96 G14 G14 GO6 GE7 95 G15 G07 G15 BE0 86 B16 B16 BO0 RxO3- 39 RxO3+ 38 L1 RxO3-BE1 85 B17 B17 B01 92 B10 91 B11 L2 RxO3+ BE2 B10 BO₂ BE3 BO3 B11 90 B12 89 B13 BE4 B12 BO4 BE5 B13 BO5 BE6 88 B14 **B14** BO6 87 B15 BE7 B07 B15 M1 RxOC-M2 RxOC+ 76 R26 75 R27 R26 RO0 RE0 RO1 R27 RE1 RO2 84 R20 R20 RE2 81 R21 80 R22 RO3 R21 RE3 RO4 R22 RE4 79 R23 J1 RxE0-RxE0- 37 RO5 R23 RE5 78 R24 77 R25 RxE0+ 36 J2 RxE0+ RO6 R24 RE6 RO7 RE7 R25 66 G26 GE0 GO0 G26 RxE1- 34 RxE1+ 33 H1 RxE1-65 G27 GO1 G27 GE1 GO2 74 G20 H2 RxE1+ G20 GE2 GO3 73 G21 G21 GE3 72 G22 G04 G22 GE4 G1 RxE2-RxE2- 32 71 G23 GO5 G23 GE5 RxE2+ 31 G2 RxE2+ G06 70 G24 G24 GE6 G07 69 G25 G25 GE7 58 B26 BO0 B26 BE0 E1 RxE3-E2 RxE3+ 57 B27 RxE3- 29 B01 B27 BE1 RxE3+ 28 64 B20 BO2 B20 BE2 63 B21 BO3 B21 BE3 62 B22 BO4 B22 BE4 61 B23 BO5 B23 BE5 60 B24 BO6 B24 BE6 59 B25 BO7 B25 BE7 Note Note F1 RxEC-RxC- 42 Hsync 54 Hsync Hsync 71 Hsync RxC+ 41 F2 RxEC+ Vsync 55 Vsync Vsync 70 Vsync DE 56 DE DE 69 DE CLOCK CLKOUT 42 CLOCK 11 CLKIN

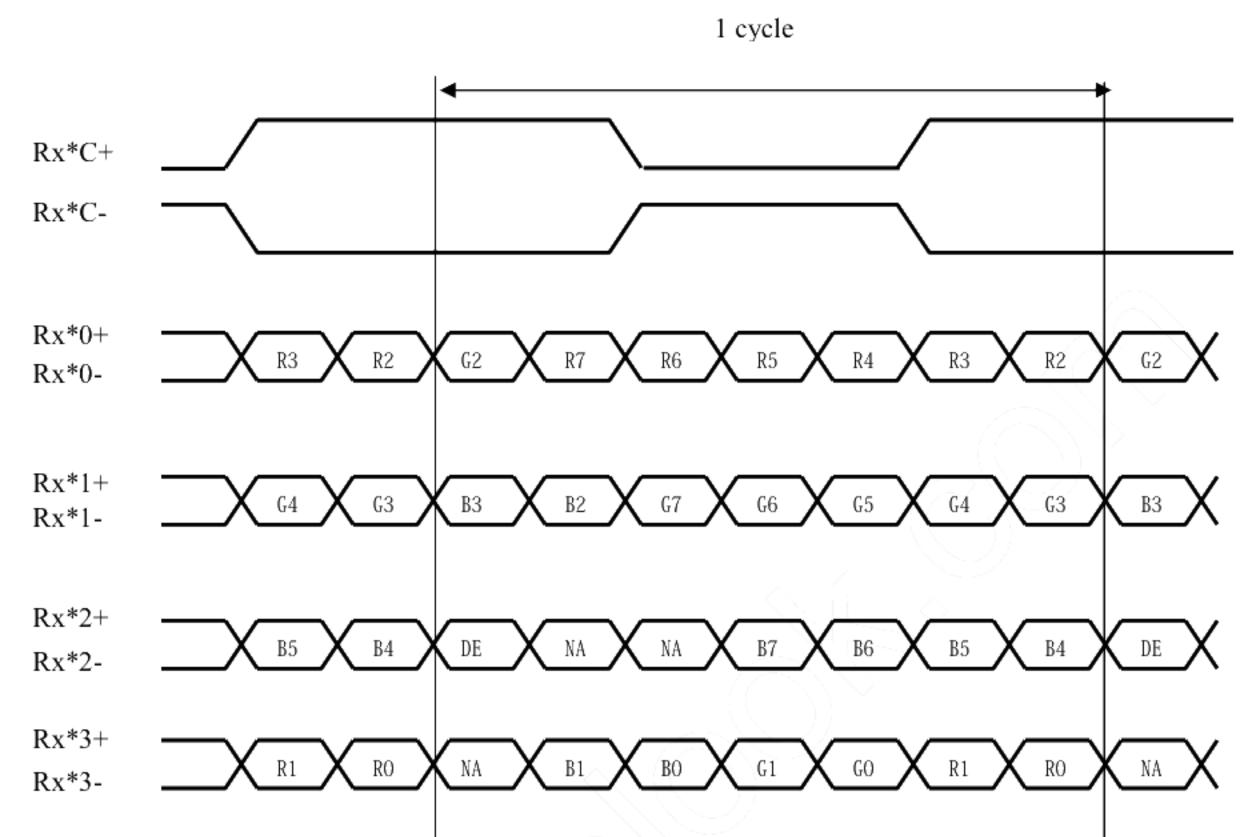
Note-1 Hsync, Vsync Not use

Note-2 Use twisted differential cable (impedance = 100Ω)

Note-3 DS90C387: Non-DC Balanced Mode

Figure 4-2-4 Interface pin assignment

4-3. Input signal timing chart



0 or E

DE : Display Enable NA : Not Available

4-4. Backlight

CN2

Using connector : S12B-PH-SM4-TB (JST)

Corresponding connector: PHR-12 (JST) (Cable: AWG#24 recommended)

Pin No.	Symbol	Function	Remark
1	V_{LED}	+12.0V LED Backlight power supply	
2	V_{LED}	+12.0V LED Backlight power supply	
3	V_{LED}	+12.0V LED Backlight power supply	
4	V_{LED}	+12.0V LED Backlight power supply	
5	V_{LED}	+12.0V LED Backlight power supply	
6	LED GND	LED GND	
7	LED GND	LED GND	
8	LED GND	LED GND	
9	LED GND	LED GND	
10	LED GND	LED GND	
11	Vent	BL ON/OFF	
12	Vvr	Brightness control	

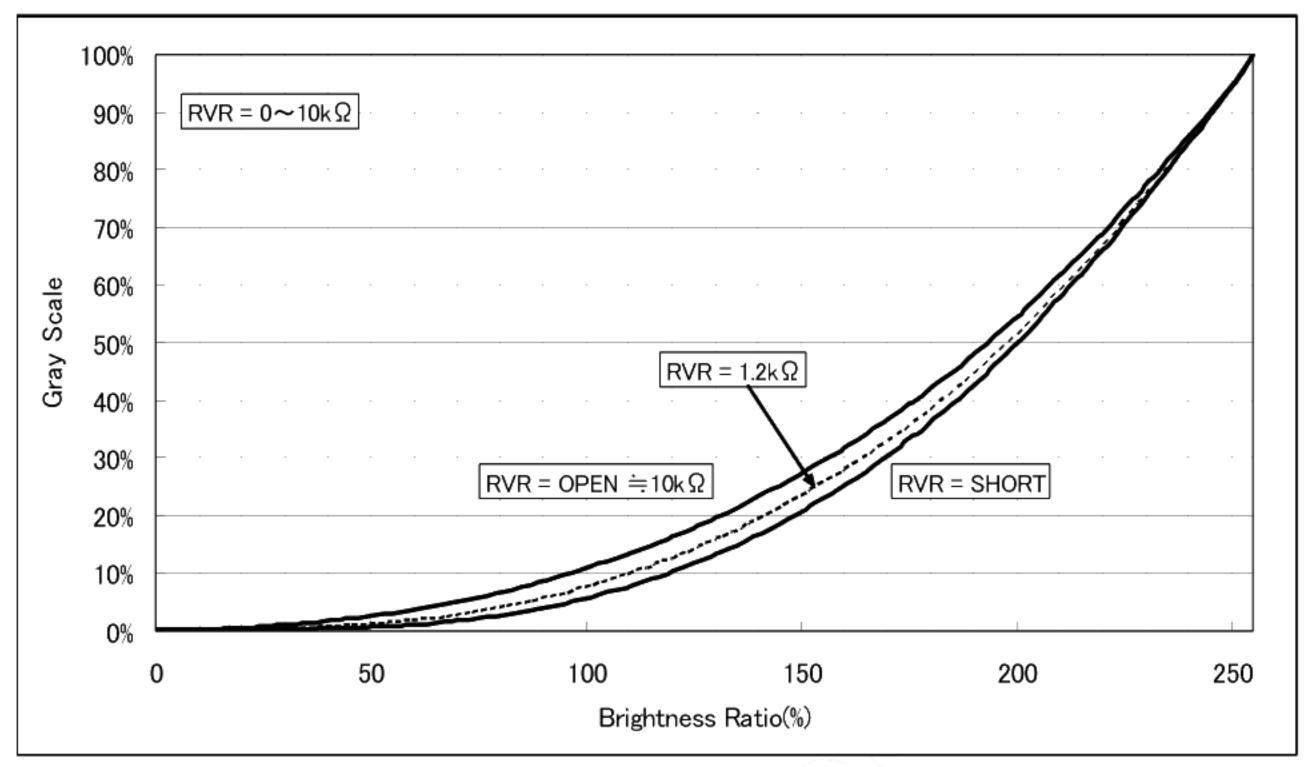


Figure 4-5-1 Gamma characteristics

Note) Fig.4-5-1 shows reference characteristics but dose not guarantee it.

5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Terminal Symbol]	Ratings	Unit	Remark
Supply voltage Vcc		Ta=25°C	Vcc	-0.3 ∼ + 6.0		V	[Note1]
LED Supply voltage	V_{LED}	Ta=25°C	V_{LED}	-0.3	~ +14.0		
Input voltage	V _L	Ta=25°C	LVDS input signal	-0.3	3 ∼ +3.6	V	
	Vent	Ta=25°C	Vent	-0.3	3∼VLED	V	
	Vvr	Ta=25℃	Vvr		0~4.0	V	
LED temperature	T_{LED}	_	_	+90		°C	[Note2]
Storage temperature	T_{STG}	Ambient	_	-20~ +65		°C	[Note1]
Operating temperature	T_{OPA}	Ambient	_	min	0	°C	[Note1]
		Panel surface (Active area)	_	max	+ 60		

[Note1] Humidity: 90%RH Max. ($Ta \le 40^{\circ}C$)

Maximum wet-bulb temperature at 39° C or less. (Ta> 40° C)

No condensation.

- [Note2] The measurement point refers to Fig. 1.
- [Note3] There is a possibility of causing deterioration in the irregularity and others of the screen and the display fineness when preserving or using it in more than 60°C.
- [Note4] When ambient temperature is more than 50°C, please cool the back side of the LCD module by a fan.

6. Electrical Characteristics

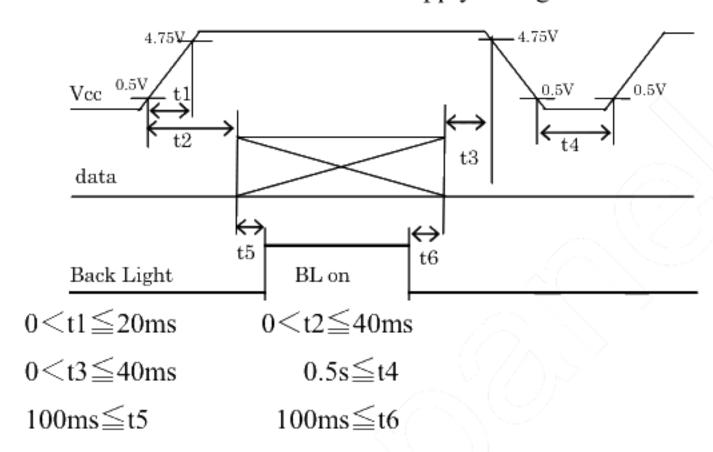
6-1. TFT-LCD panel driving

$Ta=+25^{\circ}$	2
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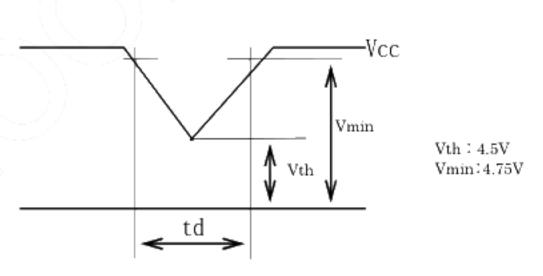
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arameter		Symbol	Min.	Typ.	Max.	Unit	Remark
Supply voltage		V_{CC}	+4.75	+5.0	+5.25	V	[Note2]
Current dissipation	Vcc=5.0V	I_{CC}	_	1100	1800	mA	[Note3]
Input voltage for LDI	LDI signal	V_{L}	0	_	2.4	V	
Permissive input ripple voltage		V_{RP}	_	_	100	mVp-p	Vcc=+5.0V
Differential input threshold	High	V_{TH}	_	_	V _{CM} +100	mV	V _{CM} =+1.2V
voltage							
	Low	V _{TL}	V_{CM} -100	_	_	mV	[Note1]
Terminal resistor		R _T	_	100	_	Ω	Differential
							input

[Note1] V_{CM}: Common mode voltage of LDI driver.

[Note2] On-off condition for supply voltage



Vcc-dip conditions



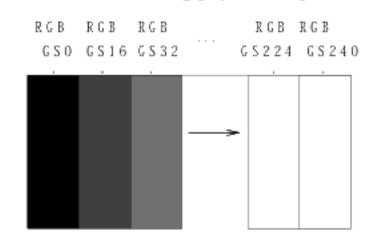
- 1) Vth \leq Vcc < Vmin td \leq 20ms
- 2) Vcc<Vth

Vcc-dip conditions should also follow the on-off conditions for supply voltage.

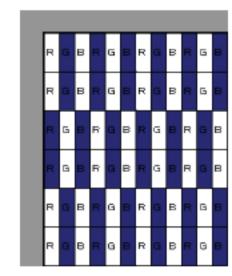
[Note3] Current dissipation

Standard value: 16-gray-bar pattern

(Measurement condition Vcc=+5.0V, 1/Tc=81MHz, Ta=25°C) Refer to Chapter 8 for RGB each gray scale



Maximum value: vertical 2dot checker (0/256-255/256) (Measurement condition Vcc=+4.75V, 1/Tc=81MHz, Ta=25°C)



6-2. Backlight

The backlight system is an edge-lighting type with white-LED.

(It is usually required to measure under the following condition.condition: Ta=25°C \pm 2°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Supply voltage	V_{LED}	11.4	12.0	12.6	V	
Current dissipation	I_{LED}	_	5.0	5.8	A	$V_{LED} = 12.0V$
						$V_{Vr} = 0V$
Brightness Control Voltage	V_{VR}	0	-	3.5	V	[Note1]
LED ON/OFF Low Voltage	V _{ent} L	0		1.0	V	[Note2]
LED ON/OFF High Voltage	V _{ent} H	4.5		V_{LED}	V	[Note2]
LED life time	-	30,000	50,000	-	h	LED only

The reference LED life time is 30,000h defined by below.

(Continuous turning on at LED Junction temperature (Tj) 100°C,

LED Current (If) = 150mA (equal to Max.Brightness)

A state only for LED)

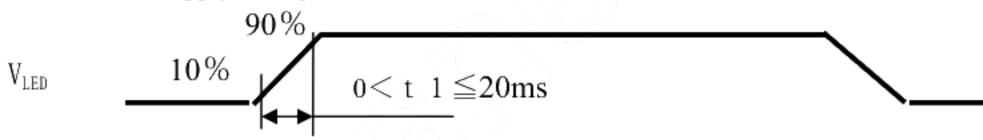
When a brightness of LED surface became 50% of the specifications minimum.

[Note1] V_{VR} input :0V= Max.Brightness 3.5V=Min. Brightness

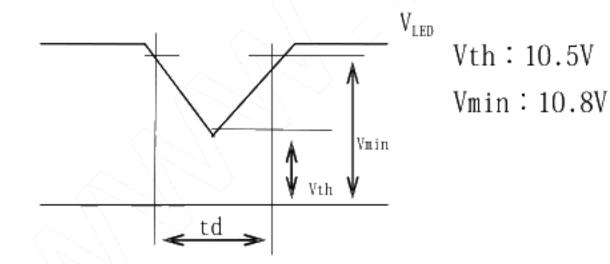
[Note2] V_{ent} input: Low or OPEN = BL turn on High = BL turn off

[Note3]

On-off condition for supply voltage



 $V_{\text{LED}}\text{-dip}$ conditions



- 1) Vth \leq V_LED < Vmin td \leq 20ms
- 2) $V_{LED} \le Vth$

7. Timing characteristics of input signals

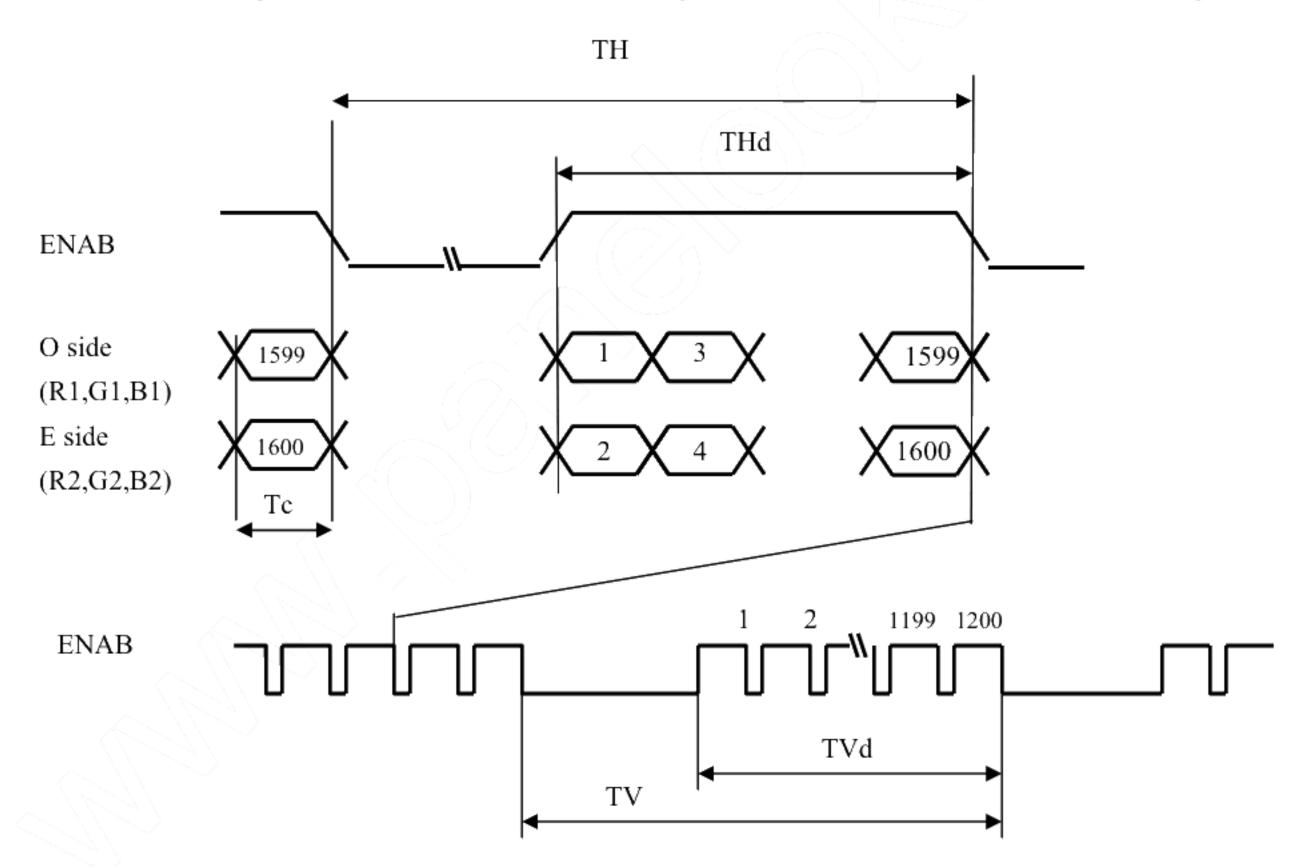
7-1. Timing characteristics

Parameter		Symbol	Min.	Тур.	Max.	Unit	Remark
Clock signal	Frequency	1/Tc	77.18	81	85	MHz	
ENAB signal	Horizontal period	TH	865	1080	1130	clock	[Note2]
			11.3	13.3	14.65	μs	
	Horizontal period (High)	THd	800	800	800	clock	
	Vertical period	TV	1220	1250	1280	line	[Note1]
			15.9	16.7	17.5	ms	[Note2]
	Vertical period (High)	TVd	1200	1200	1200	line	

[Note1] In case of using the long vertical period, the deterioration of display quality, flicker etc. may occur.

[Note2] The horizontal display position is determined by ENAB signal and the input data corresponding to the rising edge of DCLK is displayed at the left end of the active area.

Regarding the vertical display position, the data starting form following ENAB rising is displayed at the top of the active area in case of no rising ENAB more than 2003clk from ENAB rising.



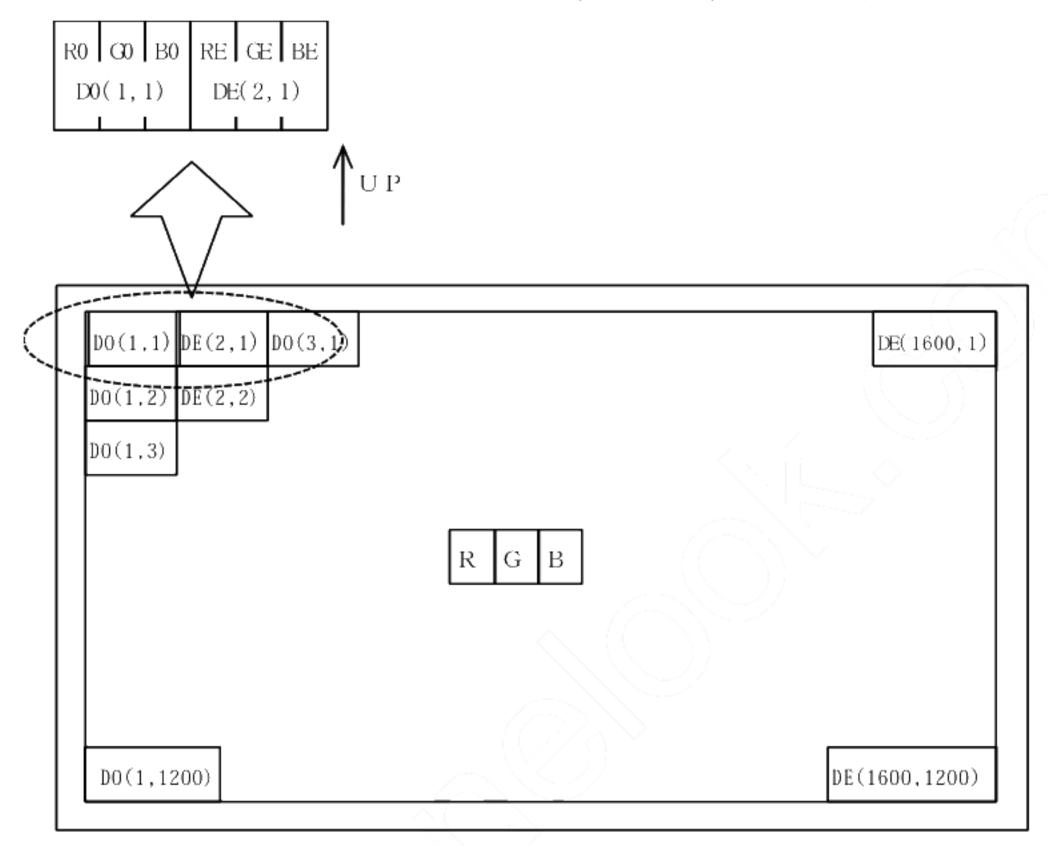
7-2. Input Data Signals and Display Position on the screen

Display position of input data (H, V)

Two pixels data is sampled at the same time.

※ DO (odd 1 data): RO0∼RO7, GO0∼GO7, BO0∼BO7

※ DE (even 1 data): RE0∼RE7, GE0∼GE7, BE0∼BE7



8. Input Signals, Basic Display Colors and Gray Scale of Each Color

8-1. 8bit input

,	l. 8bit ing																									
			_	Data signal																						
	Colors &	Gray	R0	Rl	R2	R3	R4	R5	R6	R7	G0	Gl	G2	G3	G4	G5	G6	G7	В0	В1	В2	В3	В4	В5	В6	В7
	Gray scale	Scale																								
Basic Color	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	_	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan	_	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	/1_	1	1	1	1
	Red	_	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	_	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	_	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	_	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	បិ	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	បិ	+	V						V					↓												
	Û	+	\downarrow						↓					↓												
	Brighter	253	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Û	254	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
G	បិ	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray S	Darker	2	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale	បិ	\leftarrow	$\Psi \left\langle \left\langle \left\langle \left\langle \left\langle \left\langle \left\langle \left\langle \left\langle \left\langle \left\langle \left\langle \left\langle $						↓					→												
e of	Û	+	↓ \ (()) \						↓					↓												
Scale of Green	Brighter	253	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0
l ^m	û	254	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Green	255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	បិ	_ 1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Gray Sca	Darker	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	បិ	V	↓						↓							↓										
le oi	Û	V	↓					\downarrow						↓												
Scale of Blue	Brighter	253	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1
le l	Û	254	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1
																										

0 : Low level voltage,

1 : High level voltage.

Each basic color can be displayed in 256 gray scales of red, 256 gray scales of green, and 256 gray scales of blue from 8 bit data signals. According to the combination of total 24 bit data signals, 16,777,216 color display can be achieved on the screen.

9. Optical Characteristics

Ta=25°C, Vcc =+5.0V

						I	Г	i – – – – – – – – – – – – – – – – – – –
Pa	rameter	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Viewing	Horizontal	θ 21, θ 22		85	_	_	Deg.	
angle	angle Vertical		CR>10	85	_	_	Deg.	
range	range All direction				80	_	Deg.	
Con	trast ratio	CRn	Optimum	350	600	_		[Note2,4]
			viewing					
			angle					
Resp	onse Time	τ r+ τ d		_	12	_	ms	[Note3(Condition2),4,5]
(Black→	White→Black)						/	
Resp	Response Time			_	8	_	ms	Average response time
Rise	Rise or decay							
(Gr	(Gray scale)							/
Chro	Chromaticity of			0.257	0.292	0.337	,,,	[Note4]
1	White			0.290	0.315	0.370	/	
Chro	Chromaticity of		$\theta = 0^{\circ}$	_	0.629) <u>}</u>		
	Red				0.362			
Chro	Chromaticity of				0.322	_		
(Green			(2)	0.610	_		
Chromaticity of		X			0.149	_		
Blue		у		<u> </u>	0.108	_		
Luminance of white		ance of white YLI		400	500	_	cd/m ²	[Note4]
White Uniformity		δw			_	1.33		[Note5]

*The measurement shall be executed 30 minutes after lighting at rating.

The optical characteristics shall be measured in a dark room or equivalent state with the method shown

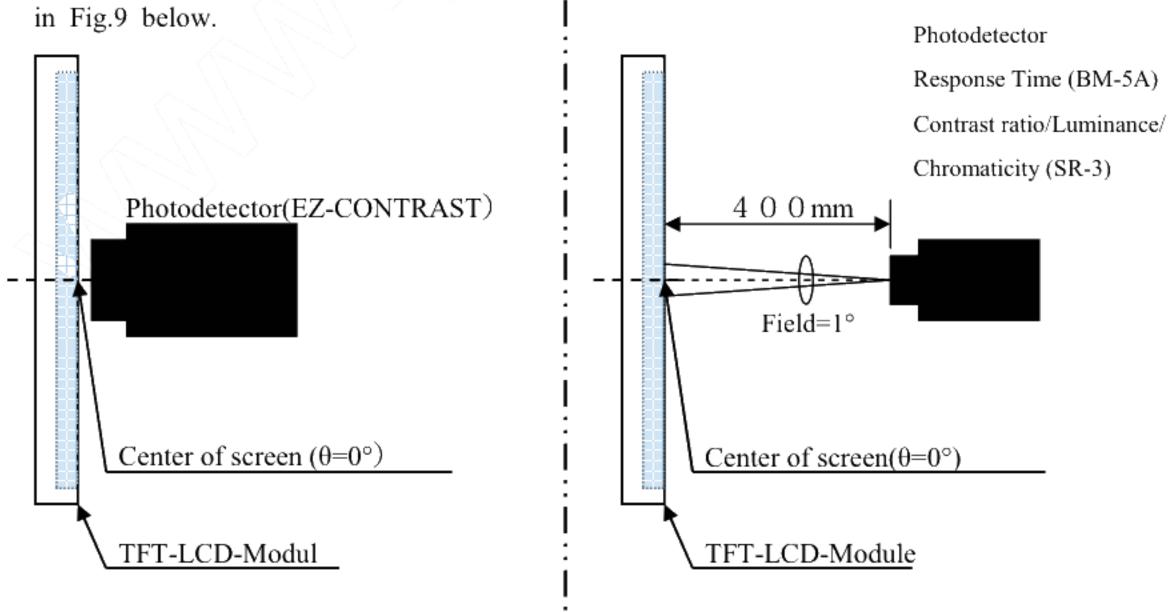
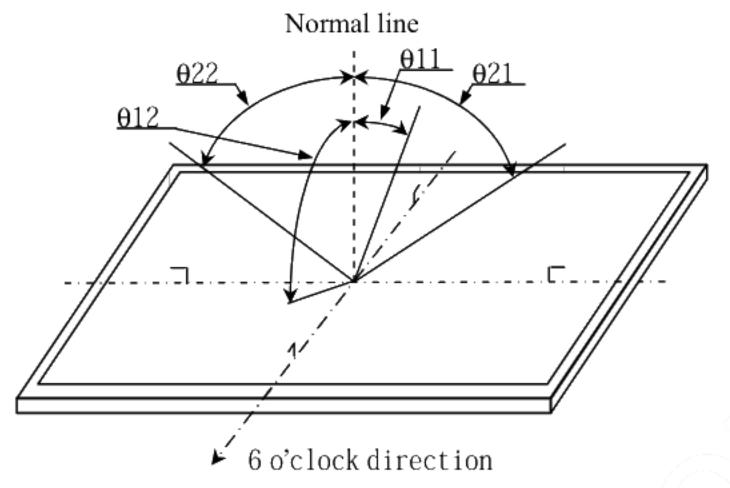


Fig9-1 Viewing angle measurement method

Fig9-2 Luminance/Contrast ratio/Response time/Chromaticity

[Note1] Definitions of viewing angle range:



[Note2] Definition of contrast ratio:

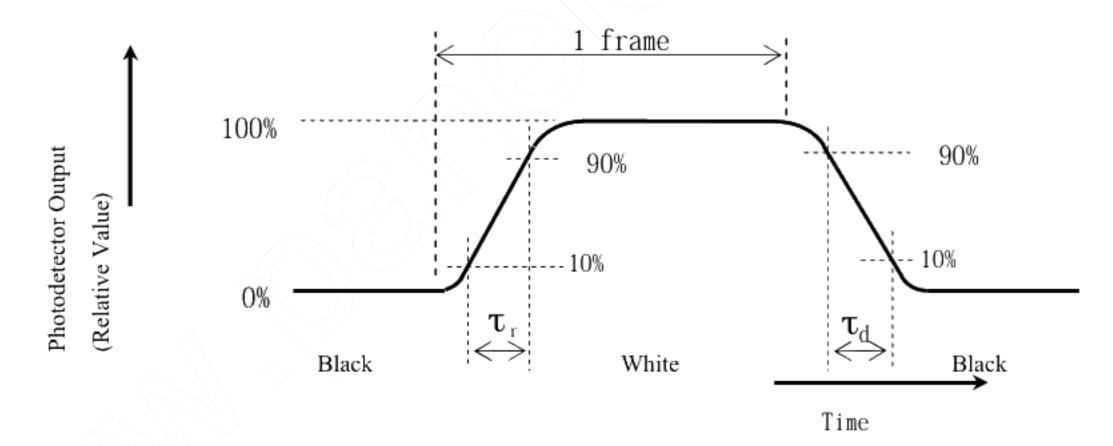
The contrast ratio is defined as the following.

Luminance (brightness) with all pixels white

Luminance (brightness) with all pixels black

[Note3] Definition of response time:

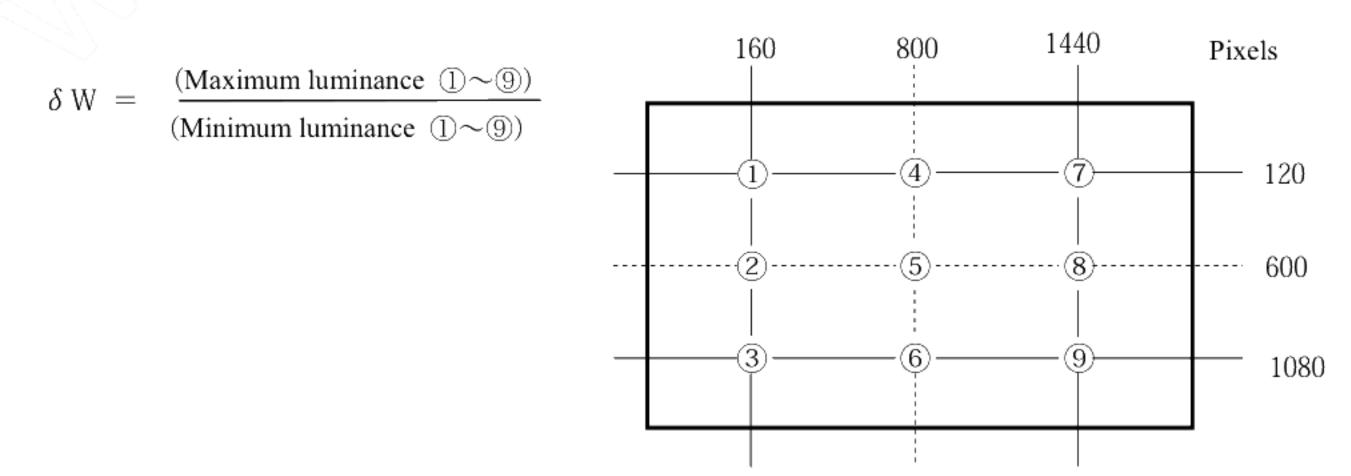
The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



[Note4] This shall be measured at center of the screen.

[Note5] Definition of white uniformity:

White uniformity is defined as the following with nine measurements ($1 \sim 9$).



10. Display dignity

The item concerning externals and the display dignity is decided by the shipment inspection standard book..

11. Handling Precautions

- a) Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.
- c) Since the front polarizer is easily damaged, pay attention not to scratch it.
 - Blow away dust on the polarizer with antistatic N_2 blow. It is undesirable to wipe off because a polarizer is sensitive.
 - It is recommended to peel off softly using the adhesive tape when soil or finger oil is stuck to the polarizer. When unavoidable, wipe off carefully with a cloth for wiping lenses.
- d) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- e) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- f) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
- g) Since CMOS LSI is used in this module, take care of static electricity and set the human earth when handling. Observe all other precautionary requirements in handling components.
- h) Since there is a circuit board in the module back, stress is not added at the time of a design assembly. If stress is added, there is a possibility that circuit parts may be damaged.
- i) Protection film is attached to the module surface to prevent it from being scratched. Peel the film off slowly, just before the use, with strict attention to electrostatic charges. Blow off 'dust' on the polarizer by using ionized nitrogen.
- j) The polarizer surface on the panel is treated with Anti-Glare for low reflection. In case of attaching protective board over the LCD, be careful about the optical interface fringe etc. which degrades display quality.
- k) Do not expose the LCD panel to direct sunlight.
 - Lightproof shade etc. should be attached when LCD panel is used under such environment. If a light strong against a LCD panel is irradiated, it may lead to degradation of the panel characteristic and display grace may get worse.
- 1) Connect GND to 4 place of mounting holes to stabilize against EMI and external noise.
- m) When handling LCD modules and assembling them into cabinets, please be noted that long-term storage in the environment of oxidization or deoxidization gas, and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion, discoloration, degradation of display grace, and abnormalities of operation.
- n) When install LCD modules in the cabinet, please tighten with "torque = max 0.441 N·m (max 4.5kgf·cm). Be sure to confirm it in the same condition as it is installed in your instrument.
- Liquid crystal contained in the panel may leak if the LCD is broken. Rinse it as soon as possible if it gets inside your eye or mouth by mistake.
- p) Notice: Never dismantle the module, because it will cause failure.
 - Moreover, please do not peel off and do not cut the tapes pasted to the product.
 - However, the tape fixed panel protection film is excluded.
- q) Be careful when using it for long time with fixed pattern display as it may cause afterimage. (Please use a screen saver etc., in order to avoid an afterimage.)
- r) Adjusting volume has been set optimally before shipment, so do not change any adjusted value. If adjusted value is changed, the specification may not be satisfied.
- s) If a minute particle enters in the module and adheres to an optical material, it may cause display non-uniformity issue, etc. Therefore, fine-pitch filters have to be installed to cooling and inhalation hole if you intend to install a fan.
- t) The LED used for this product is very sensitive to the temperature. Luminance decreases rapidly when it is used for a long time under the environment of the high temperature. Please consult our company when it is used under the environment like the above mentioned.
- u) Epoxy resin (amine series curing agent), silicone adhesive material (dealcoholization series and oxime series), tray forming agent (azo compound) etc, in the cabinet or the packing materials may induce abnormal display with polarizer film deterioration regardless of contact or noncontact to polarizer film.
 Be sure to confirm the component of them.

12. Packing form

Product countries / Areas	JAPAN
Piling number of cartons	8 (MAX)
Packing quantity in one carton	2pcs
Carton size [mm]	630(W) × 515(D) × 195(H)
Total mass of one carton filled with full modules	11kg
Packing form is shown	Page 22 (Fig.2)

13. Reliability test items

No	Test item	Conditions	
1	High temperature storage test	Ta = 65°C 240h	
2	Low temperature storage test	$Ta = -20^{\circ}C$ 240h	
3	High temperature	Ta = 40°C ; 90%RH 240h	
	& high humidity operation test	(No condensation)	
4	High temperature operation test	Ta = 60°C 240h (Panel surface:Activ Area)	
5	Low temperature operation test	Ta = 0°C 240h	
6	Vibration test	Waveform : Sine wave	
		Frequency: 10~57Hz/Vibration width (one side): 0.076mm	
		: 57~500Hz/Gravity : 9.8m/s ²	
		Sweep time: 11minutes	
		Test period : 3 hours	[Note]
		(1 hour for each direction of X,Y,Z)	
7	Shock test	Max. gravity: 147m/s ²	
		Pulse width: 6ms, sine half-wave	
		Direction: $\pm X$, $\pm Y$, $\pm Z$,	
		once for each direction.	

[Note]

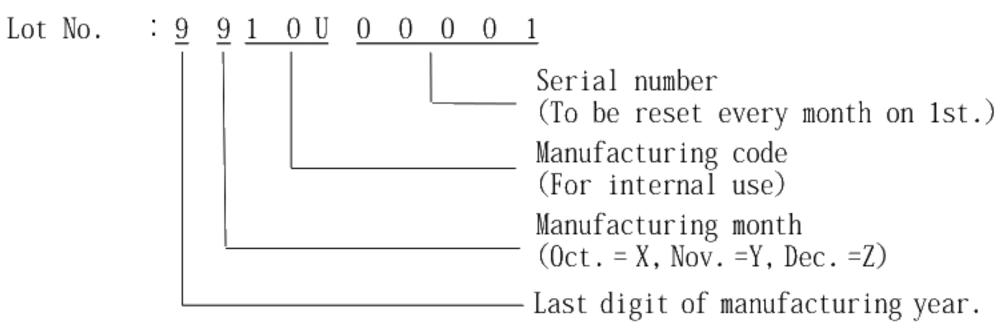
A gap of panel shall not occur by vibration or the shock.

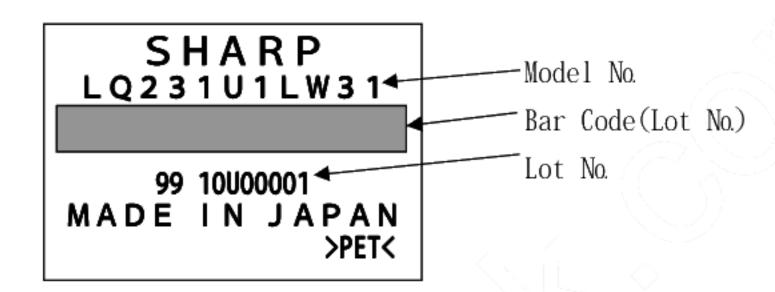
[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.

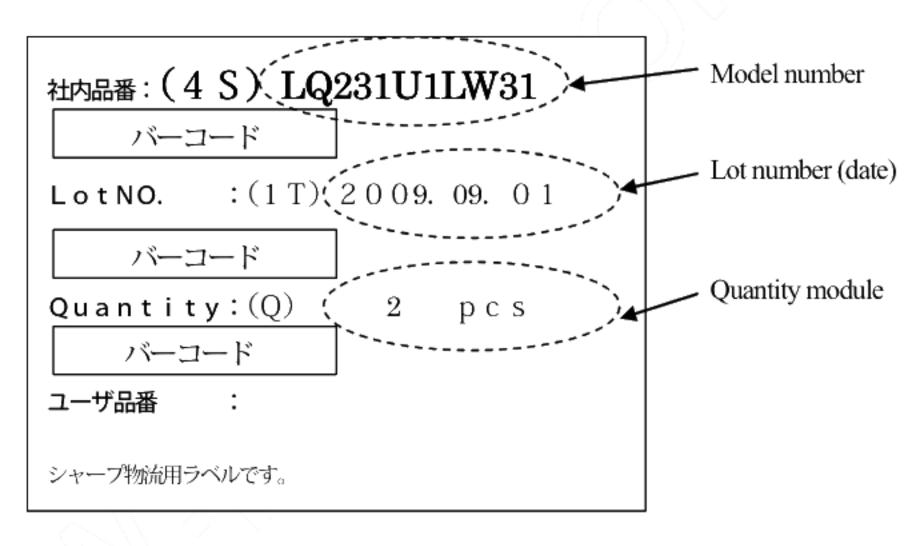
14. Others

14-1. Lot No. Label





14-2. Packing box Label



- 14-3. The chemical ozone depleting substance is not used.
- 14-4. If any problem occurs in relation to the description of this specification, it shall be resolved through discussion with spirit of cooperation.

The figure left below (cardboard box recycling symbol mark) is written to the packing box.

And, the figure right below is written to the packing box of the settlement for the RoHS restriction.

* R.C. (RoHS Compliance) means it suits the RoHS directive.

This LCD module is compliant with RoHS Directive.



Cardboard box •

Recycling symbol mark



Mark for RoHS directive

15. Range of storage temperature and humidity environmental condition

Temperature $0\sim40^{\circ}\text{C}$

Relative humidity 90% and below

 (Note) • Please manage as average value of the storage temperature and humidity environment referring to the following condition.

Please manage within 240 hours in total at the time kept under the environment of 40°C 90%RH.

Direct sunlight

Please keep it in the state of wrapping or the darkroom so that direct sunshine should not strike directly into the product.

Ambient atmosphere

Please do not keep it in the place with the danger of the generation of the causticity gas and the volatile solvent.

Dewy condensation prevention

 Please do not put the wrapping box directly on the floor, and keep it on palette or rack to avoid dewy condensation.

Moreover, please put it in a constant direction correctly to improve ventilation under the palette.

- · Please separate from the wall in the storage warehouse and keep it.
- · Please pay attention that ventilation is improved, and set up the ventilator etc. in the warehouse.
- · Please manage so that there is no rapid temperature change more than natural environment.

Storage period

Please keep within one year under the above-mentioned storage condition.

UNlT:mm

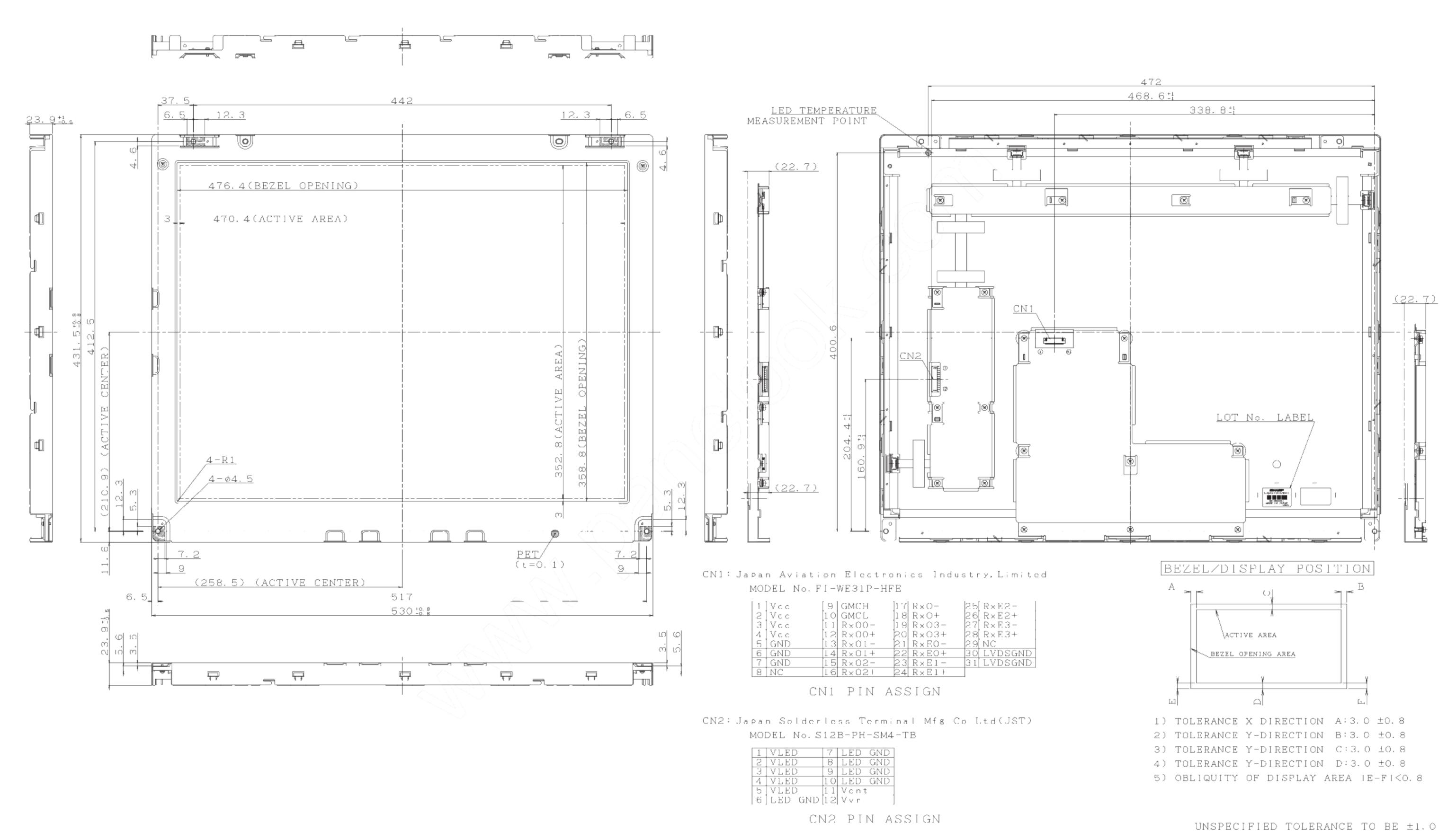


Fig1:LQ231U1LW31 Outline dimensions