TFT COLOR LCD MODULE

NL12880BC20-07F

31cm (12.1 Type) WXGA LVDS interface (1port)

PRELIMINARY DATA SHEET

DOD-PP-1852 (4th edition)

This PRELIMINARY DATA SHEET is updated document from DOD-PP-1761(3).

All information is subject to change without notice. Please confirm the sales representative before starting to design your system.

INTRODUCTION

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The products are classified into three grades: "Standard", "Special", and "Specific".

Each quality grade is designed for applications described below. Any customer who intends to use a product for application other than that of Standard is required to contact an NLT sales representative in advance.

The **Standard:** Applications as any failure, malfunction or error of the products are free from any damage to death, human bodily injury or other property (Products Safety Issue) and not related the safety of the public (Social Issues), like general electric devices.

Examples: Office equipment, audio and visual equipment, communication equipment, test and measurement equipment, personal electronic equipment, home electronic appliances, car navigation system (with no vehicle control functions), seat entertainment monitor for vehicles and airplanes, fish finder (except marine radar integrated type), PDA, etc.

The **Special:** Applications as any failure, malfunction or error of the products might directly cause any damage to death, human bodily injury or other property (Products Safety Issue) and the safety of the public (Social Issues) and required high level reliability by conventional wisdom.

Examples: Vehicle/train/ship control system, traffic signals system, traffic information control system, air traffic control system, surgery/operation equipment monitor, disaster/crime prevention system, etc.

The **Specific:** Applications as any failure, malfunction or error of the products might severe cause any damage to death, human bodily injury or other property (Products Safety Issue) and the safety of the public (Social Issues) and developed, designed and manufactured in accordance with the standards or quality assurance program designated by the customer who requires extremely high level reliability and quality.

Examples: Aerospace system (except seat entertainment monitor), nuclear control system, life support system, etc.

The quality grade of this product is the "Standard" unless otherwise specified in this document.

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1. OUTLINE

1.1 STRUCTURE AND PRINCIPLE

Color LCD module NL12880BC20-07F is composed of the amorphous silicon thin film transistor liquid crystal display (a-Si TFT LCD) panel structure with driver LSIs for driving the TFT (Thin Film Transistor) array and a backlight.

The a-Si TFT LCD panel structure is injected liquid crystal material into a narrow gap between the TFT array glass substrate and a color-filter glass substrate.

Color (Red, Green, Blue) data signals from a host system (e.g. signal generator, etc.) are modulated into best form for active matrix system by a signal processing board, and sent to the driver LSIs which drive the individual TFT arrays.

The TFT array as an electro-optical switch regulates the amount of transmitted light from the backlight assembly, when it is controlled by data signals. Color images are created by regulating the amount of transmitted light through the TFT array of red, green and blue dots.

1.2 APPLICATION

• For industrial use

1.3 FEATURES

- Ultra high luminance
- High contrast
- Wide temperature range
- · Small foot print
- LVDS interface
- Reversible-scan direction
- Selectable 8bit or 6bit digital signals for data of RGB
- Replaceable lamp for backlight
- ColorXcell technology (Color Enhancement)



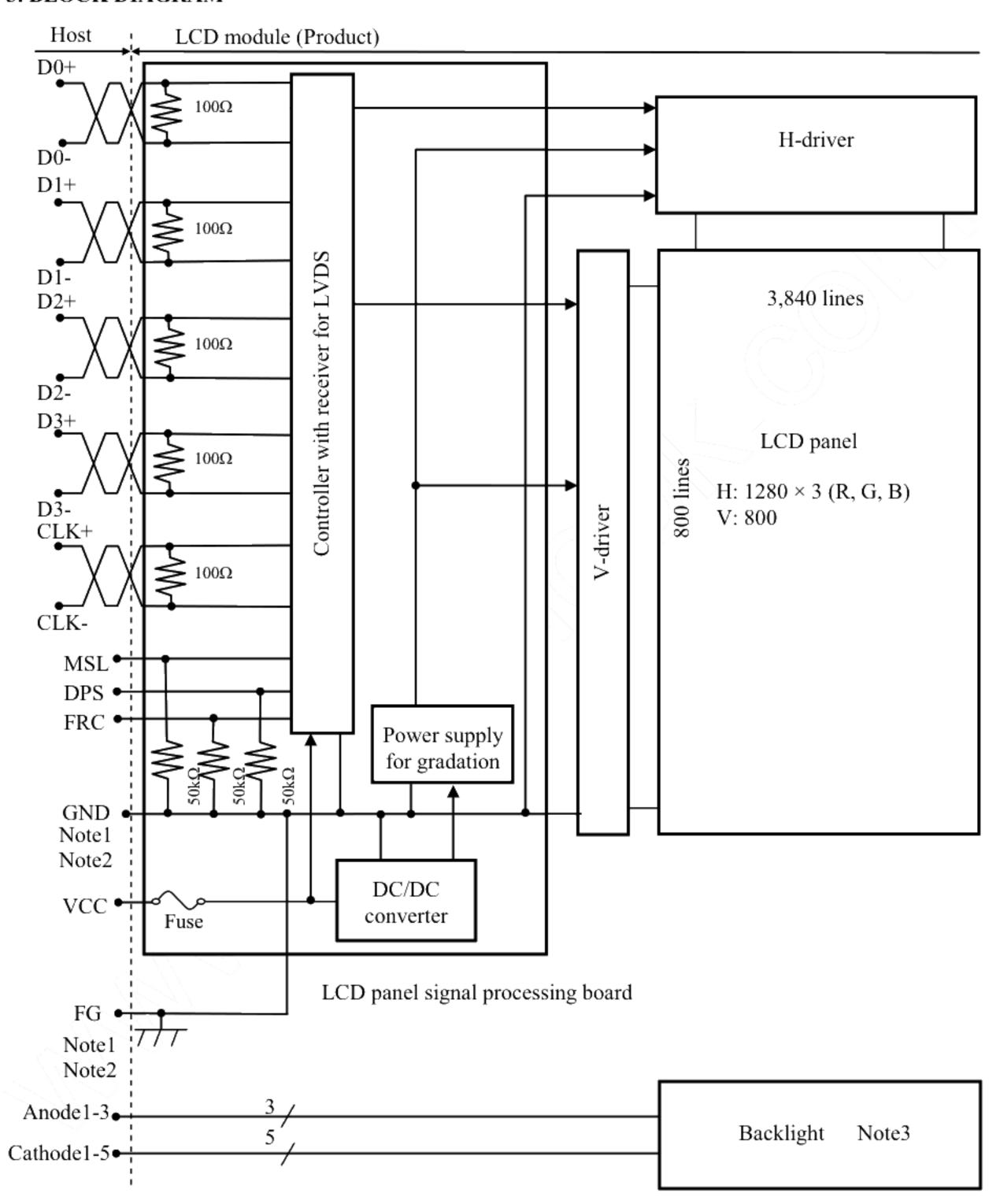
2. GENERAL SPECIFICATIONS

Display area	261.12 (H) × 163.2 (V) mm
Diagonal size of display	31cm (12.1 inches)
Drive system	a-Si TFT active matrix
Display color	16,777,216 colors (At 8-bit input, FRC terminal= High) 262,144 colors (At 6-bit input, FRC terminal= Low or Open)
Pixel	1280 (H) × 800 (V) pixels
Pixel arrangement	RGB (Red dot, Green dot, Blue dot) vertical stripe
Dot pitch	0.068 (H) × 0.204 (V) mm
Pixel pitch	0.204 (H) × 0.204 (V) mm
Module size	$277.7 \text{ (W)} \times 180.6 \text{ (H)} \times 8.7 \text{ (D)} \text{ mm (typ.)}$
Weight	(460) g (typ.)
Contrast ratio	(800):1 (typ.)
Viewing angle	At the contrast ratio ≥10:1 • Horizontal: Right side 80° (typ.), Left side 80° (typ.) • Vertical: Up side 80° (typ.), Down side 80° (typ.)
Designed viewing direction	 At DPS= Low or Open: Normal scan Viewing direction without image reversal: Up side (12 o'clock) Viewing direction with contrast peak: Down side (6 o'clock) Viewing angle with optimum grayscale (γ = 2.2): Normal axis (perpendicular)
Polarizer surface	Antiglare
Polarizer pencil-hardness	3H (min.) [by JIS K5600]
Color gamut	At LCD panel center 40 % (typ.) [against NTSC color space]
Response time	$Ton+Toff (10\% \longleftrightarrow 90\%)$ 15 ms (typ.)
Luminance	$At IL = (110)mA/One \ circuit$ $(1,800) \ cd/m^2 \ (typ.)$
Signal system	LVDS 1port (Receiver: THC63LVDF84B, THine Electronics Inc. or equivalent) [8bit/6bit digital signals for data of RGB colors, Dot clock (CLK), Data enable (DE)]
Power supply voltage	LCD panel signal processing board: 3.3V
	LED backlight:
Backlight	Replaceable part • Lamp holder set: Type No. 121LHS202
Power consumption	At IL= (110)mA/One circuit, Checkered flag pattern (17.0) W (typ.)

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3. BLOCK DIAGRAM



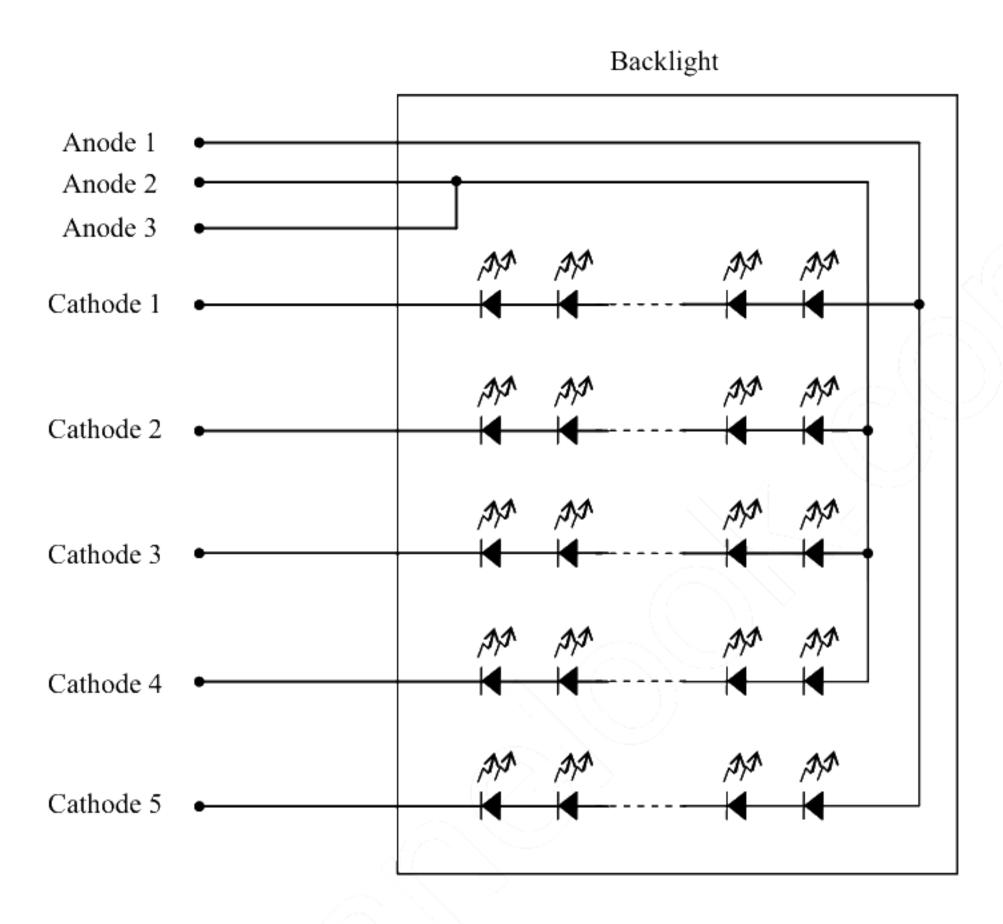
Note1: Relations between GND (Signal ground) and FG (Frame ground) in the LCD module are as follows.

GND- FG Connected

Note2: GND and FG must be connected to customer equipment's ground, and it is recommended that these grounds to be connected together in customer equipment.



Note3: Backlight in detail



4. DETAILED SPECIFICATIONS

4.1 MECHANICAL SPECIFICATIONS

Parameter	Specification		Unit
Module size	$277.7 \pm 0.5 \text{ (W)} \times 180.6 \pm 0.5 \text{ (H)} \times 8.7 \pm 0.5 \text{ (D)}$	Note1	mm
Display area	261.12 (H) × 163.2 (V)	Note1	mm
Weight	(460) (typ.), (490) (max.)		g

Note1: See "8. OUTLINE DRAWINGS".

4.2 ABSOLUTE MAXIMUM RATINGS

	Parameter		Symbol	Rating	Unit	Remarks
Power supply voltage	LCD panel signal	processing board	VCC	(-0.3 to +4.0)	V	
Input voltage	Display No	_	VD	(-0.3 to VCC+0.3)	V	-
for signals	Function No	_	VF	(-0.5 to VCC+0.5)	v	
Backlight	Forward	current	IL	130	mA	per one circuit
	Storage temperature		Tst	-30 to +80	°C	-
On monting a		Front surface	TopF	-20 to +70	°C	Note3
Operating	temperature	Rear surface	TopR	-20 to +70	°C	Note4
				≤ 95	%	Ta ≤ 40°C
	Relative humidity		RH	≤ 85	%	40°C < Ta ≤ 50°C
	Note5		1111	≤ 55	%	50°C < Ta ≤ 60°C
		<u>/ </u>		≤ 36	%	60°C < Ta ≤ 70°C
	Absolute humidity Note5	>	АН	≤ 70 Note6	g/m ³	Ta > 70°C

Note1: D0+/-, D1+/-, D2+/-, D3+/- and CLK+/-

Note2: DPS, FRC and MSL

Note3: Measured at LCD panel surface (including self-heat)

Note4: Measured at LCD module's rear shield surface (including self-heat)

Note5: No condensation

Note6: Water amount at Ta= 70°C and RH= 36%

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4.3 ELECTRICAL CHARACTERISTICS

4.3.1 LCD panel signal processing board

 $(Ta=25^{\circ}C)$

Parameter		Symbol	min.	typ.	max.	Unit	Remarks
Power supply voltage		VCC	3.0	3.3	3.6	V	-
Power supply current		ICC	-	(490) Note l	(820) Note2	mA	at VCC= 3.3V
Permissible ripple voltage		VRP	-	-	100	mVp-p	for VCC
Differential input	High	VTH	-	-	+100	mV	at VCM= 1.2V
threshold voltage	Low	VTL	-100	-	-	mV	Note3
Terminating resistance		RT	-	100	4	Ω	-
Input voltage for DPS,	High	VFH	0.7VCC	-	VCC	, v	CMOS 11
FRC and MSL signals	Low	VFL	0	-((0.3VCC	V	CMOS level
Input current for DPS,	High	IFH	-		300	μΑ	
FRC and MSL signals	Low	IFL	-300		-	μΑ	-

Note1: Checkered flag pattern [by EIAJ ED-2522]

Note2: Pattern for maximum current

Note3: Common mode voltage for LVDS receiver

4.3.2 Backlight lamp

(Ta= 25°C, Note1, Note2, Note3)

Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Forward current	IL	-	(110)	(120)	mA	-
		(24.7)	(27.9)	(30.6)		Ta= +25°C at IL= (110)mA /One circuit
Forward Voltage	W	(23.2)	-	v	Ta= +70°C at IL= (110)mA /One circuit	
Forward Voltage	VL	-	-	(31.9)	,	Ta= -20°C at IL= (110)mA /One circuit
		-	-	(32.4)		Ta= -20°C at IL= (120)mA /One circuit

Note1: Please drive with constant current.

Note2: The above specifications are for one LED circuit of the backlight.

Note3: The Luminance uniformity may be changed depending on the current variation between 5 circuits. It is recommended that the current value difference among the circuits be less than 5%.

4.3.3 Power supply voltage ripple

This product works if the ripple voltage levels are over the permissible values as the following table, but there might be noise on the display image.

Power sup	ply voltage	Ripple voltage Note1 (Measure at input terminal of power supply)	Unit
VCC	3.3V	≤ 100	mVp-p

Note1: The permissible ripple voltage includes spike noise.

4.3.4 Fuse

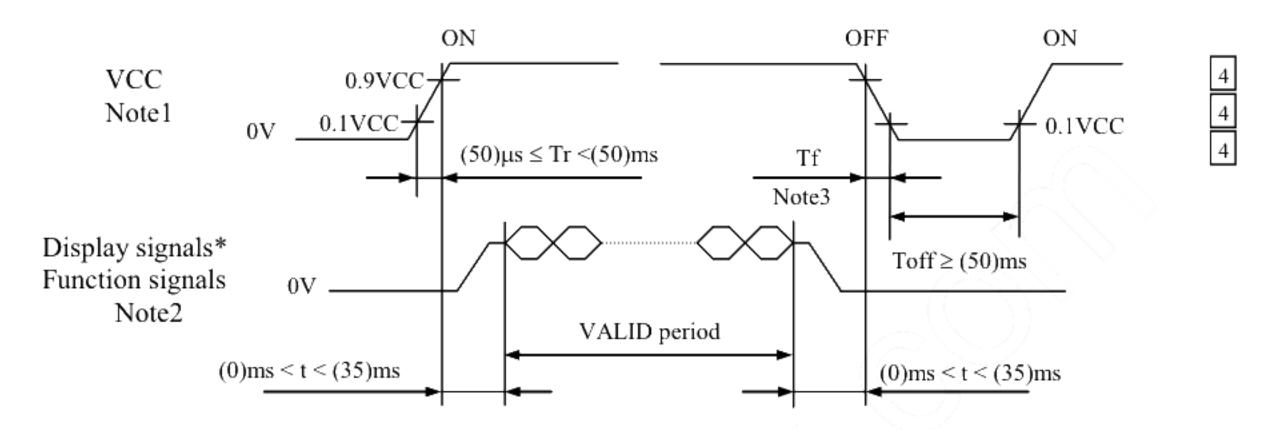
D		Fuse	D. die	E	D 1
Parameter	Туре	Supplier	Rating	Fusing current	Remarks
VCC	ECC16202 A B	KAMAYA	2.0A	4.04	Note1
VCC	FCC16202AB	ELECTRIC Co., Ltd	36.0V	4.0A	Note1

Note1: The power supply's rated current must be more than the fusing current. If it is less than the fusing current, the fuse may not blow in a short time, and then nasty smell, smoke and so on may occur.

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4.4 POWER SUPPLY VOLTAGE SEQUENCE

4.4.1 LCD panel signal processing board



* These signals should be measured at the terminal of 100Ω resistance.

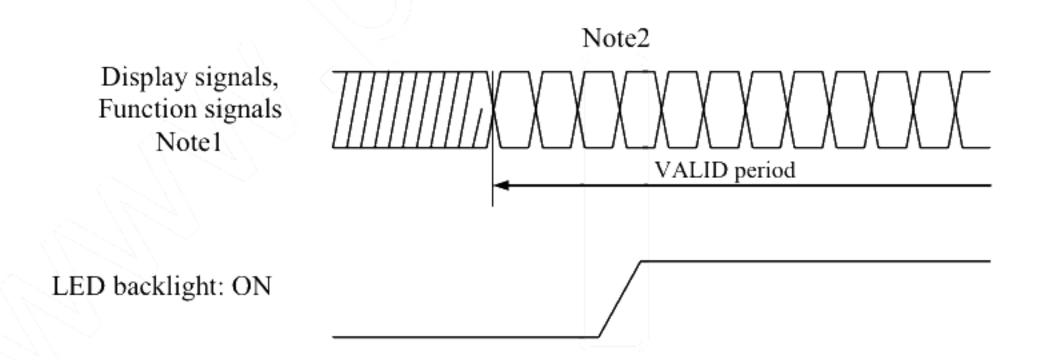
Note1: If there is a voltage variation (voltage drop) at the rising edge of VCC below TBDV, there is a possibility that a product does not work due to a protection circuit.

Note2: Display signals (D0+/-, D1+/-, D2+/-, D3+/- and CLK+/-) and function signals (DPS, FRC and MSL) must be set to Low or High-impedance, except the VALID period (See above sequence diagram), in order to avoid the circuitry damage.

If some of display and function signals of this product are cut while this product is working, even if the signal input to it once again, it might not work normally. If a customer stops the display and function signals, VCC also must be shut down.

Note3: Tf must be less than or equal to Tr in order to avoid any damage to the internal circuit.

4.4.2 LED driver



Note1: These are the display and function signals for LCD panel signal processing board.

Note2: The backlight should be turned on within the valid period of display and function signals, in order to avoid unstable data display.



4.5 CONNECTIONS AND FUNCTIONS FOR INTERFACE PINS

4.5.1 LCD panel signal processing board

CN1 socket (LCD module side): FI-SE20P-HFE (Japan Aviation Electronics Industry Limited (JAE))
Adaptable plug: FI-S20S (Japan Aviation Electronics Industry Limited (JAE))

щри	ore prag.		F1-8208 (Japa	an a tylumon Electroni	es maasay Emmea	(3712))				
No	Symbol	Signal	Input data	signal: 8bit	Input data signal:	Remarks				
140.	Symbol	Signal	MAP A	MAP B	6bit	Kemarks				
A	D3+	Pixel data	R0-R1,G0-G1,B0-B1	R6-R7,G6-G7,B6-B7	- 4	Note1 Note2				
В	GND	Ground		-	Ground	Note3				
A	D3-	Pixel data	R0-R1,G0-G1,B0-B1	R6-R7,G6-G7,B6-B7		Note1 Note2				
В	GND	Ground		-	Ground	Note3				
3	DPS	Selection of scan direction	High: Low or Open:	Reverse scan Normal scan		Note4				
1	FRC	Selection of the number of colors	Hi	gh	Low or Open	Note1 Note5				
5	GND	Ground		Ground	7	Note3				
5	CLK+	Pixel clock	Dissal at a ta							
7	CLK-	1 IACI CIOCK		Note2						
3	GND	Ground		Ground		Note3				
9 D2+			R4 R7 DE	5 DE	Note2					
0	D2-	r ixei data	B4-B7,DE	D2-D.	5,DE	Notez				
1	GND	Ground		Ground		Note3				
2	D1+	Pivel data	G3-G7 B2-B3	GL-G5	R0_R1	Note2				
3	D1-	1 IXCI data	G3-G7,B2-B3	G1-G5,	. Бо-Б1	Notez				
4	GND	Ground		Ground		Note3				
5	D0+	Dival data	P2 P7 C2	DO D	5 G0	Note2				
6	D0-	r ixei data	K2-K7,G2	KU-K	5,00	Notez				
7	GND	Ground		Ground		Note3				
8	MSL	Selection of LVDS input map	Low or Open	High Low or Open		Note5				
9	VCC	Dawas august.	Power supply							
0	VCC	rower supply	Power supply							
	No. A B A B S C C C C C C C C C C C C C C C C C C	No. Symbol A D3+ B GND A D3- B GND B GND FRC GND GND CLK- GND D2- I GND D1- GND A D0- GND MSL MSL WCC	No. Symbol Signal A D3+ Pixel data B GND Ground A D3- Pixel data B GND Ground B GND Ground G FRC Selection of the number of colors G GND Ground G CLK+ Pixel clock G CLK- Pixel data D D2+ Pixel data D D2- Pixel data D D1+ Pixel data D1- GND Ground G D0+ Pixel data G D0- Pixel data G D0- Pixel data F D0- Pixel data F	No. Symbol Signal Input data MAP A	No. Symbol Signal Input data signal: 8bit MAP A	No. Symbol Signal Input data signal: 8bit Input data signal: 6bit				

Note1: See "4.6 DISPLAY COLORS AND INPUT DATA SIGNALS".

Note2: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note3: All GND and VCC terminals should be used without any non-connected lines.

Note4: See "4.8 SCANNING DIRECTIONS".

Note5: See "4.5.4 Connection between receiver and transmitter for LVDS".



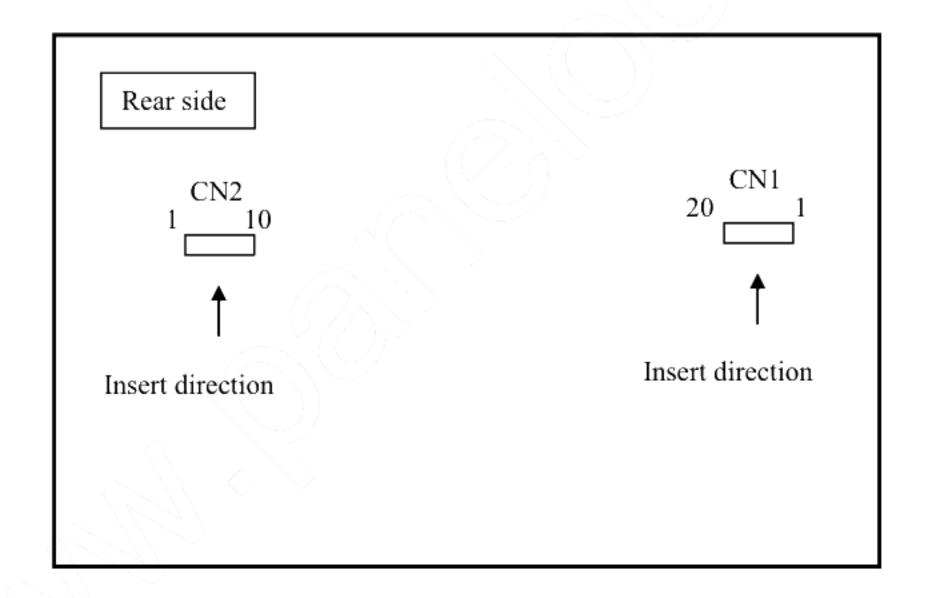
4.5.2 Backlight lamp

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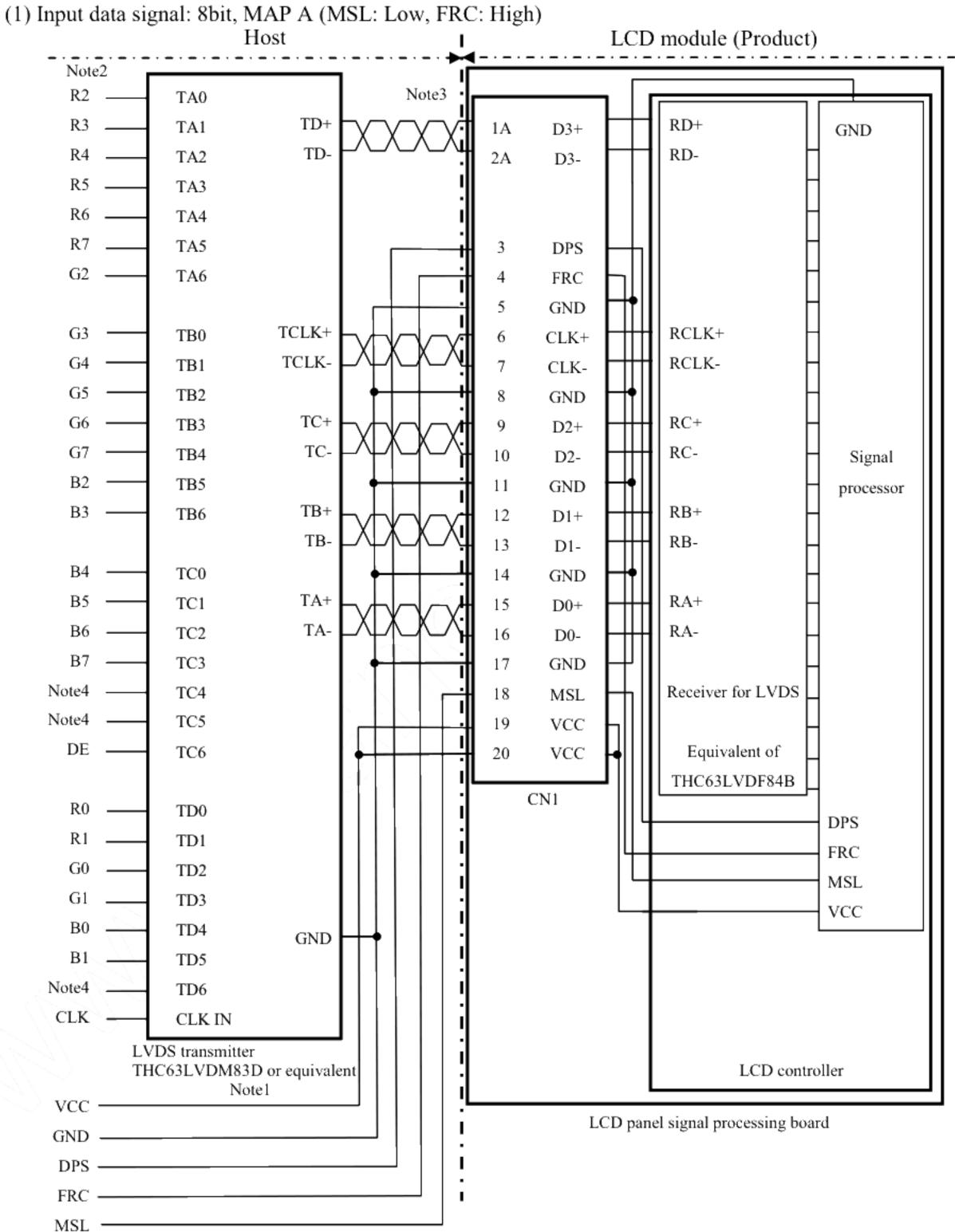
CN2 plug (LCD module side): SM10B-SHLS-TF (LF) (SN) (J.S.T. Mfg. Co., Ltd.)
Adaptable socket: SHLP-10V-S-B (J.S.T. Mfg. Co., Ltd.)

		<u></u>								
Pin No.	Symbol	Signal	Remarks							
1	A1	Anode1	-							
2	A2	Anode2	-							
3	A3	Anode3	- /~~							
4	N. C.	N. C.	Keep this pin Open.							
5	N. C.	N. C.	Keep this pin Open.							
6	K1	Cathode1								
7	K2	Cathode2	((- \) \							
8	K3	Cathode3								
9	K4	Cathode4	((() -							
10	K5	Cathode5								

4.5.3 Positions of plug and socket



4.5.4 Connection between receiver and transmitter for LVDS

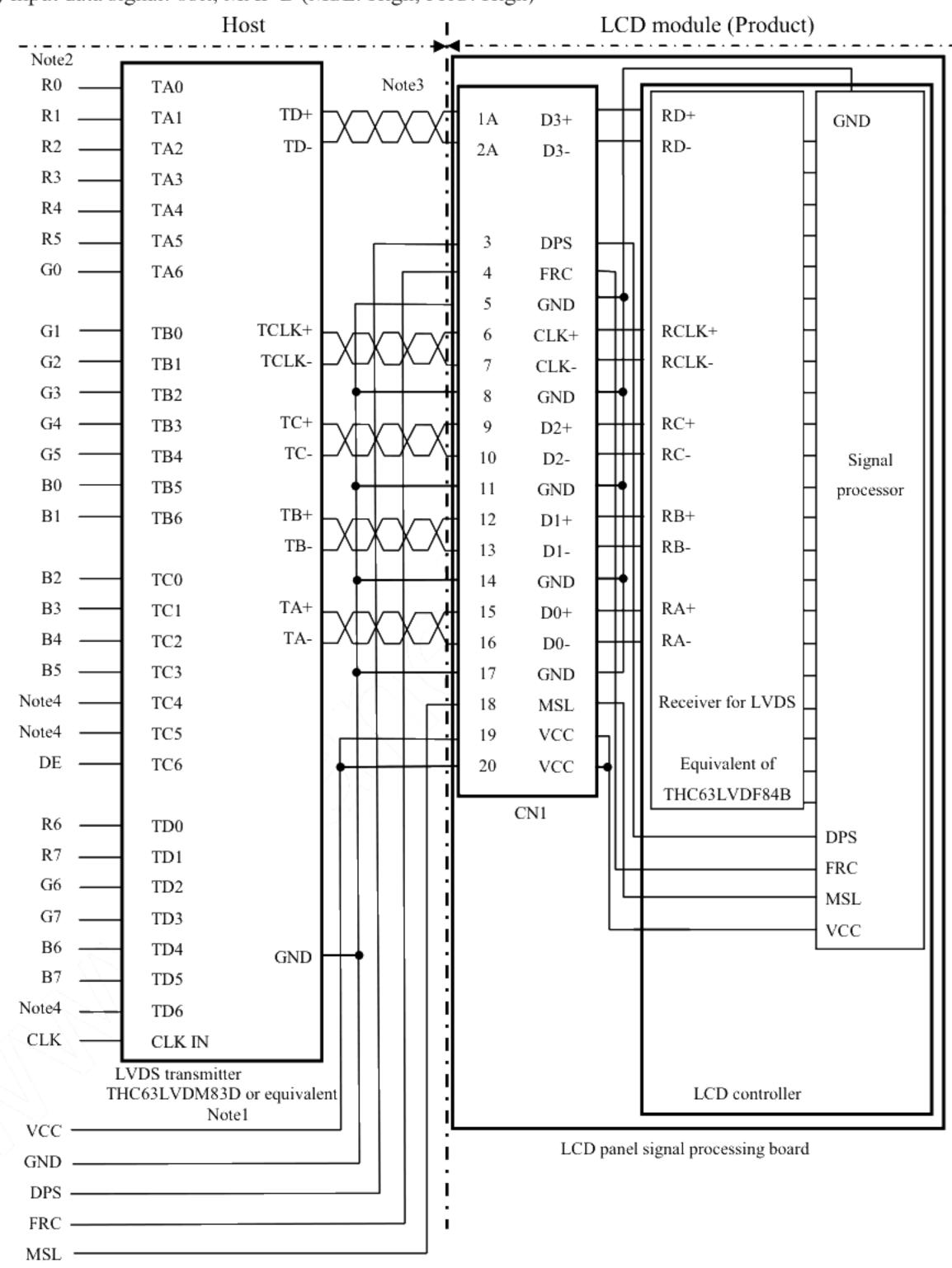


Note1: Recommended transmitter: THC63LVDM83D (THine Electronics Inc.) or equivalent

Note2: LSB (Least Significant Bit) – R0, G0, B0 MSB (Most Significant Bit) – R7, G7, B7

Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

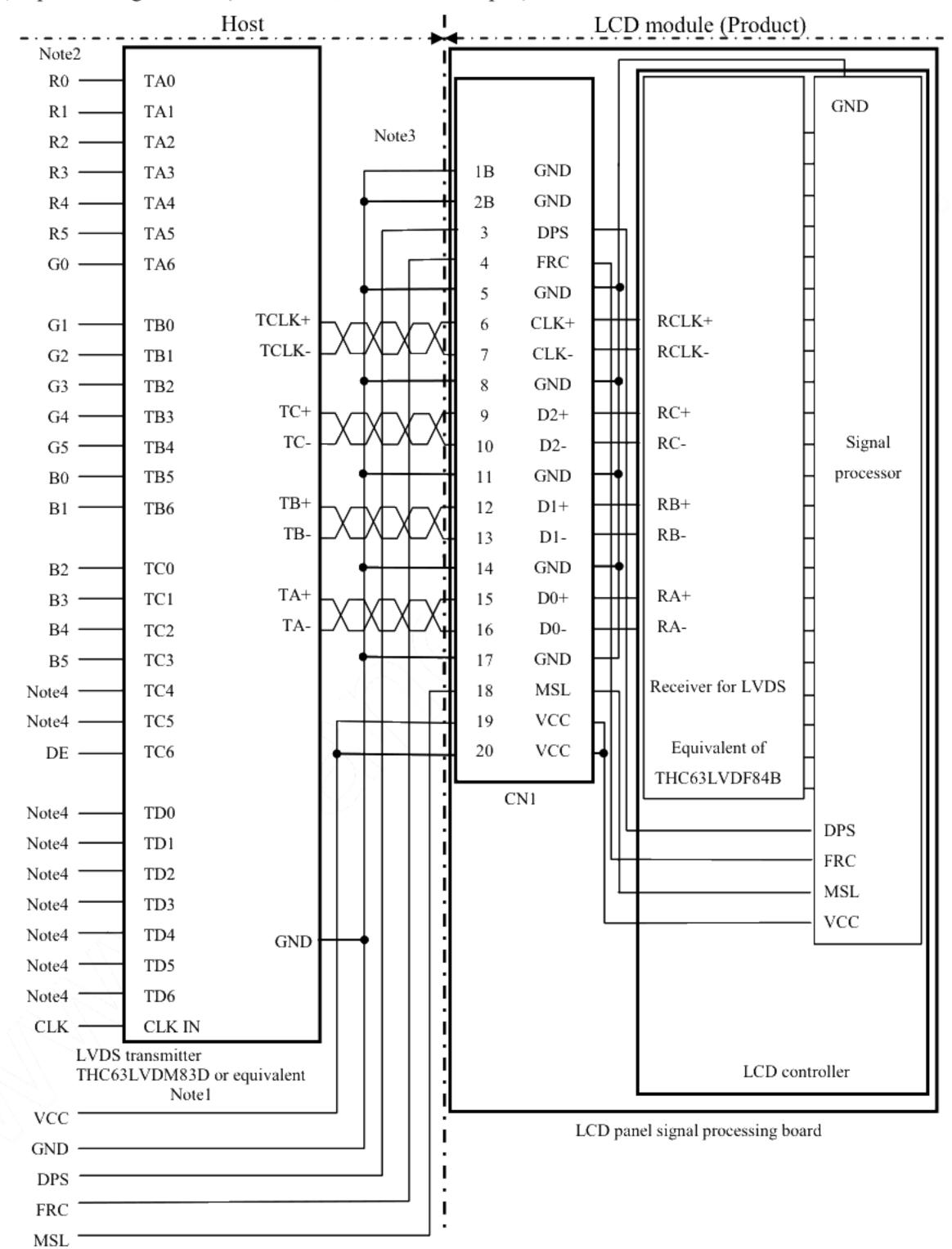
Note4: Input signals to TC4, TC5 and TD6 are not used inside the product, but do not keep them open to avoid noise problem.



(2) Input data signal: 8bit, MAP B (MSL: High, FRC: High)

- Note1: Recommended transmitter: THC63LVDM83D (THine Electronics Inc.) or equivalent
- Note2: LSB (Least Significant Bit) R0, G0, B0 MSB (Most Significant Bit) R7, G7, B7
- Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.
- Note4: Input signals to TC4, TC5 and TD6 are not used inside the product, but do not keep them open to avoid noise problem.

(3) Input data signal: 6bit (MSL: Low, FRC: Low or Open)



Note1: Recommended transmitter THC63LVDM83D (THine Electronics Inc.) or equivalent

Note2: LSB (Least Significant Bit) - R0, G0, B0 MSB (Most Significant Bit) - R5, G5, B5

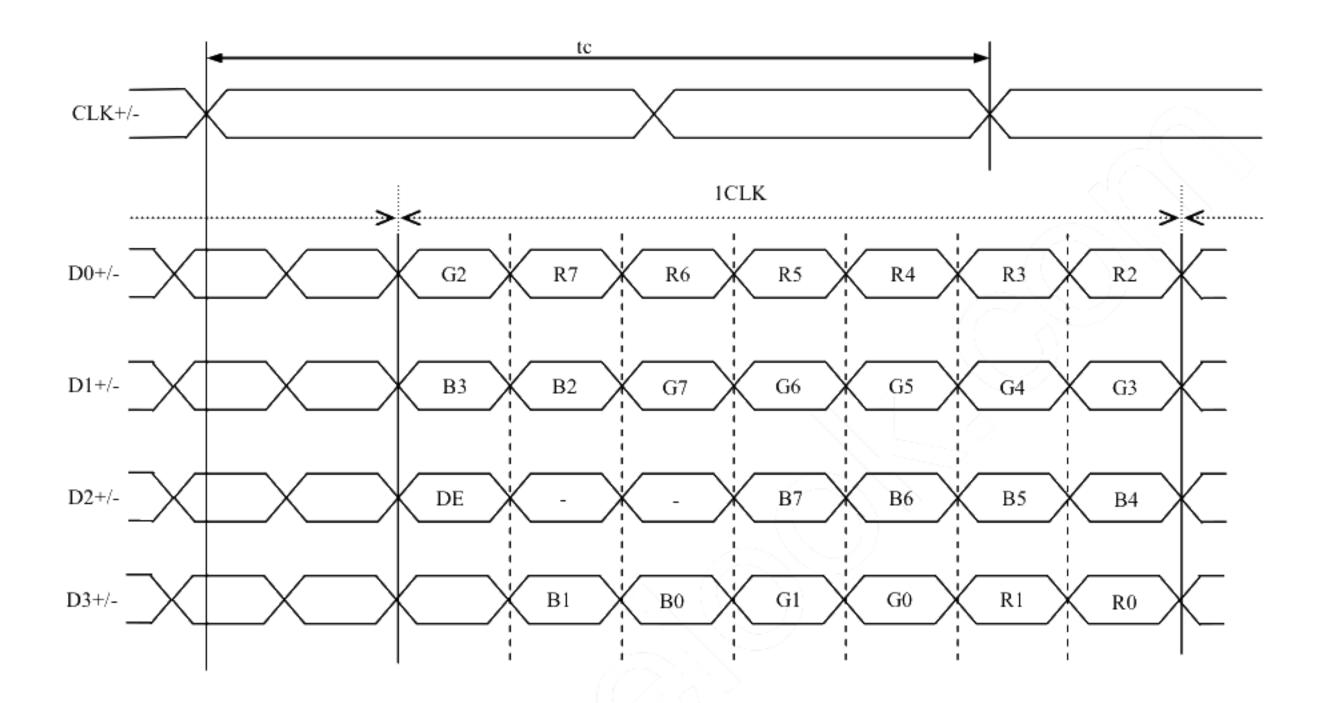
Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note4: Input signals to TC4, TC5 and TD0-6 are not used inside the product, but do not keep them open to avoid noise problem.

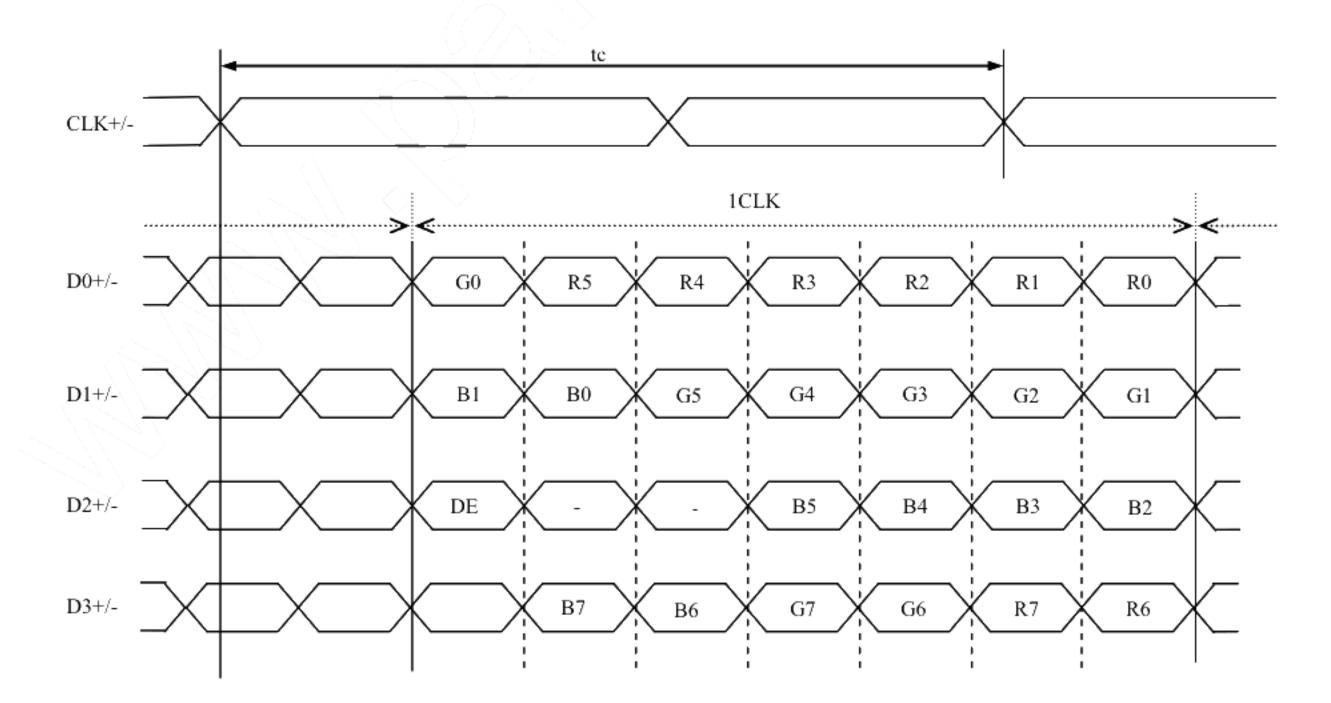


4.5.5 Input data mapping

(1) Input data signal: 8bit, MAP A



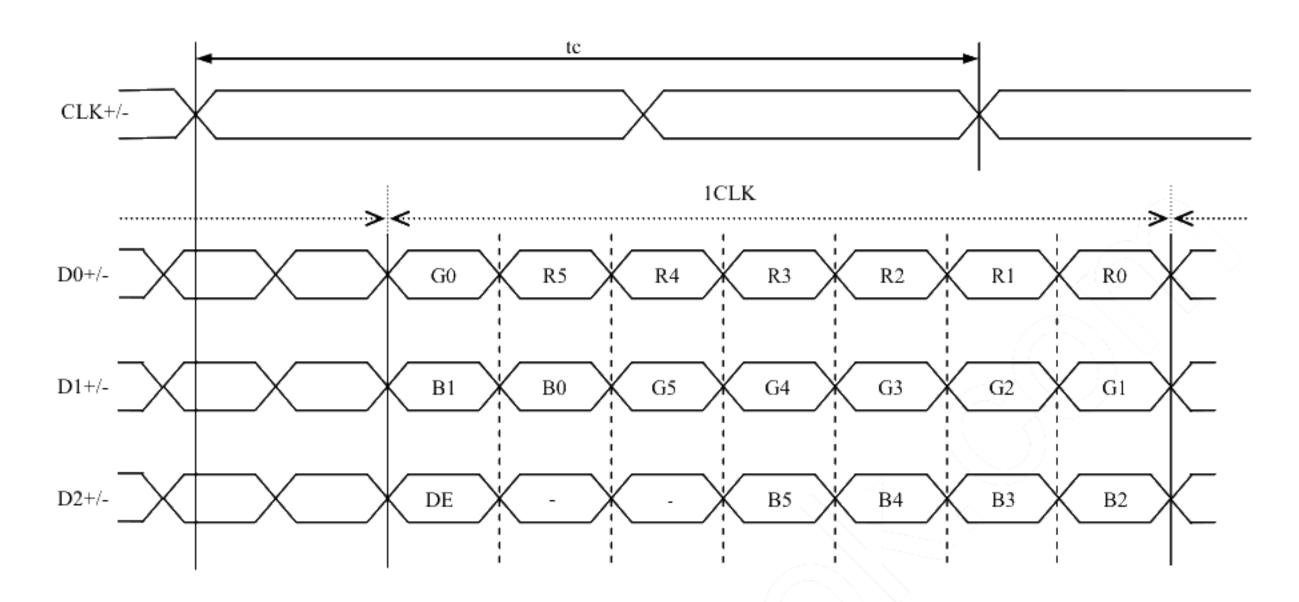
(2) Input data signal: 8bit, MAP B



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(3) Input data signal: 6bit



4.6 DISPLAY COLORS AND INPUT DATA SIGNALS

4.6.1 Combinations of input data signals, FRC and MSL signals

This product can display 16,777,216 colors equivalent with 256 gray scales and 262,144 colors with 64 gray scales by combination of input data signals, FRC and MSL signals. See the following table.

Combination	Input data signals	Input Data mapping	CN1- Pin No.1 and 2	FRC terminal	MSL terminal	Display colors	Remarks
1	8 bit	MAP A	D3+/-	High	Low	16,777,216	Note1
2	8 bit	MAP B	D3+/-	High	High	16,777,216	Note1
3	6 bit	$\Diamond \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$	GND	Low or Open	Low	262,144	Note2

Note1: See "4.6.2 16,777,216 colors". Note2: See "4.6.3 262,144 colors".



4.6.2 16,777,216 colors

This product can display 16,777,216 colors equivalent with 256 gray scales by combination ① or ②. (See "4.6.1 Combinations of input data signals, FRC and MSL signal".)

Also the relation between display colors and input data signals is as follows.

Dieni	lav galara	Data signal (0: Low level, 1: High level)							Dat	ta sig	gnal	(0: I	leve	Hig	vel)										
Disp	lay colors	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	Gl	G0	В7	В6	В5	В4	В3	B2	В1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
ors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Colors	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1.	1	. 1	1	1
Basic	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Ва	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	-1,	1	1	/1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	<u>) į – </u>	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
e e		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
scal	dark	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
gray scale	↑				:									() —								:			
d gr	↓ ↓				:									:\			<u> </u>					:			
Red	bright	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	ъ.,	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	<u>l</u>	<u>l</u>	1	1	<u>l</u>	<u>l</u>	<u>l</u>	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	-0~	0	0	0	0	0	0	0	0	0	0	0	0
ale		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
y sc	dark ◆	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
Green gray scale					:					\															
sen	\ h=:\:-1-4	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
Gre	bright	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Біаск	0	0	0	0	٥	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
ale	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
y sc	uai k ↑	Ň			Ϋ,		0	0	Ü						Ü	Ü	v	ľ	Ü				Ü	•	
Blue gray scale					Υ,																	:			
lue	bright	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
B	J.I.S.II.	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1



4.6.3 262,144 colors

This product can display 262,144 colors with 64 gray scales by combination ③. (See "4.6.1 Combinations of input data signals, FRC and MSL signal ".) Also the relation between display colors and input data signals is as follows.

Dien	lay colors						Dat	ta sign	al (0:	Low	level,	1: H	igh lev	vel)					
Disp	iay colors	R 5	R 4	R 3	R 2	R 1	R 0	G 5	G4	G3	G2	G 1	G 0	В5	B4	В3	В2	В1	B 0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
ors	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
loo	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	-1	1.	1	1
Basic colors	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Ba	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1,	1	_1	/ 1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
1 12		0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
scale	dark	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
ly s	↑			:												:			
gray	↓ ↓			:							:(:			
Red	bright	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
-		1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0.0	0	0	0	0	0	0	0	0	0	0	0
<u>e</u>		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
scale	dark	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
ray	↑ ↑			:							:					:			
Green gray	↓ ↓			:				Ь			:					:			
ree	bright	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
		0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green	0	0	_0	_0	_ 0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ا ە		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
scale	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
ay s	1 ↑										:					:			
gray :	↓ \			:							:					:			
Blue	bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
		0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1



4.7 DISPLAY POSITIONS

The following table is the coordinates per pixel (See "4.8 SCANNING DIRECTIONS".).

C (0,	0) B					
C(0, 0)	C(1, 0)		C(X, 0)		C(1278, 0)	C(1279, 0)
C(0, 1)	C(1, 1)		C(X, 1)		C(1278, 1)	C(1279, 1)
•	•	•		•	•	$\langle \cdot \rangle$.
•	•		•		•//	$\setminus \cdot \setminus \cdot$
•	•	•	•	•	• (\)) •
C(0, Y)	C(1, Y)		C(X, Y)		C(1278, Y)	C(1279, Y)
•	•	•		•		•
•	•		•	• • •	\•-\/	•
•	•	•	•	○•	•	•
C(0, 798)	C(1, 798)		C(X, 798)	• • •	C(1278, 798)	C(1279, 798)
C(0, 799)	C(1, 799)		C(X, 799)	/*·\•\	C(1278, 799)	C(1279, 799)

4.8 SCANNING DIRECTIONS

The following figures are seen from a front view.

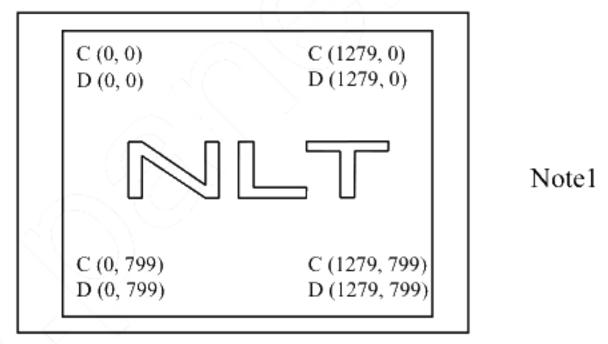


Figure 1. Normal scan (DPS: Low or Open)

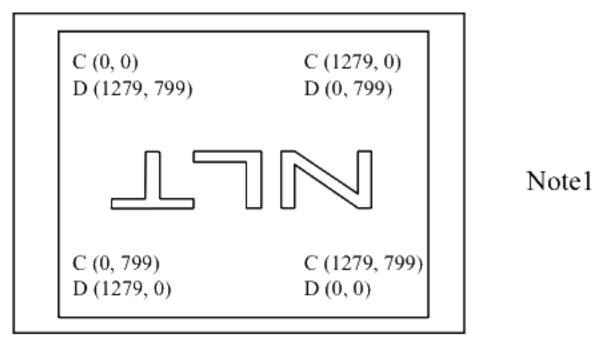


Figure 2. Reverse scan (DPS: High)

Note1: Meaning of C (X, Y) and D (X, Y)

C (X, Y): The coordinates of the display position (See "4.7 DISPLAY POSITIONS".)

D (X, Y): The data number of input signal for LCD panel signal processing board

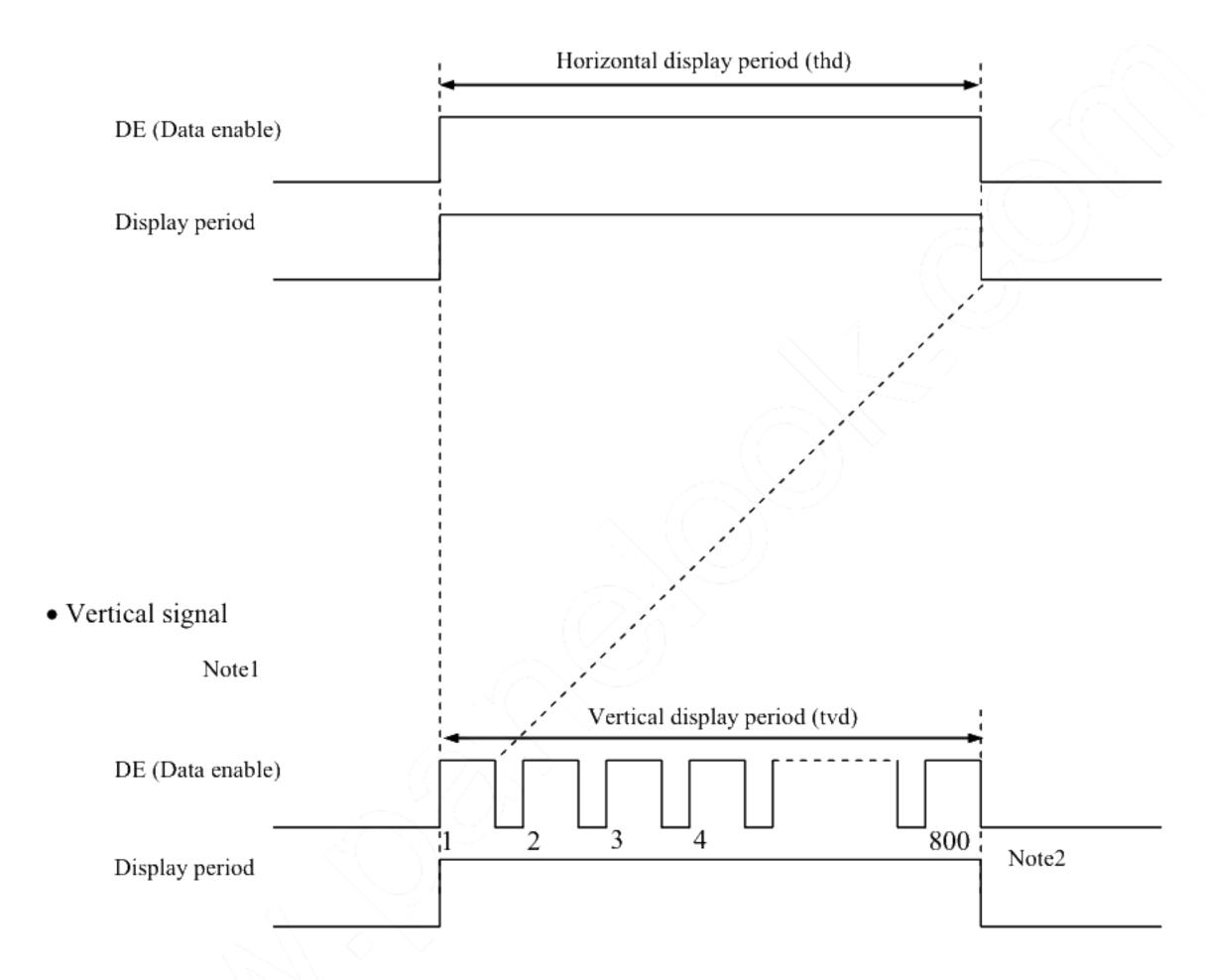


4.9 INPUT SIGNAL TIMINGS

4.9.1 Outline of input signal timings

• Horizontal signal

Note1



Note1: This diagram indicates virtual signal for set up to timing. Note2: See "4.9.3 Input signal timing chart" for the pulse number.



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4.9.2 Timing characteristics

(Note1, Note2, Note3)

	Parameter			min.	typ.	max.	Unit	Remarks	
	Frequency		Frequency		1/tc	67.0 71.0 75.0		MHz	14.085 ns (typ.)
CLK	Duty		-			-			
	Rise tim	-	-				-		
	CLK-DATA	Setup time	-				ns		
DATA	CLK-DATA	Hold time		_			ns		
	Rise tim	ne, Fall time	-						
		Cycle	th	17.20	20.28	21.49	μs		
	Horizontal	Cycle	ui	1290	1440	-	CLK	49.306 kHz (typ.)	
		Display period	thd	1280			CLK		
		Cycle	tv	14.16	16.69	17.69 /	ms		
DE	Vertical (One frame)	Сусіе	I V	-	823	- \	Н	59.92 Hz (typ.)	
	(one name)	Display period	tvd	800			Н		
	CLK-DE	Setup time	-			4	ns		
	CLK-DE	Hold time	-				ns	-	
	Rise time, Fall time		-				ns		

Note1: Definition of parameters is as follows.

tc=1CLK, th=1H

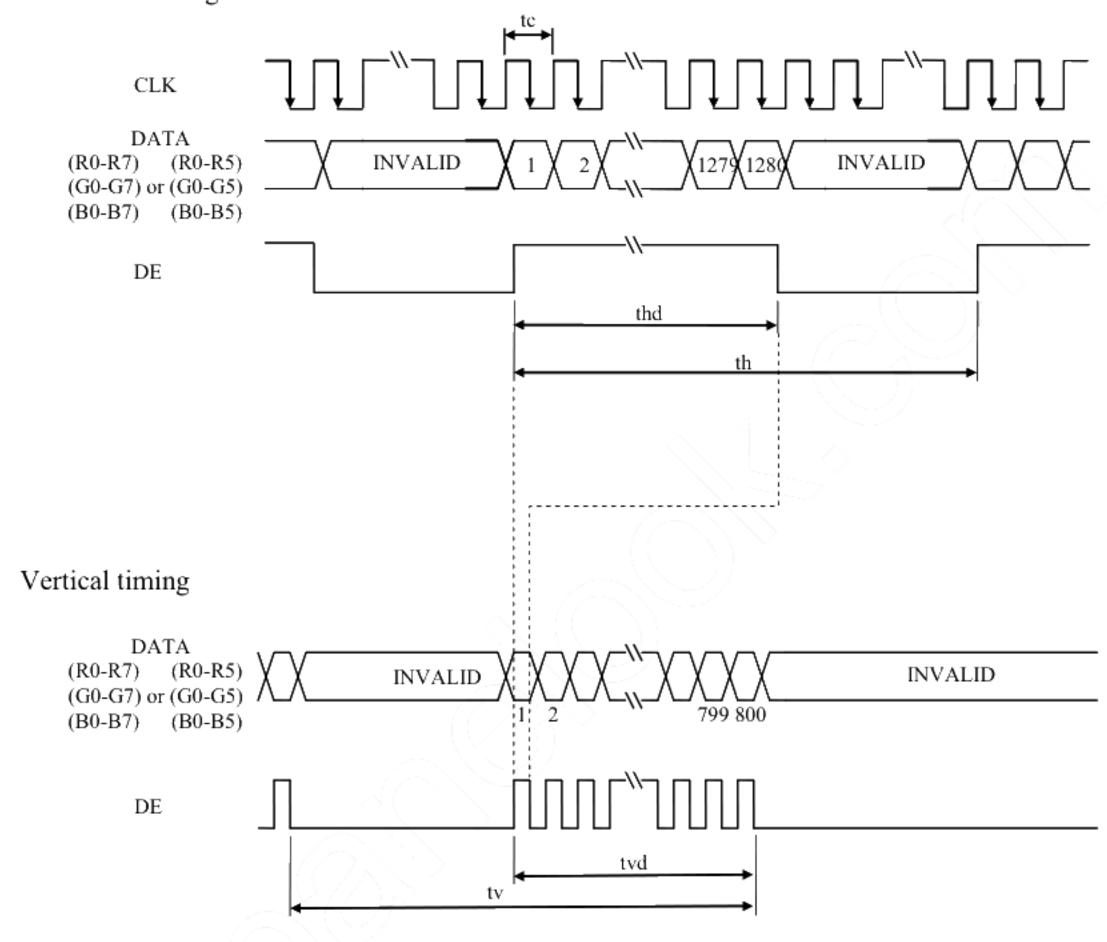
Note2: See the data sheet of LVDS transmitter.

Note3: Vertical cycle (tv) should be specified in integral multiple of Horizontal cycle (th).



4.9.3 Input signal timing chart

Horizontal timing



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4.10 OPTICS

4.10.1 Optical characteristics

(Note1, Note2)

Parameter		Condition	Symbol	min.	typ.	max.	Unit	Measuring instrument	Remarks
Luminance		White at center $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	L	(1300)	(1800)	-	cd/m ²	BM-5A	-
Contrast ratio		White/Black at center $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	CR	(500)	(800)	1	-	BM-5A	Note3
Luminance uni	formity	White $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	LU	ı	(1.25)	(1.4)	-	BM-5A	Note4
	N/ledan	x coordinate	Wx	0.263	0.313	0.363	-60		
	White	y coordinate	Wy	0.279	0.329	0.379	1-1		
	Red	x coordinate	Rx	-	TBD	-46	Y -X	-2/	
C1		y coordinate	Ry	-	TBD	-//	زاول		
Chromaticity	Green	x coordinate	Gx	-	TBD	- `		SR-3	Note5
		y coordinate	Gy	-	TBD	<u>-</u> .	-	SIX-3	Notes
	Di	x coordinate	Bx	- /	TBD	-	-		
	Blue	y coordinate	Ву	-((TBD	-	-		
Color gamut		θ R= 0°, θ L= 0°, θ U= 0°, θ D= 0° at center, against NTSC color space	C	35	40	-	%		
D		White to Black	Ton	_ - //	3	5	ms	BM-5A	Note6
Response time		Black to White	Toff	-	12	16	ms	-10000	Note7
	Right	θU= 0°, θD= 0°, CR≥ 10	θR	70	80	-	٥		
Viennie – – – 1-	Left	θU= 0°, θD= 0°, CR≥ 10	θL	70	80	-	٥	EZ	Note8
Viewing angle	Up	$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}, \ CR \ge 10$	θU	70	80	-	٥	Contrast	NOICO
	Down	$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}, \ CR \ge 10$	θD	70	80	-	0		

Note1: These are initial characteristics.

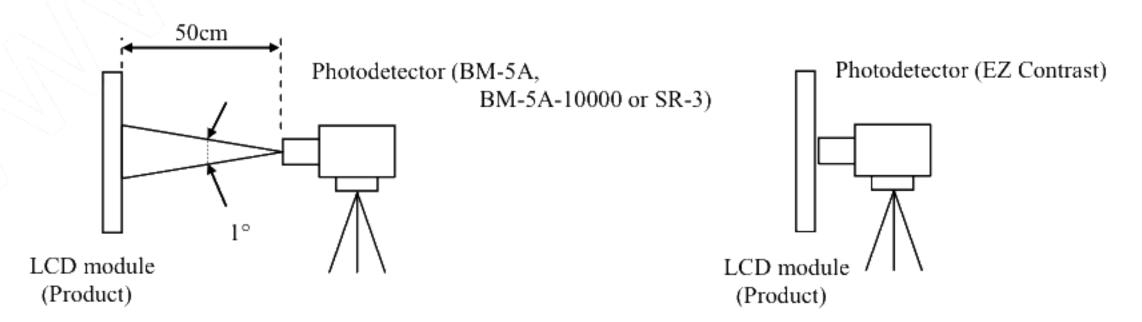
Note2: Measurement conditions are as follows.

Ta= 25°C, VCC= 3.3V, IL= (110)mA/One circuit, Display mode: WXGA,

Horizontal cycle= 1/49.306kHz, Vertical cycle= 1/59.92Hz,

DPS= Low or Open: Normal scan

Optical characteristics are measured at luminance saturation 20minutes after the product works in the dark room. Also measurement methods are as follows.



Note3: See "4.10.2 Definition of contrast ratio".

Note4: See "4.10.3 Definition of luminance uniformity".

Note5: These coordinates are found on CIE 1931 chromaticity diagram.

Note6: Product surface temperature: TopF= 40 °C

Note7: See "4.10.4 Definition of response times". Note8: See "4.10.5 Definition of viewing angles".

4



4.10.2 Definition of contrast ratio

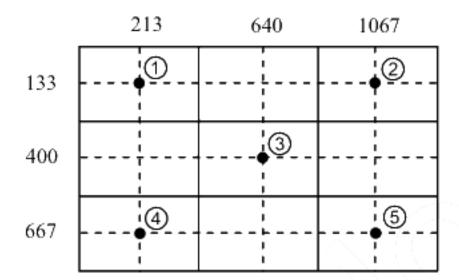
The contrast ratio is calculated by using the following formula.

4.10.3 Definition of luminance uniformity

The luminance uniformity is calculated by using following formula.

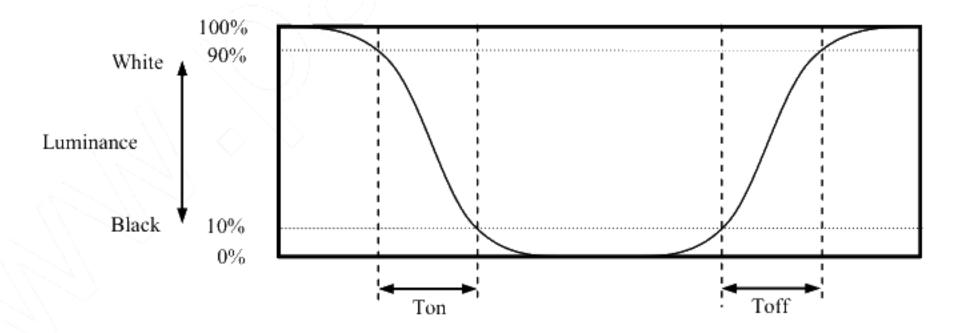
$$Luminance uniformity (LU) = \frac{Maximum luminance from ① to ⑤}{Minimum luminance from ① to ⑥}$$

The luminance is measured at near the 5 points shown below.

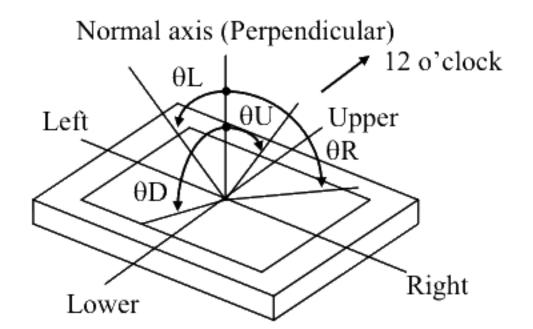


4.10.4 Definition of response times

Response time is measured at the time when the luminance changes from "white" to "black", or "black" to "white" on the same screen point, by photo-detector. Ton is the time when the luminance changes from 90% up to 10%. Also Toff is the time when the luminance changes from 10% down to 90% (See the following diagram.).



4.10.5 Definition of viewing angles





5. ESTIMATED LUMINANCE LIFETIME

The luminance lifetime is the time from initial luminance to half-luminance.

This lifetime is the estimated value, and is not guarantee value.

	Condition	Estimated luminance lifetime (Life time expectancy) Note1, Note2, Note3	Unit
LED elementary substance	25°C (Ambient temperature of the product) Continuous operation, IL= (110)mA/One circuit	60,000	h
	70°C (Ambient temperature of the product) Continuous operation, IL= (110)mA/One circuit	50,000	h



Note1: Life time expectancy is mean time to half-luminance.

Note2: Estimated luminance lifetime is not the value for LCD module but the value for LED elementary substance.

Note3: By ambient temperature, the lifetime changes particularly. Especially in case the product works under high temperature environment, the lifetime becomes short.

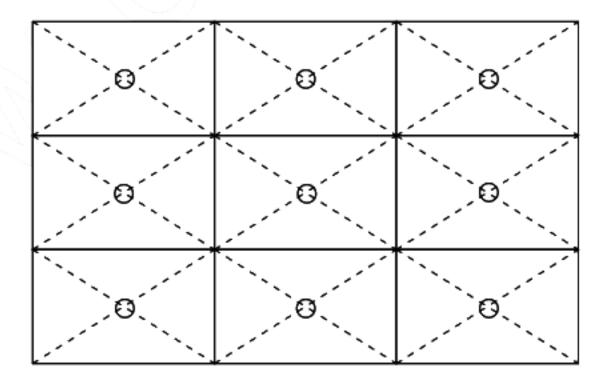


6. RELIABILITY TESTS

Test item	Condition	Judgment Note l		
High temperature and humidity (Operation)	① 60 ± 2°C, RH= 90%, 240hours ② Display data is black.			
High temperature (Operation)	 70 ± 3°C ,240hours Display data is black. 	No display malfunctions		
Heat cycle (Operation)	 ① -20 ± 3°C1hour 70 ± 3°C1hour ② 50cycles, 4 hours/cycle ③ Display data is black. 			
Thermal shock (Non operation)	 30 ± 3°C30minutes 80 ± 3°C30minutes 100cycles, 1hour/cycle Temperature transition time is within 5 minutes. 			
ESD (Operation)	 ① 150pF, 150Ω, ±10kV ② 9 places on a panel surface Note2 ③ 10 times each places at 1 sec interval 			
Dust (Operation)	 ① Sample dust: No. 15 (by JIS-Z8901) ② 15 seconds stir ③ 8 times repeat at 1 hour interval 			
Vibration (Non operation)	 5 to 100Hz, 19.6m/s² 1 minute/cycle X, Y, Z directions 120 times each directions 	No display malfunctions No physical damages		
Mechanical shock (Non operation)	 539m/ s², 11ms ±X, ±Y, ±Z directions 5 times each directions 			

Note1: Display and appearance are checked under environmental conditions equivalent to the inspection conditions of defect criteria.

Note2: See the following figure for discharge points.



7. PRECAUTIONS

7.1 MEANING OF CAUTION SIGNS

The following caution signs have very important meaning. Be sure to read "7.2 CAUTIONS" and "7.3 ATTENTIONS"!



This sign has the meaning that a customer will be injured or the product will sustain damage if the customer practices wrong operations.



This sign has the meaning that a customer will be injured if the customer practices wrong operations.

7.2 CAUTIONS



* Do not shock and press the LCD panel and the backlight! There is a danger of breaking, because they are made of glass. (Shock: Equal to or no greater than 539m/s² and equal to or no greater than 11ms, Pressure: Equal to or no greater than 19.6 N (\$\phi16mm jig))

7.3 ATTENTIONS



7.3.1 Handling of the product

- Take hold of both ends without touching the circuit board when the product (LCD module) is picked up from inner packing box to avoid broken down or misadjustment, because of stress to mounting parts on the circuit board.
- ② When the product is put on the table temporarily, display surface must be placed downward.
- 3 When handling the product, take the measures of electrostatic discharge with such as earth band, ionic shower and so on, because the product may be damaged by electrostatic.
- ④ The torque for product mounting screws must never exceed 0.230N·m. Higher torque might result in distortion of the bezel. And the length of product mounting screws must be ≤ 2.5mm.
- ⑤ The product must be installed using mounting holes without undue stress such as bends or twist (See outline drawings). And do not add undue stress to any portion (such as bezel flat area). Bends or twist described above and undue stress to any portion may cause display mura.
- ⑥ Do not press or rub on the sensitive product surface. When cleaning the product surface, wipe it with a soft dry cloth.
- ② Do not push or pull the interface connectors while the product is working.
- When handling the product, use of an original protection sheet on the product surface (polarizer) is recommended for protection of product surface. Adhesive type protection sheet may change color or characteristics of the polarizer.
- ① Usually liquid crystals don't leak through the breakage of glasses because of the surface tension of thin layer and the construction of LCD panel. But, if you contact with liquid crystal by any chance, please wash it away with soap and water.

7.3.2 Environment

- ① Do not operate or store in high temperature, high humidity, dewdrop atmosphere or corrosive gases. Keep the product in packing box with antistatic pouch in room temperature to avoid dusts and sunlight, when storing the product.
- ② In order to prevent dew condensation occurred by temperature difference, the product packing box must be opened after enough time being left under the environment of an unpacking room. Evaluate the storage time sufficiently because dew condensation is affected by the environmental temperature and humidity. (Recommended leaving time: 6 hours or more with the original packing state after a customer receives the package)
- ③ Do not operate in high magnetic field. If not, circuit boards may be broken.
- This product is not designed as radiation hardened.

7.3.3 Characteristics

The following items are neither defects nor failures.

- ① Characteristics of the LCD (such as response time, luminance, color uniformity and so on) may be changed depending on ambient temperature. If the product is stored under condition of low temperature for a long time, it may cause display mura. In this case, the product should be operated after enough time being left under condition of operating temperature.
- ② Display mura, flickering, vertical streams or tiny spots may be observed depending on display patterns.
- 3 Do not display the fixed pattern for a long time because it may cause image sticking. Use a screen saver, if the fixed pattern is displayed on the screen.
- The display color may be changed depending on viewing angle because of the use of condenser sheet in the backlight.
- ⑤ Optical characteristics may be changed depending on input signal timings.

7.3.4 Others

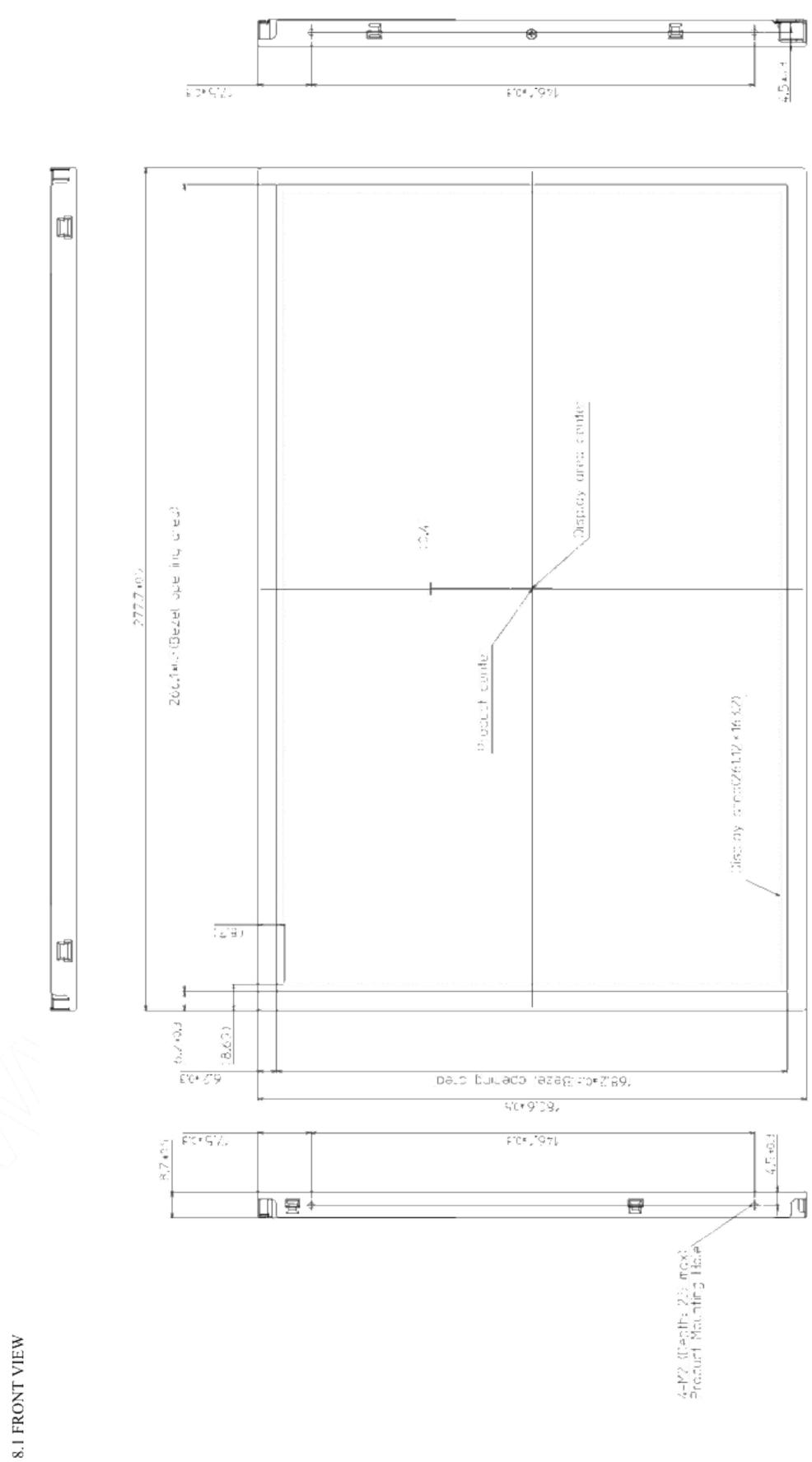
- ① All VCC and GND terminals should be used without any non-connected lines.
- ② Do not disassemble a product or adjust variable resistors.
- ③ See "REPLACEMENT MANUAL FOR LAMP HOLDER SET", when replacing lamp holder set.
- Pack the product with the original shipping package, in order to avoid any damages during transportation, when returning the product to NLT.

Unit: mm

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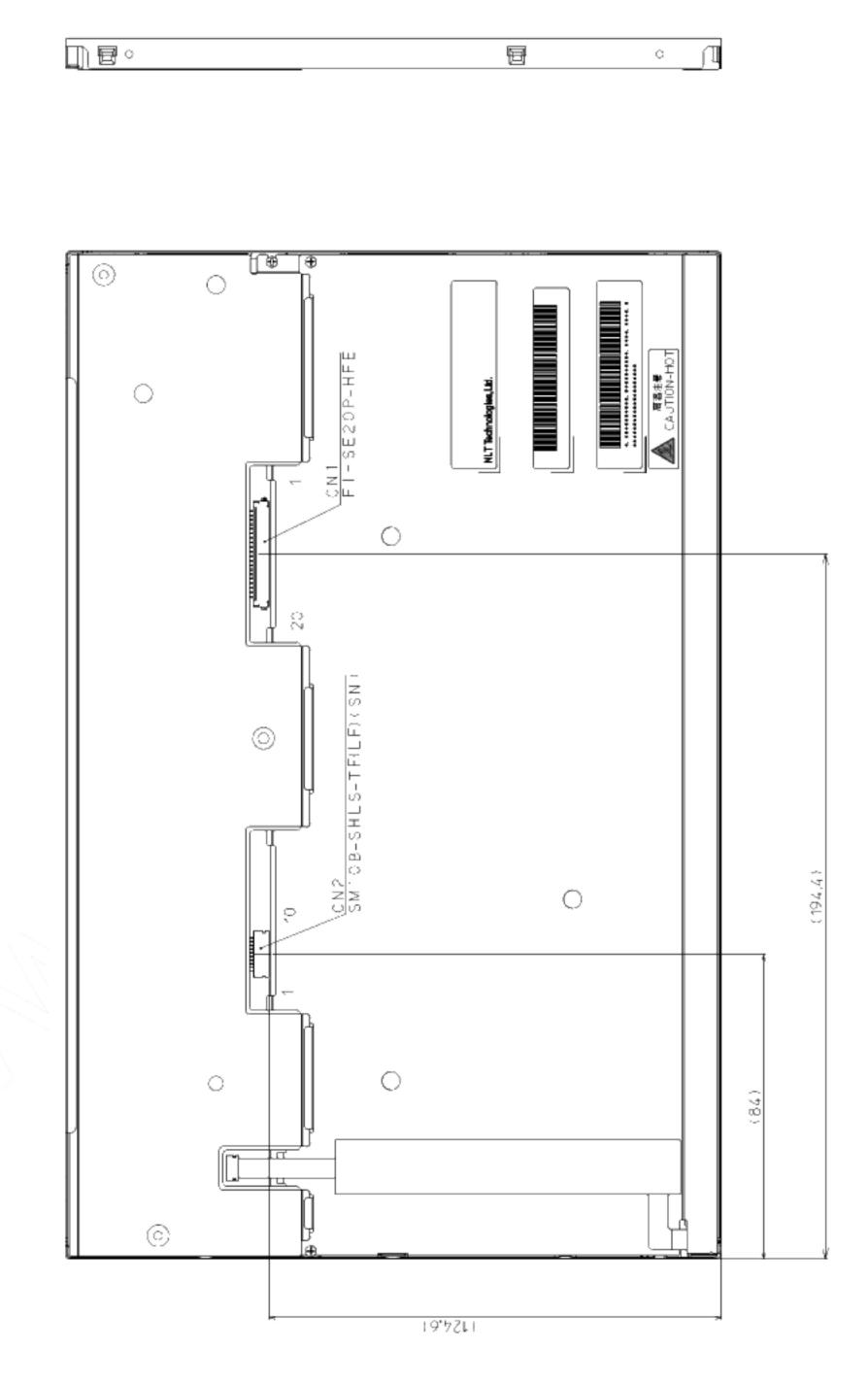
PRELIMINARY

8. OUTLINE DRAWINGS



Note 1: The values in parentheses are for reference. Note 2: The torque for product mounting screws must be ≤ 2.5 mm.

8.2 REAR VIEW



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Note1: The values in parentheses are for reference. Note2: The torque for product mounting screws must be ≤ 2.5 mm.

Unit: mm



REVISION HISTORY

The inside of latest specifications is revised to the clerical error and the major improvement of previous edition. Only a changed part such as functions, characteristic value and so on that may affect a design of customers, are described especially below.

Edition	Document number	Prepared date	Revision contents and signature					
1st edition	DOD-PP- 1678	June 7, 2013	Revision contents New issue					
			Writer	Charlette	Prepared by			
			Approved by R. KAWASHIMA	Checked by	A. KUMANO			
2nd	DOD-PP-	July 19,	Revision contents					
edition	1708	2013						
			CORRECTION OF DESCRIP	TIVE CONTENTS				
			P5 General specifications • Signal system					
				3LVDF84B, THine Electron	ics Inc. or equivalent			
			P6,7 Block diagram	7				
			• GND- FG: $47k\Omega \rightarrow 50k\Omega$	(change)				
			• Note3 (revised)					
			P5 General specifications, P8 I	-				
			• Weight: TBD g (typ.) → (
			• Note3 (addition)	ing board				
			P12 LCD panel signal process:	ing board				
			• Pin No.18- MSL					
			- Input data signal: 8bit-	MAP A, Input data signal: 6b	it: Low → Low or Open			
			P13 Positions of plug and sock	et				
			• Figure (addition)					
			P14-16 Connection between re					
			• (1) Input data signal: 861t, • TBD →THC63LVDF84	MAP A, (2) 8bit, MAP B and	a (3) Input data signal: ooit			
			P27 Estimated luminance lifeti					
				$A \rightarrow IL = (110) \text{mA}$ (correction	1)			
			P28 Reliability tests	, , ,	,			
			• Vibration: @ 30 times	. → 120 times (change)				
		11/2/11	P29 Precautions					
			Attentions					
			 Handling of the product 					
			· ·	N·m. Higher bezel. And .				
<			l .	The torque 0.23N·m. High	ner bezel. And ≤ 2.5 mm.			
			P31,32 Outline drawings					
10			 Front view and Rear view Figure (addition) 					
			Note1 and Note2 (addition)	on)				
			·	,				
			Writer	Charles I be	Prepared by			
			Approved by	Checked by	A. KUMANO			
			R. KAWASHIMA		A. KUMANO			
3rd edition	DOD-PP- 1761	Sep. 27, 2013	Revision contents					
			PRODUCT CHANGE					
			P7 Block diagram					
			 Note3: Backlight in detail 	(change)				



REVISION HISTORY

Edition	Document number	Prepared date	I.	Revision contents and si	gnature				
3rd	DOD-PP-	Sep. 27,	Revision contents						
edition	1761	2013	CORRECTION OF DESCRIPT	TIVE CONTENTS					
			P30 Attentions - Others	IIVE CONTENTS					
			• @ for repairing and s	o on (elimination)					
			Writer						
			Approved by	Checked by	Prepared by				
			R. KAWASHIMA		E. YOSHIMURA				
th	DOD-PP-	Feb. 21,	Revision contents						
lition	1852	2014	P5 GENERAL SPECIFICATION	MC					
			• Weight: (490) g (typ.) \rightarrow (490)						
			• Response time: (18) ms (ty						
			 Luminance: 1,500 cd/m² (t 	yp.) \to (1,800) cd/m ² (ty	•				
			Backlight- Lamp holder se						
			Power consumption: TBD P8 DETAILED SPECIFICATION		р.)				
			Mechanical specifications						
			 Weight: (490), TBD (typ., max.) g → (460), (490) (typ., max.) g 						
			Absolute maximum ratings						
			 Backlight- Forward current: TBD mA → 130 mA P9 Electrical characteristics 						
			LCD panel signal processing						
			Power supply current: TE	90), (820) (typ., max.) mA					
			P10 Fuse						
			 VCC: TBD → specified P11 Power supply voltage sequ 	ence					
			LCD panel signal processing						
			• VCC: TBD → specified	.6 00.11					
			: 0.1VCC (addition)						
			Display signals, Function LED driver (Title is above.)		ed				
			LED driver (Title is change P25 Optics	ed)					
		\wedge	Optical characteristics						
			• Luminance: TBD, 1500 (min., typ.) $cd/m^2 \rightarrow (130)$	00), (1800) (min., typ.) cd/m ²				
	200		Contrast ratio: TBD (min	. ,	`				
			 Luminance uniformity: T Color gamut: TBD (min.) 		3)				
			• Response time- Ton: (3),		3, 5 (typ., max.) ms				
			- Toff: (15), TBD (typ., max.) ms -	→ 12, 16 (typ., max.) ms				
(Viewing angle- Right, Le		ı.) ° → 70 (min.) °				
			Note6: TopF= TBD °C — P27 ESTIMATED LUMINAN						
			LED elementary substance		n)				
	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		P32 OUTLINE DRAWINGS		,				
			Rear view: Labels were add	ded.					
			Signature of writer						
			Approved by	Checked by	Prepared by				
			12 . Harrashina		A V				
			R. KAWASHIMA		A. KUMANO				