PRELIMINARY

NEC NEC LCD Technologies, Ltd.

TFT COLOR LCD MODULE

NL4864HL11-01B

8.9cm (3.5 Type)
VGA

PRELIMINARY DATA SHEET

DOD-PP-0182 (2nd edition)

This PRELIMINARY DATA SHEET is updated document from DOD-PP-0144(1).

All information is subject to change without notice. Please confirm the sales representative before starting to design your system.

INTRODUCTION

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Examples: Military systems, aircraft control equipment, aerospace equipment, nuclear reactor control systems, medical equipment/devices/systems for life support, etc.

The quality grade of this product is the **"Standard"** unless otherwise specified in this document.

CONTENTS

1. OUTLINE

1.1 STRUCTURE AND PRINCIPLE

Color LCD module NL4864HL11-01B is composed of the low temperature poly silicon thin film transistor liquid crystal display (LTPS TFT LCD) panel structure with driver LSIs for driving the TFT (Thin Film Transistor) array, touch panel (T/P) and a backlight.

The LTPS TFT LCD panel structure is injected liquid crystal material into a narrow gap between the TFT array glass substrate and a color-filter glass substrate.

Color (Red, Green, Blue) data signals from a host system (e.g. signal generator, etc.) are modulated into best form for active matrix system by a controller, and sent to the driver LSIs which drive the individual TFT arrays.

The TFT array as an electro-optical switch regulates the amount of transmitted light from the backlight assembly, when it is controlled by data signals. Color images are created by regulating the amount of transmitted light through the TFT array of red, green and blue dots.

1.2 APPLICATION

- PDAs

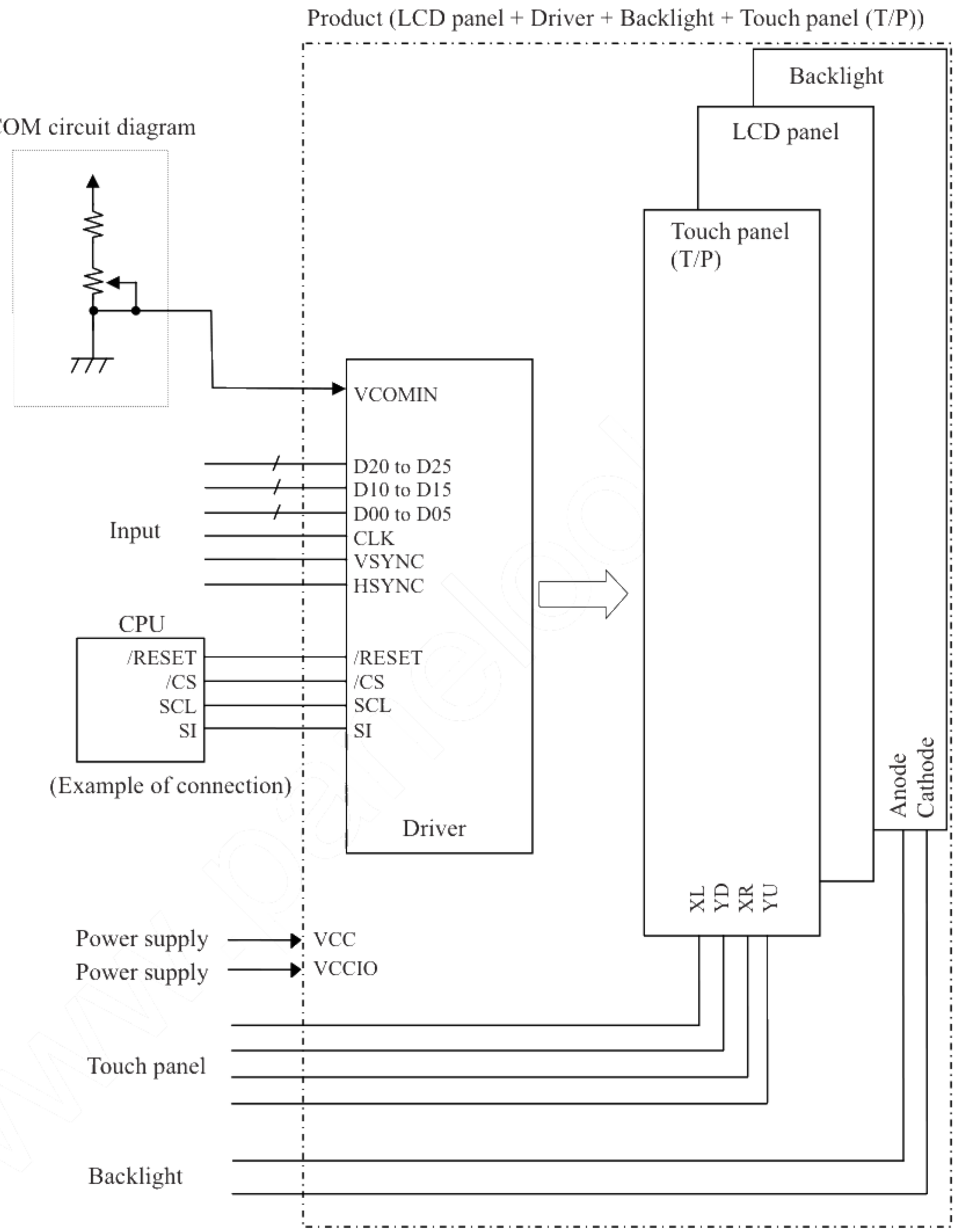
1.3 FEATURES

- Adoption of SR-NLT (Super-Reflective Natural Light TFT) (Transflective type)
- Multi resolution (VGA or QVGA)
- Backlight and touch panel attached
- Including LCD controller and power supply

2. GENERAL SPECIFICATIONS

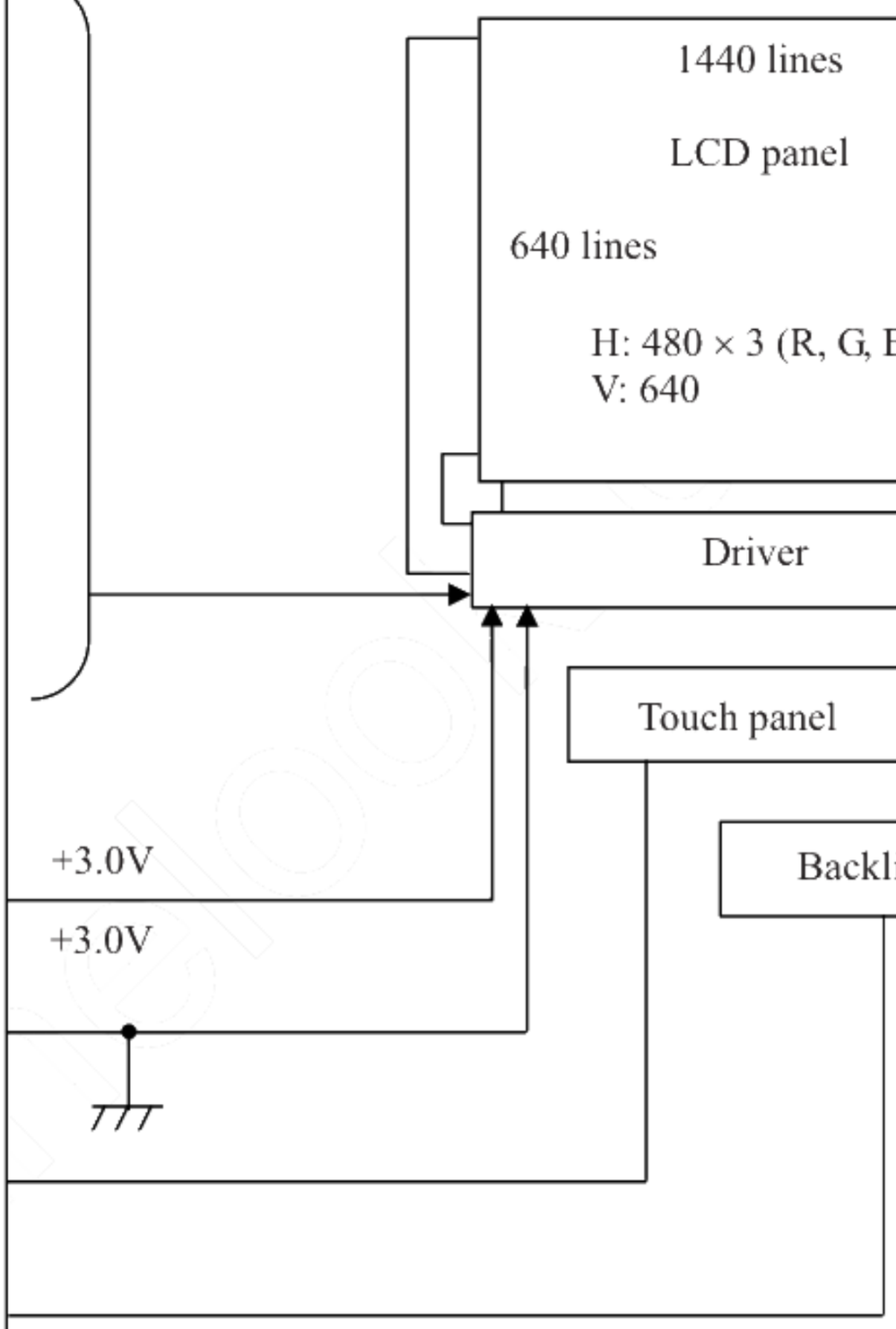
Display area	53.28 (H) × 71.04 (V) mm
Diagonal size of display	8.9cm (3.5 inches)
Drive system	LTPS TFT active matrix
Display color	262,144 colors
Pixel	480 (H) × 640 (V) pixels
Pixel arrangement	RGB (Red dot, Green dot, Blue dot) vertical stripe
Dot pitch	0.037 (H) × 0.111 (V) mm
Pixel pitch	0.111 (H) × 0.111 (V) mm
Module size	(63.5) (H) × (85.0) (V) × (4.2) (D) mm (typ.)
Weight	(43)g (typ.)
Touch panel surface	Clear
Touch panel pencil-hardness	3H (min.) [by JIS K5400]
Designed viewing direction	• Viewing direction without image reversal: lower side (6 o'clock) • Viewing direction with contrast peak: up side (12 o'clock)
Luminance	*At IL= 20mA, With Touch panel* (200)cd/m^2 (typ.)
Reflection ratio	*With Touch panel* (TBD)% (typ.)
Contrast ratio	*At IL= 20mA, with Touch panel* (TBD) (typ.)
	At reflective mode, with Touch panel (TBD) (typ.)
Response time	*Ton+Toff (10%←→ 90%)* (TBD) ms (typ.)
Signal system	6-bit digital signals for data of RGB colors, Dot clock (CLK), Horizontal synchronous signal (HSYNC), Vertical synchronous signal (VSYNC) Serial interface (SPI correspondence) (/CS, SCL, SI)
Supply voltage	VCCIO: 3.0V (typ.) VCC: 3.0V (typ.)
Power consumption	LCD panel + Driver: (TBD)mW (typ.) Backlight: (512)mW (typ., at IL= 20mA)

3. BLOCK DIAGRAM

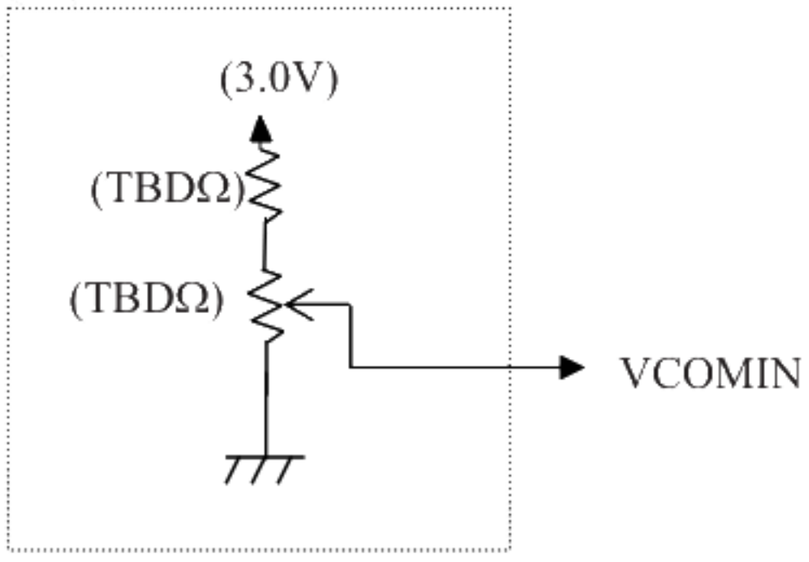


FPC I/F

Pin No.	Symbol
13 to 18	D00 to D05
19 to 24	D10 to D15
25 to 30	D20 to D25
10	VSYNC
9	HSYNC
11	CLK
8	/RESET
35	/CS
32	SCL
33	SI
36	VCOMIN
37	VCCIO
3,4,5,6	VCC
1,2,7,12 31,38,43	GND
39,40, 41,42	XL, YD XR, YU
44	ANODE
45	CATHODE



Reference design of COM circuit



4. DETAILED SPECIFICATIONS

4.1 MECHANICAL SPECIFICATIONS

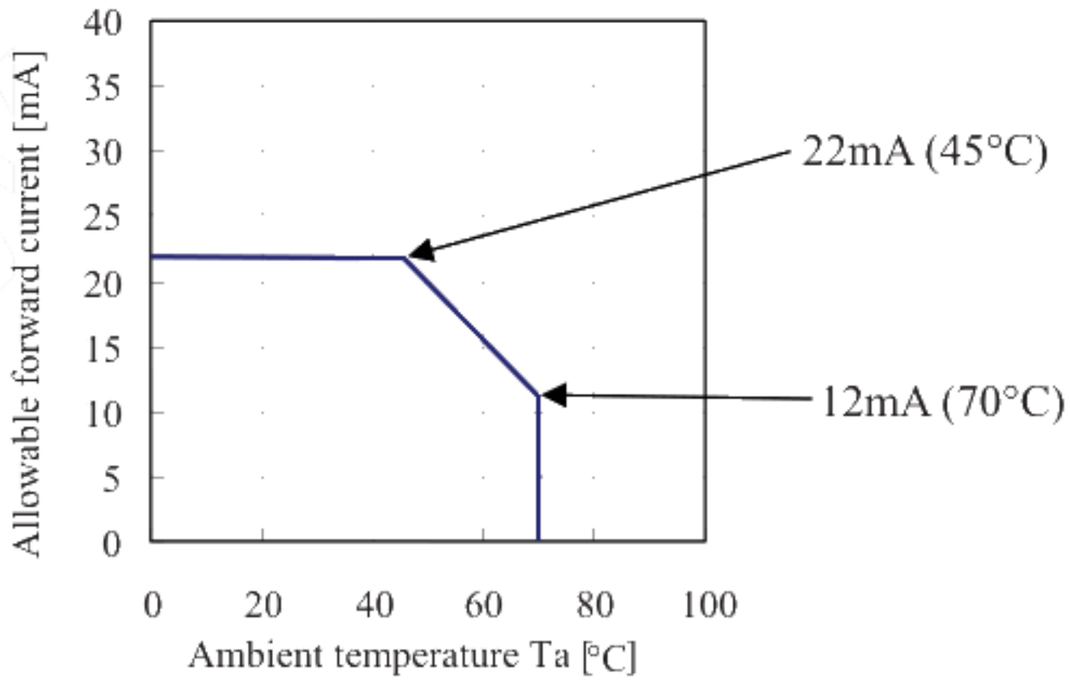
Parameter	Specification		Unit
Module size	(63.5) ± 0.3 (W) × (85.0) ± 0.3 (H) × (4.2) ± 0.2 (D) Note1	Note2	mm
Display area	53.28 (H) × 71.04 (V)	Note2	mm
Weight	(43) (typ.), TBD (max.)		g

Note1: Excluding FPC
Note2: See "**11. OUTLINE DRAWINGS**".

4.2 ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Rating	Unit	Remarks
Supply voltage (DC/DC)		VCC	-0.3 to + 3.7	V	Ta= 25°C
Supply voltage (Logic)		VCCIO	-0.3 to + 6.0	V	Ta= 25°C
Logic input voltage		VI	-0.3 to VCCIO + 0.3	V	Logic signals
Backlight	Reverse voltage	VR	(≤ 40)	V	Ta= 25°C
	Power dissipation	PD	(≤ 984)	mW	
	Forward current	IL	Note1	mA	
	Pulse forward current	IFP	(100)	mA	Pulse width ≤ 10ms, Duty ≤ 1/10
Storage temperature		Tst	(-30 to +80)	°C	-
Operating temperature		Top	(-20 to +70)		Product surface Note2
Relative humidity Note3		RH	≤ 95	%	Ta≤ 40°C
			≤ 85		40°C <Ta≤ 50°C
			≤ 55		50°C <Ta≤ 60°C
Absolute humidity Note3		AH	≤ 71 Note4	g/m^3	Ta> 60°C
Storage altitude			≤ 13,600	m	-30°C ≤ Ta ≤ 80°C
Operating altitude			≤ 4,850	m	-20°C ≤ Ta ≤ 70°C

Note1: Allowable forward current



Note2: Measured at display area
Note3: No condensation
Note4: Water amount at Ta= 60°C and RH= 55%

4.3 ELECTRICAL CHARACTERISTICS

(1) Logic/ LCD driving

(Ta= 25°C)

Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Supply voltage (DC/DC)	VCC	TBD	(3.0)	TBD	V	-
Supply voltage (Logic)	VCCIO	(1.65)	(3.0)	(3.3)	V	-
Logic input high voltage	VIH	0.8VCCIO	-	VCCIO	V	Logic signal
Logic input low voltage	VIL	0	-	0.2VCCIO	V	
VCOM input voltage	VCOMIN	-	TBD	-	V	at VCC= 3.0V Note1
VCC supply current	ICC	-	TBD	TBD	mA	at VCC= 3.0V Note2
VCCIO supply current	ICCIO	-	TBD	TBD	mA	at VCCIO= 3.0V Note2

Note1: The optimum value for VCOMIN is in the range of TBD V to TBD V.
Note2: PPCLK= (19.6)MHz, PPHSYNC= (38.88)kHz, PPVSYNC= (60)Hz,
Checkered flag pattern (by EIAJ ED-2522)

(2) Backlight

(Ta= 25°C)

Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Forward Current	IL	-	(20)	(22)	mA	-
Forward Voltage	VL	-	(25.6)	(28.0)	V	at IL= 20mA

(3) Touch panel

(Ta= 25°C)

Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Touch panel input voltage	Vtp	-	-	(5.5)	V	-
Resistor between pins(XL-XR)	Rx	(190)	-	(500)	Ω	-
Resistor between pins(YU-YD)	Ry	(140)	-	(540)	Ω	-
Line linearity (X direction)	Xlin	-	-	(1.5)	%	-
Line linearity (Y direction)	Ylin	-	-	(1.5)	%	-
Insulation resistance	Rins	(20)	-	-	MΩ	at DC 25V
Static Capacitance	Ctp	-	-	(100)	nF	-
Chattering	Chat	-	-	(10)	ms	-
Operation starting force	Ost	-	-	(0.78)	N	-
				(80)	gf	
Surface hardness	Hs	3	-	-	H	Pencil hardness
Point hitting life	Lhr	1,000,000	-	-	times	Silicon rubber: R8mm, Hardness 60° Load: 2.94N(300gf)

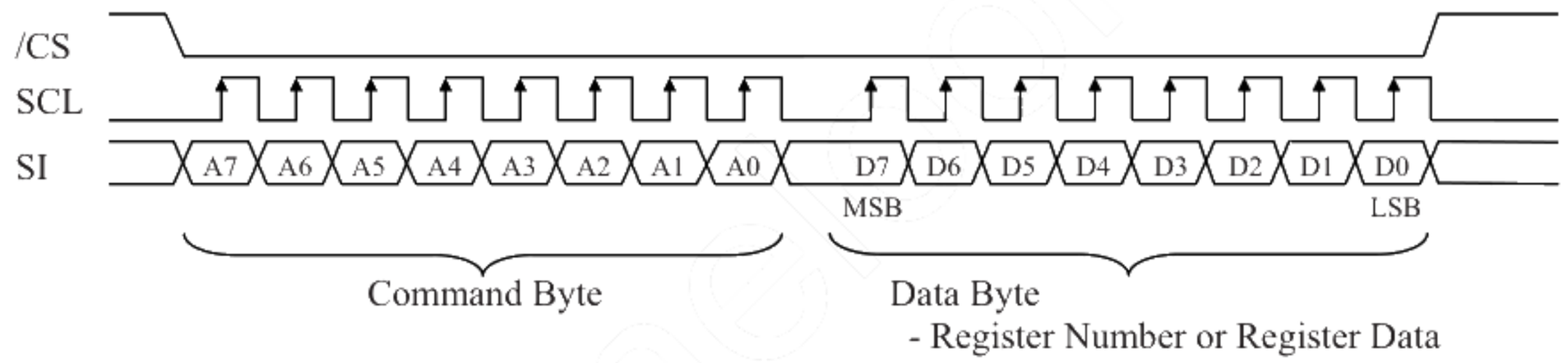
4.4 SETTING OF THE INTERNAL RESISTER

Initial setting of the internal Resister is undefined data. So the Resister Data must be written in the Resister, after initialization by the /RESET pin. The Resister Data can be written from serial interface pins (/CS, SCL and SI). This serial interface supports SPI. The setting method is as follows.

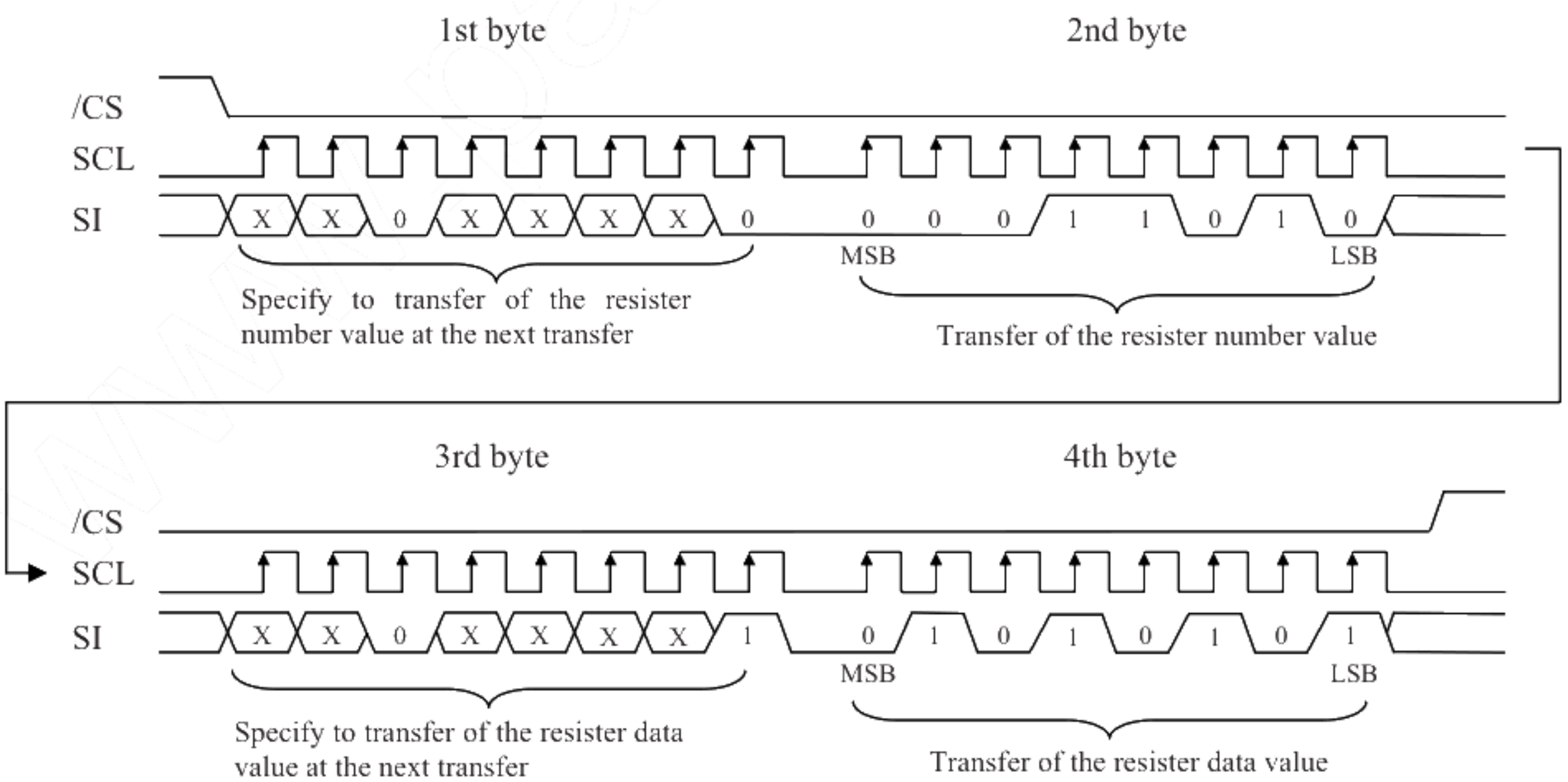
(1) Command Byte Function

Bits	Functions	Discription
A7	-	-
A6	-	-
A5	Read / Write	0:Write 1:Read
A4	-	-
A3	-	-
A2	-	-
A1	-	-
A0	Register Number / Data	0:Register Number 1:Register Data

(2) Timing chart



Ex) When data 55h is written to Register R26 (R1Ah)



Note1: During 32-bit transfer of the Resister Data, /CS pin (Pin No.35) must be maintained active.
Note2: "X" is set in accordance with the usage conditions.
Note3: Please transmit the data transfer every 32 bits.

Command sequence

2

① Power On

Sequence	Register Number	Data	Comment	Sequence	Register Number	Data	Comment
1	Power On			33	R44	(05h)	-
2	200us min. wait.			34	R45	(0Eh)	-
3	R3	(01h)	-	35	R46	(16h)	-
4	R0	(00h)	-	36	R47	(0Bh)	-
5	R1	(09h)	-	37	R48	(0Ah)	-
6	R4	(07h)	-	38	R49	(19h)	-
7	R5	(14h)	-	39	R50	(03h)	-
8	R6	(24h)	-	40	R51	(02h)	-
9	R16	(D7h)	-	41	R52	(02h)	-
10	R17	(03h)	-	42	R53	(02h)	-
11	R18	(00h)	-	43	R55	(04h)	-
12	R19	(55h)	-	44	R58	(04h)	-
13	R20	(01h)	-	45	R80	(02h)	-
14	R21	(70h)	-	46	R83	(42h)	-
15	R22	(0Ch)	-	47	R84	(42h)	-
16	R23	(2Eh)	-	48	R85	(4Fh)	-
17	R24	(2Eh)	-	49	R86	(1Eh)	-
18	R25	(FFh)	-	50	R89	(08h)	-
19	R26	(02h)	-	51	R90	(01h)	-
20	R27	(80h)	-	52	R91	(00h)	-
21	R32	(2Bh)	-	53	R92	(0Fh)	-
22	R33	(05h)	-	54	R93	(0Ah)	-
23	R34	(0Eh)	-	55	R94	(57h)	-
24	R35	(16h)	-	56	R95	(18h)	-
25	R36	(0Bh)	-	57	R98	(3Bh)	-
26	R37	(0Ah)	-	58	R99	(18h)	-
27	R38	(19h)	-	59	R102	(1Fh)	-
28	R39	(03h)	-	60	R103	(18h)	-
29	R40	(02h)	-	61	R106	(02h)	-
30	R41	(02h)	-	62	R116	(0Bh)	-
31	R42	(02h)	-	63	20us min. wait		
32	R43	(0Bh)	-	64	R2	(00h)	-

② Power Off

Sequence	Register Number	Data	Comment
1	R16	05h	-
2	20 us min. wait.		
3	R16	01h	-
4	20 us min. wait.		
5	R16	00	
6	20 us min. wait.		
7	R3	01h	-
8	Power Off		

4.5 INTERFACE PIN CONNECTIONS

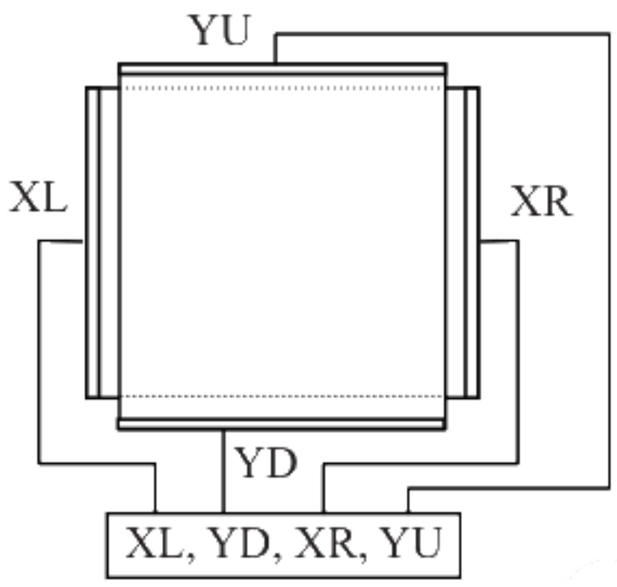
CN1 (FPC)
Adaptable socket: FH23-45S-0.3SHW(05) (Hirose Electric Co., Ltd.(HRS))

Pin No.	Symbols	Functions	Pin No.	Symbols	Functions
1	GND	Ground Note1	25	D20	Red data (LSB)
2	GND	Ground Note1	26	D21	Red data
3	VCC	Power supply Note1	27	D22	Red data
4	VCC		28	D23	Red data
5	VCC		29	D24	Red data
6	VCC		30	D25	Red data (MSB)
7	GND	Ground Note1	31	GND	Ground Note1
8	/RESET	Reset	32	SCL	Serial clock
9	HSYNC	Horizontal synchronous signal	33	SI	Serial input
10	VSYNC	Vertical synchronous signal	34	RSVD	Keep this pin Open.
11	CLK	Dot clock	35	/CS	Chip selection
12	GND	Ground Note1	36	VCOMIN	COM high voltage input
13	D00	Blue data (LSB)	37	VCCIO	Power supply (Logic)
14	D01	Blue data	38	GND	Ground Note1
15	D02	Blue data	39	XL	Horizontal terminal (Left side)
16	D03	Blue data	40	YD	Vertical terminal (Down side)
17	D04	Blue data	41	XR	Horizontal terminal (Right side)
18	D05	Blue data (MSB)	42	YU	Vertical terminal (Up side)
19	D10	Green data (LSB)	43	GND	Ground Note1
20	D11	Green data	44	ANODE	LED voltage (Anode)
21	D12	Green data	45	CATHODE	LED voltage (Cathode)
22	D13	Green data			
23	D14	Green data			
24	D15	Green data (MSB)			

Note1: All GND terminals should be used without any non-connected lines.
Note2: Do not fold the FPC. When folding the FPC, pattern disconnection may occur. In case of bending FPC, the minimum curvature (R) must be more than 1.0 mm.

Description of terminals

Terminals	Description
/RESET	When /RESET is L, an internal reset is performed. The reset operation is executed at the /RESET signal level. Be sure to perform reset via this pin at power application.
/CS	This pin is used for chip select signals. When /CS= L, the chip is active and can perform data I/O operations including command and data I/O.
SCL	This pin is clock input of serial interface.
SI	This pin is data input of serial interface.
VCOMIN	This pin is the Common input voltage. The voltage needs to be adjusted. The details are explained the above. See **"3 BLOCK DIAGRAM - Reference design of COM circuit"**.
YU,XR,YD,XL	Refer to the below "**Circuits of touch panel**".
ANODE, CATHODE	Refer to the below "**Circuits of backlight**".



Circuits of touch panel

Circuits of backlight

4.6 DISPLAY COLORS AND INPUT DATA SIGNALS

This product can display in equivalent to 262,144 colors in 64 gray scales. Also the relation between display colors and input data signals is as the following table.

Display colors		Data signal (0: Low level, 1: High level)																	
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red gray scale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	dark	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑				:					:						:			
	↓				:					:						:			
	bright	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green gray scale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	dark	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	↑				:					:						:			
	↓				:					:						:			
	bright	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
		0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue gray scale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	↑				:					:						:			
	↓				:					:						:			
	bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
		0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

4.7 DISPLAY POSITIONS

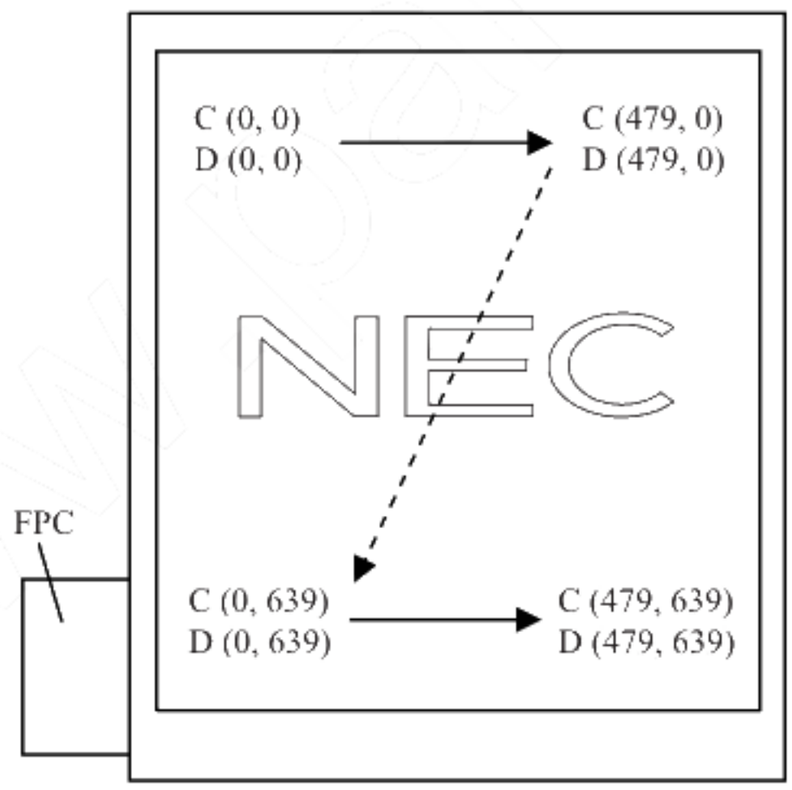
The following table is the coordinates per pixel (See figure of "**4.8 SCANNING DIRECTIONS**".).

C (0, 0)			C (1, 0)		
R	G	B	R	G	B

C(0, 0)	C(1, 0)	•••	C(X, 0)	•••	C(478, 0)	C(479, 0)
C(0, 1)	C(1, 1)	•••	C(X, 1)	•••	C(478, 1)	C(479, 1)
• • •	• • •	• ••• •	• • •	• ••• •	• • •	• ••• •
C(0, Y)	C(1, Y)	•••	C(X, Y)	•••	C(478, Y)	C(479, Y)
• • •	• • •	• ••• •	• • •	• ••• •	• • •	• • •
C(0, 638)	C(1, 638)	•••	C(X, 638)	•••	C(478, 638)	C(479, 638)
C(0, 639)	C(1, 639)	•••	C(X, 639)	•••	C(478, 639)	C(479, 639)

4.8 SCANNING DIRECTIONS

The following figures are seen from a front view. Also the arrow shows the direction of scan.



Note1: Meaning of C (X, Y) and D (X, Y)
C (X, Y): The coordinates of the display position (See "**4.7 DISPLAY POSITIONS**".)
D (X, Y): The data number of input signal for LCD panel

4.9 INPUT SIGNAL TIMINGS

4.9.1 RGB interface (Ta= 25°C, VCCIO= 3.0V)

(1) Timing characteristics

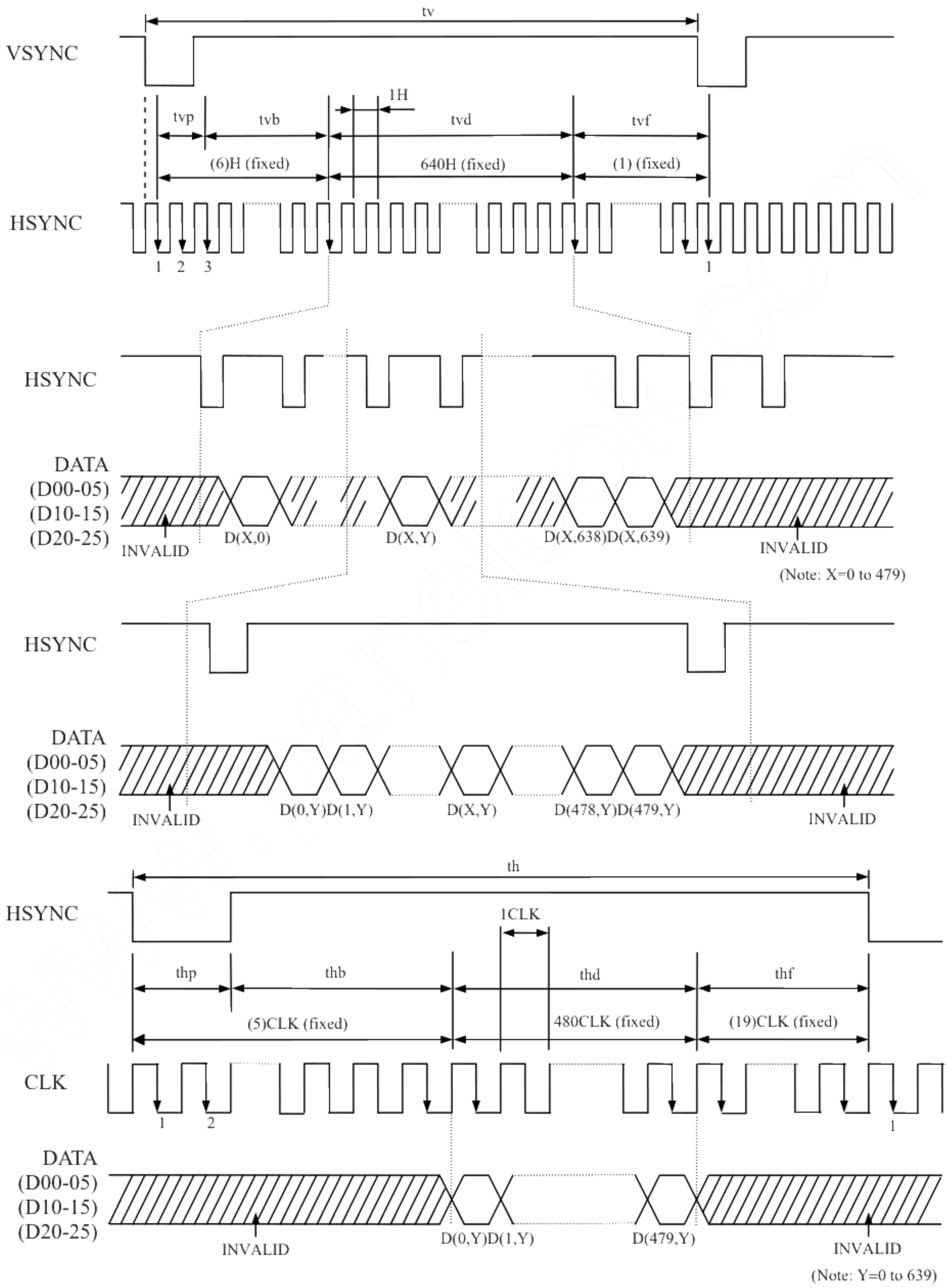
Parameter			Symbol	min.	typ.	max.	Unit	Remarks
CLK	Frequency		1/tc	TBD	(19.6)	TBD	MHz	(51ns) (typ.)
	Duty		tcd	0.4	0.5	0.6	-	-
	Rise time, Fall time		tcrf	-	-	(2)	ns	
DATA (D00-05) (D10-15) (D20-25)	CLK-DATA	Setup time	tds	(25)	-	-	ns	-
		Hold time	tdh	(25)	-	-	ns	
	Rise time, Fall time		tdrf	-	-	(2)	ns	
HSYNC	Cycle		th	TBD	(25.72)	TBD	µs	(38.88kHz) (typ.)
				(504)			CLK	-
	Display period		thd	480			CLK	
	Front-porch		thf	(19)			CLK	
	Pulse width		thp	(1)			CLK	
	Back-porch		thb	(4)			CLK	
	CLK-HSYNC	Setup time	ths	(25)	-	-	ns	
		Hold time	thh	(25)	-	-	ns	
	Rise time, Fall time		thrf	-	-	(2)	ns	
VSYNC	Cycle		tv	TBD	(16.59)	TBD	ms	(60Hz) (typ.)
				(648)			H	-
	Display period		tvd	640			H	
	Front-porch		tvf	(1)			H	
	Pulse width		tvp	(1)			H	
	Back-porch		tvb	(6)			H	
	VSYNC-HSYNC timing		tvh	TBD	-	-	ns	
	Rise time, Fall time		tvrf	-	-	(2)	ns	

Note1: Definition of parameters is as follows.

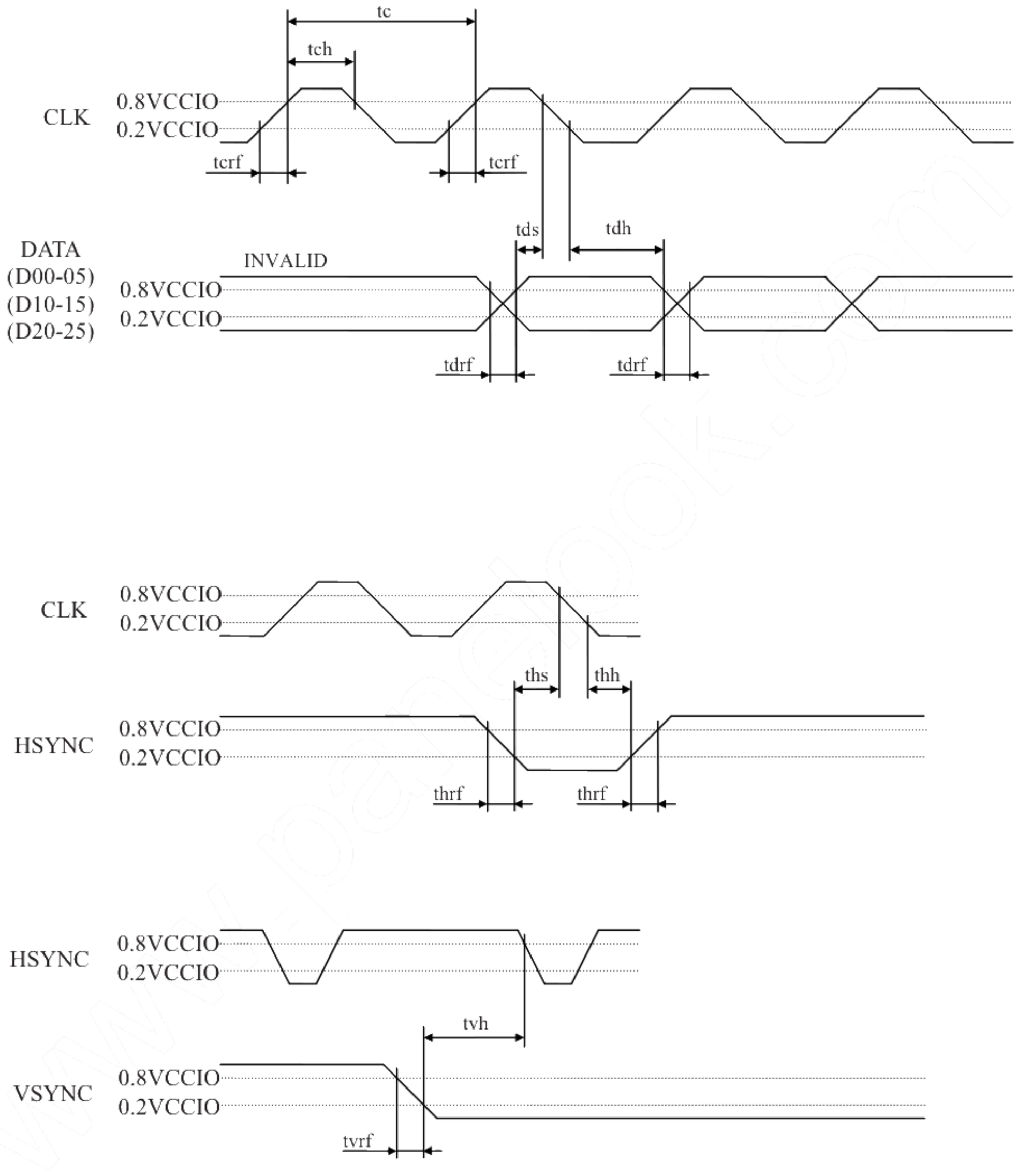
tc= 1CLK, tcd= tch/tc, th= 1H

Note2: All parameters should be kept within the specified range.

(2) Input signal timing chart



Note1: Unless otherwise specified, the input level is defined to be VIH= 0.8VCCIO, VIL= 0.2VCCIO.



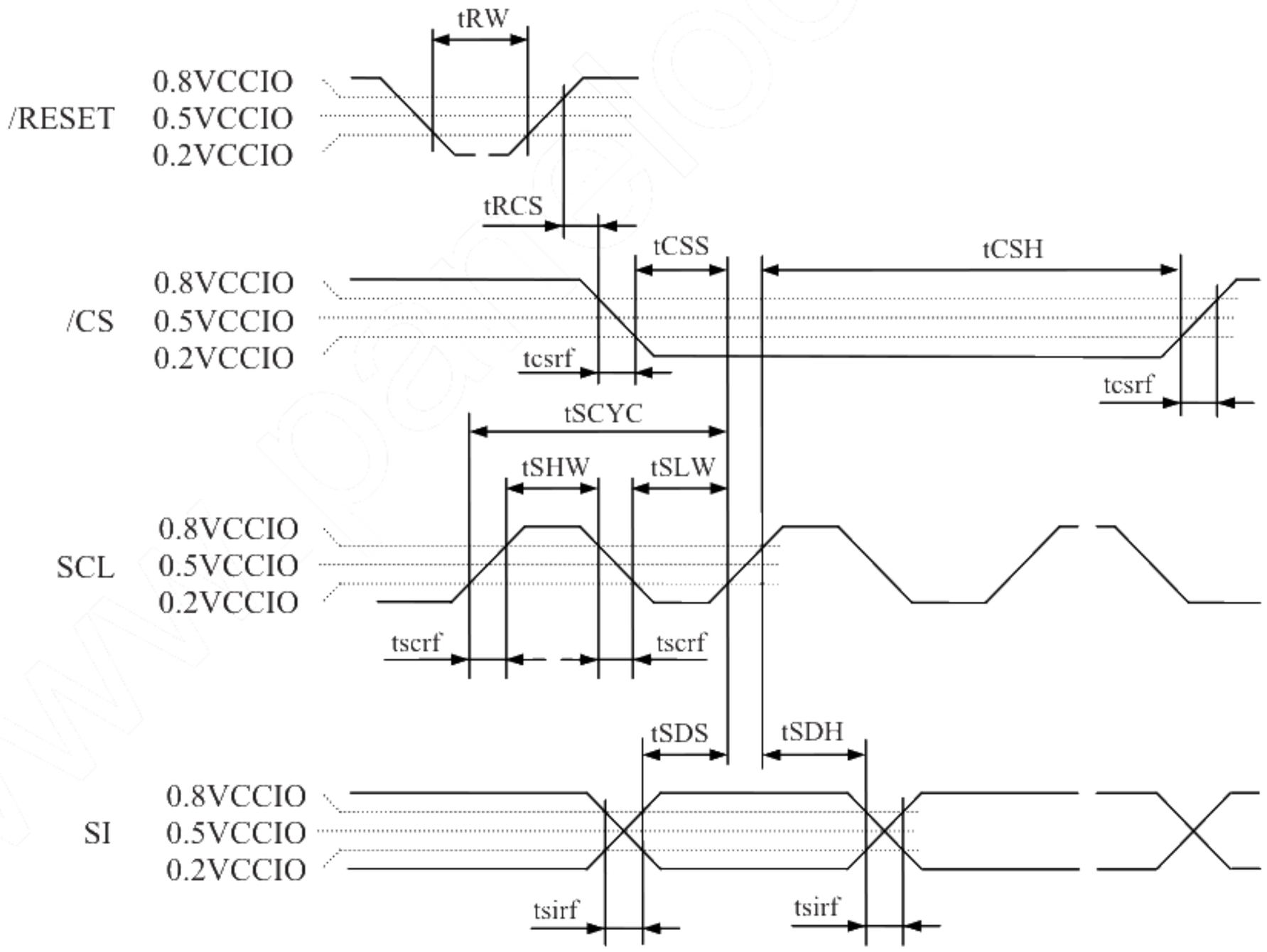
Note1: Unless otherwise specified, the input level is defined to be VIH= 0.8VCCIO, VIL= 0.2VCCIO.

4.9.2 Serial interface (Ta= 25°C, VCCIO= 3.0V)

(1) Timing characteristics

Parameter	Symbol	Condition	min.	typ.	max.	Unit	Remarks
Serial clock cycle	tSCYC	SCL	(100)	-	-	ns	-
SCL high level pulse width	tSHW	SCL	(50)	-	-	ns	-
SCL low level pulse width	tSLW	SCL	(50)	-	-	ns	-
/CS rise time, fall time	tcsrf	/CS	-	-	(2)	ns	-
SCL rise time, fall time	tscrf	SCL	-	-	(2)	ns	-
SI rise time, fall time	tsirf	SI	-	-	(2)	ns	-
/CS setup time	tCSS	/CS	(50)	-	-	ns	-
/CS hold time	tCSH	/CS	(50)	-	-	ns	-
Data setup time	tSDS	SI	(50)	-	-	ns	-
Data hold time	tSDH	SI	(50)	-	-	ns	-
Reset pulse width	tRW	/RESET	(10)	-	-	µs	-
/RESET↑ to /CS time	tRCS	/RESET↑ to /CS	TBD	-	-	µs	-

Note1: All parameters should be kept within the specified range.



Note2: Unless otherwise specified, the input level is defined to be VIH= 0.8VCCIO, VIL= 0.2VCCIO.

4.10 OPTICAL CHARACTERISTICS

<Backlight turning OFF>

(Note1, Note3, Note4)

Parameter	Condition	Symbol	min.	typ.	max.	Unit	Remarks
Reflection ratio	White, at center	RE	TBD	TBD	-	%	Note6
Contrast ratio	White/Black, at center	CR	TBD	TBD	-	-	Note7

<Backlight turning ON>

(Note2, Note3, Note5)

Parameter	Condition	Symbol	min.	typ.	max.	Unit	Remarks
Luminance	White at center θR= 0°, θL= 0°, θU= 0°, θD= 0°	L	TBD	(200)	-	cd/m^2	-
Contrast ratio	White/Black at center θR= 0°, θL= 0°, θU= 0°, θD= 0°	CR	TBD	TBD	-	-	Note7
Luminance uniformity	White θR= 0°, θL= 0°, θU= 0°, θD= 0° Maximum luminance: 100%	LU	TBD	(70)	-	%	Note11

Reference data

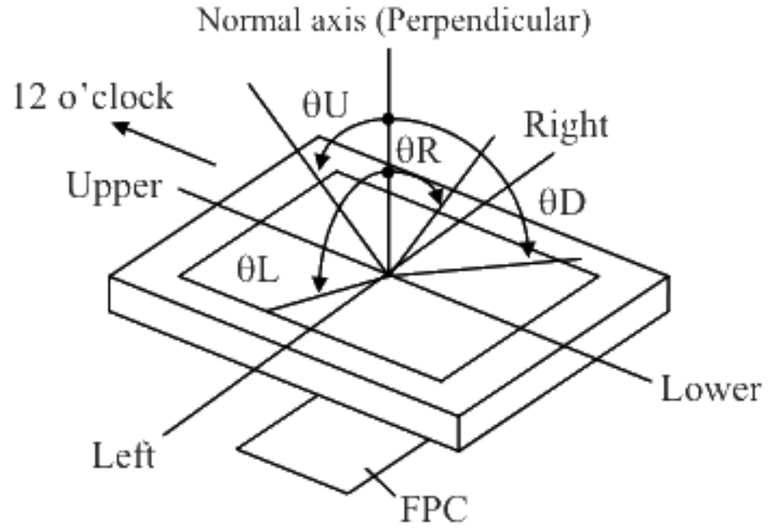
(Note2, Note3, Note5)

Parameter		Condition		Symbol	min.	typ.	max.	Unit	Remarks
Chromaticity coordinates		White		Wx	TBD	TBD	TBD	-	Note8
				Wy	TBD	TBD	TBD	-	
Color gamut		θR= 0°, θL= 0°, θU= 0°, θD= 0° at center, against NTSC color space		C	-	TBD	-	%	
Response time		White to black	90%→ 10%	Ton	-	TBD	TBD	ms	Note9 Note10
		Black to white	10%→ 90%	Toff	-	TBD	TBD		
Viewing angle	Right	θU= 0°, θD= 0°, CR≥ 5		θR	-	TBD	-	°	-
	Left	θU= 0°, θD= 0°, CR≥ 5		θL	-	TBD	-	°	
	Up	θR= 0°, θL= 0°, CR≥ 5		θU	-	TBD	-	°	
	Down	θR= 0°, θL= 0°, CR≥ 5		θD	-	TBD	-	°	

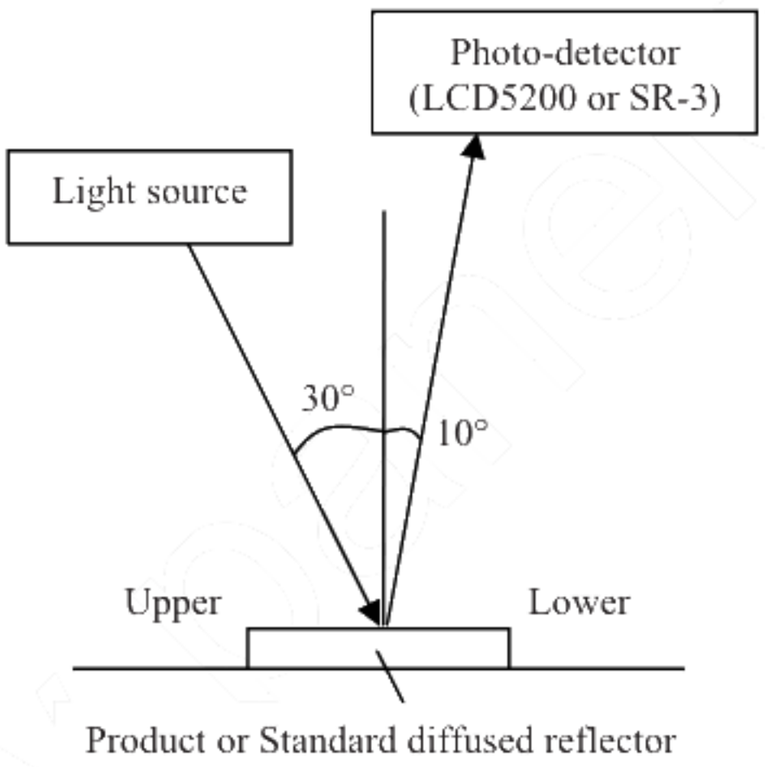
Note1: Measurement conditions are as follows.
Ta= 25°C, VCC= 3.0V, VCCIO= 3.0V

Note2: Measurement conditions are as follows.
Ta= 25°C, VCC= 3.0V, VCCIO= 3.0V, IL= 20mA

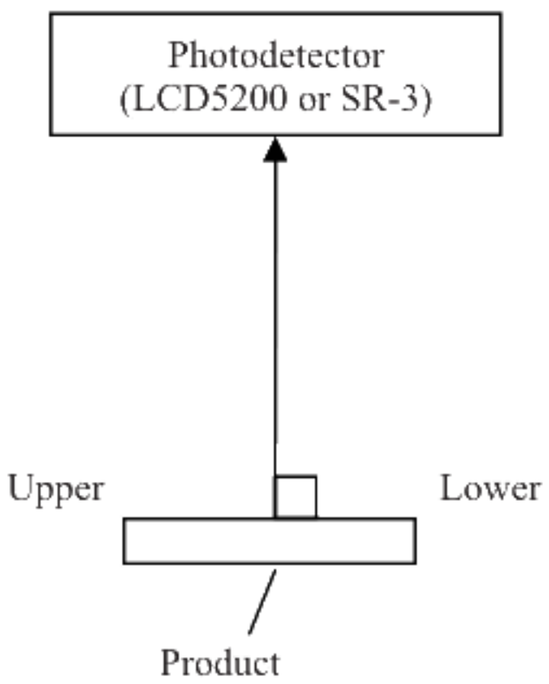
Note3: Definition of viewing angles



Note4: In reflective mode (Backlight turning OFF), Reflection ratio, Contrast ratio, Chromaticity coordinates and Color gamut are measured as follows.



Note5: In transmissive mode (Backlight turning ON), Luminance, Contrast ratio, Chromaticity coordinates and Color gamut are measured as follows.



Note6: Definitions of reflection ratio

The reflection ratio is calculated by using the following formula.

$$\text{Reflection (RE)} = \frac{\text{Luminance of reflected light at white screen}}{\text{Luminance of standard diffused reflector}} \times 100$$

Note7: Definition of contrast ratio

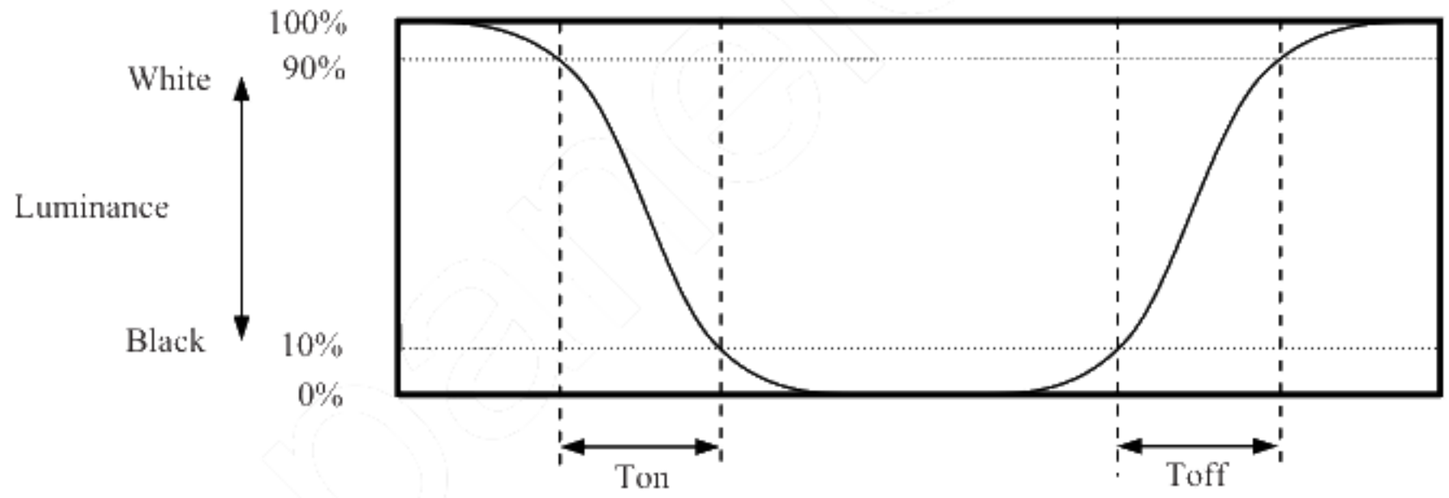
The contrast ratio is calculated by using the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance of white screen}}{\text{Luminance of black screen}}$$

Note8: The White chromaticity coordinates are deviated by the LED deviation in addition to color filter deviation.

Note9: Definition of response times

Response time is measured, the luminance changes from "white" to "black", or "black" to "white" on the same screen point, by photo-detector. Ton is the time it takes the luminance change from 90% down to 10%. Also Toff is the time it takes the luminance change from 10% up to 90% (See the following diagram.).

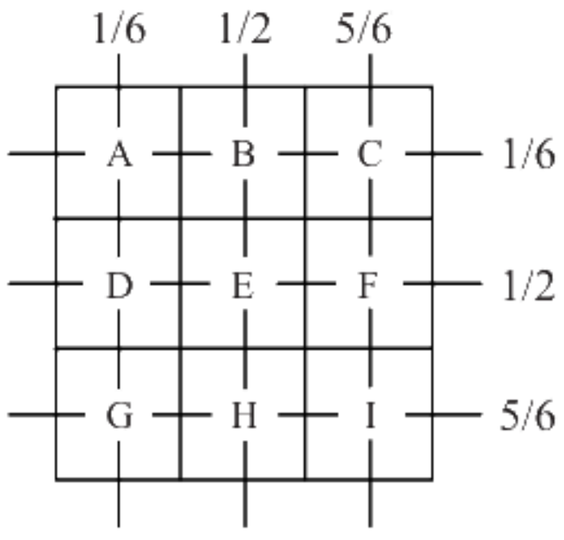


Note10: Product surface temperature: Top= 25°C

Note11: Definition of luminance uniformity

Luminance uniformity is calculated by using the following formula.

$$\text{Luminance uniformity (LU)} = \frac{\text{Minimum luminance from A to I}}{\text{Maximum luminance from A to I}} \times 100$$



5. RELIABILITY TESTS

TBD

6. PRECAUTIONS

6.1 MEANING OF CAUTION SIGNS

The following caution signs have very important meaning. **Be sure to read "6.2 CAUTIONS" and "6.3 ATTENTIONS", after understanding these contents!**

This sign has the meaning that customer will be injured by himself or the product will sustain a damage, if customer has wrong operations.

This sign has the meaning that customer will get an electrical shock, if customer has wrong operations.

This sign has the meaning that customer will be injured by himself, if customer has wrong operations.

6.2 CAUTIONS

∗ Do not touch the working backlight. There is a danger of an electric shock.

∗ Do not touch the working backlight. There is a danger of burn injury.
∗ Do not shock and press the LCD panel and the backlight! There is a danger of breaking, because they are made of glass. (Shock: To be not greater TBD m/s^2 and to be not greater TBD ms)

6.3 ATTENTIONS

6.3.1 Handling of the product

① Take hold of both ends without touching the FPC when the product (LCD module) is picked up from the tray.
② Do not hook nor pull the FPC in order to avoid any damage.
③ When the product is put on the table temporarily, display surface must be placed downward.
④ When handling the product, take the measures of electrostatic discharge with such as earth band, ionic shower and so on, because the product may be damaged by electrostatic.
⑤ The product must be installed and/or handled without undue stress such as bends or twist. Bends, twist or undue stress to any portion may cause display failures. And also do not put heavy or hard materials on the product.
⑥ Do not hit or rub the surface of touch panel with hard materials, because it is easily scratched. (Touch panel pencil-hardness: 3H)
⑦ When cleaning the T/P surface, wipe it with a soft dry cloth.
⑧ Do not push nor pull the FPC while the product is working.
⑨ Do not fold the FPC. When the FPC is folded, pattern disconnection may be caused. In case of bending FPC, the minimum curvature (R) must be more than 1.0 mm.
⑩ When installing the product, do not contact a conductor such as a metal to the FPC excluding the terminal area. There is a risk of short circuit which is caused by breakage of insulation layer of the FPC.
⑪ When installing the product, apply the waterproof design to avoid going of water into the product.

6.3.2 Environment

① Do not operate or store in high temperature, high humidity, dewdrop atmosphere or corrosive gases. Keep the product in packing box with antistatic pouch in room temperature to avoid for dusts and sunlight, when storing the product.
② In order to prevent dew condensation occurring by temperature difference, the product packing box should be opened after enough time being left under the environment of an unpacking room. Evaluate the leaving time sufficiently because a situation of dew condensation occurring is changed by the environmental temperature and humidity. (Recommended leaving time: 6 hours or more with packing state)
③ Do not operate in high magnetic field. Circuits may be broken down by it.
④ This product is not designed as radiation hardened.

6.3.3 Characteristics

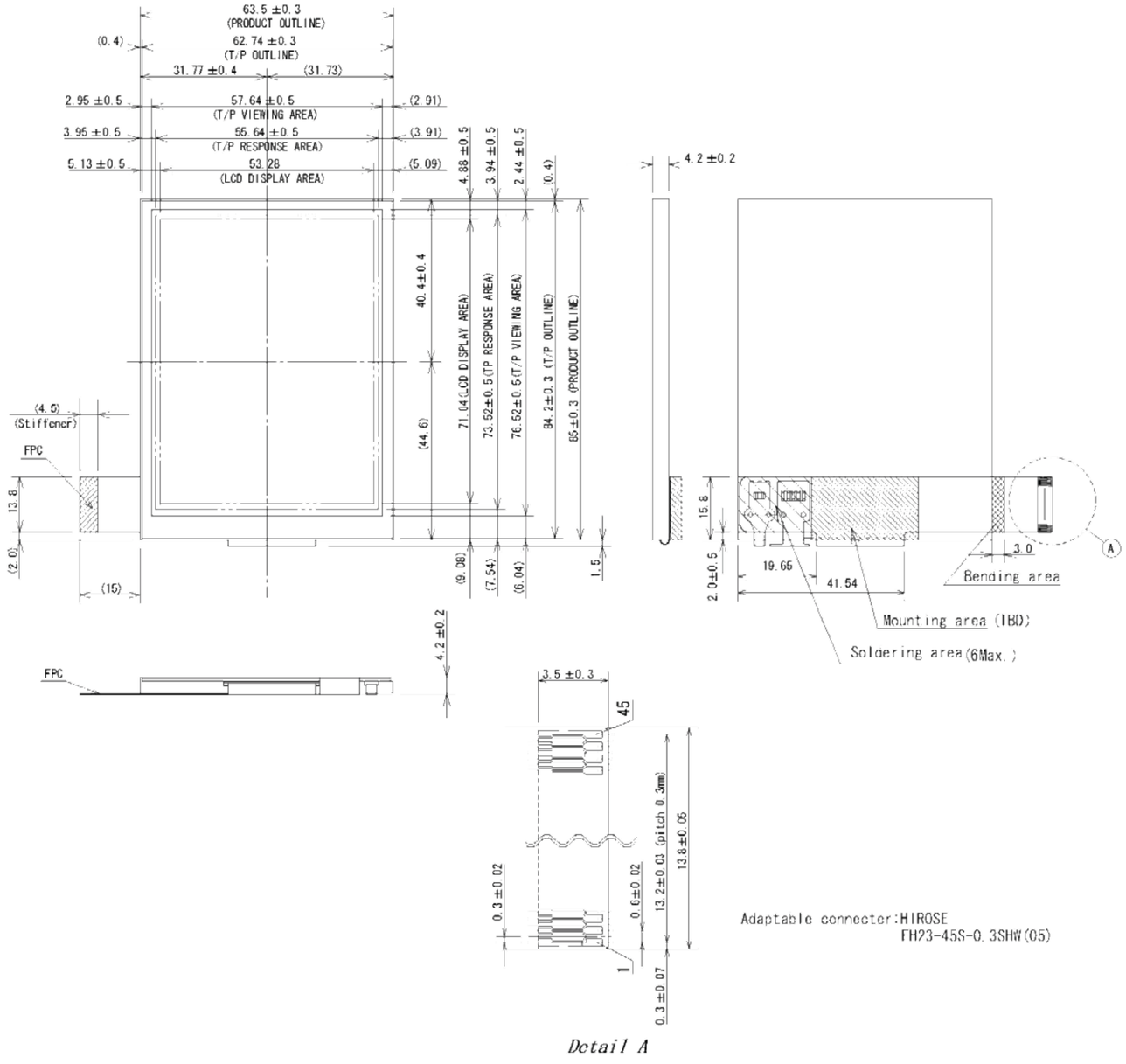
The following items are neither defects nor failures.

① Response time, luminance and color may be changed by ambient temperature.
② Display mura, flicker, vertical seam or small spot may be observed depending on display patterns.
③ Do not display the fixed pattern for a long time because it may cause image sticking.
④ Optical characteristics may be changed depending on input signal timings.
⑤ Touch panel film has polarizing characteristic. And the polarizer characteristics differ among products. Therefore, when seeing the displays through the other polarizing material (for example polarizing sunglasses), some displays can not be seen and some displays look different color darker because of polarizer characteristic mismatching between touch panel film and the other polarizing material.

6.3.4 Other

① All GND terminals should be used without any non-connected lines.
② Do not disassemble the product.
③ Pack the product with original shipping package, in order to avoid any damages during transportation, when returning the product to NEC.

7. OUTLINE DRAWINGS

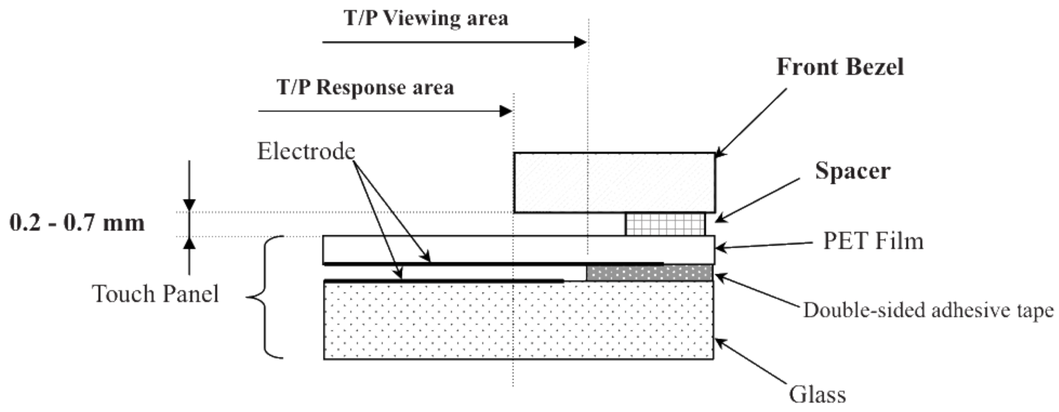


Pin No.	Symbols	Pin No.	Symbols
1	GND	25	D20
2	GND	26	D21
3	VCC	27	D22
4	VCC	28	D23
5	VCC	29	D24
6	VCC	30	D25
7	GND	31	GND
8	/RESET	32	SCL
9	HSYNC	33	SI
10	VSYNC	34	RSVD
11	CLK	35	/CS
12	GND	36	VCOMIN
13	D00	37	VCCIO
14	D01	38	GND
15	D02	39	XL
16	D03	40	YD
17	D04	41	XR
18	D05	42	YU
19	D10	43	GND
20	D11	44	ANODE
21	D12	45	CATHODE
22	D13		
23	D14		
24	D15		

Note1: The values in parentheses are for reference.
Note2: When installing the product to customer equipment, do not apply undue stress to the rear side of the product, FPC, A area, Soldering Area and Mounting Area.
If not, it may cause display un-uniformity or break down of the product.
Note3: While the product is working, do not contact a conductor such as a metal to the Soldering Area and Mounting Area of the FPC.

Unit: mm

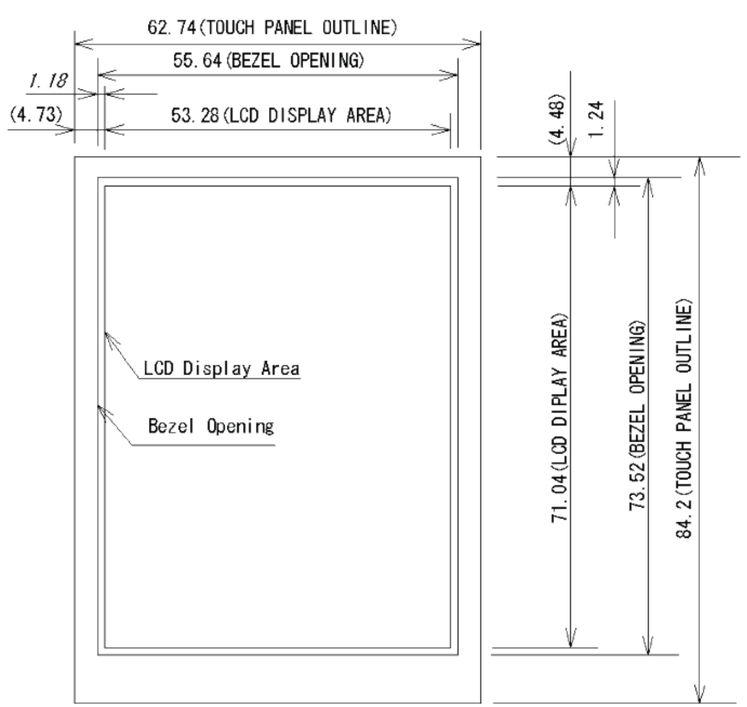
8. RECOMMENDATION DESIGN OF FRONT BEZEL



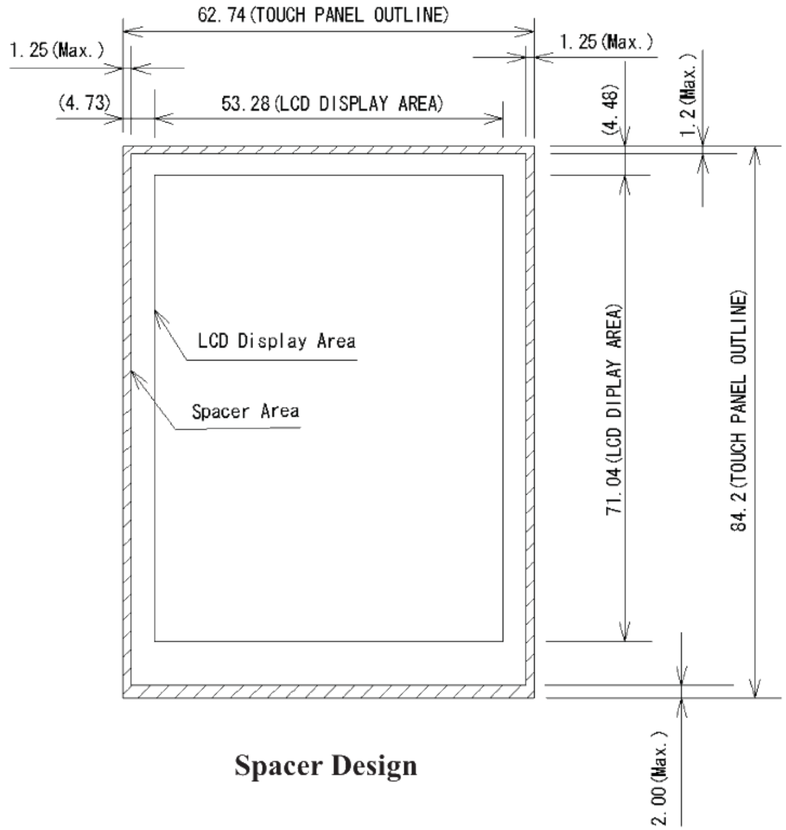
Design guidance for the front bezel and the spacer

1. Front Bezel opening design
   a. Please place the front bezel opening to maintain the operation by a stylus pen inside the T/P response area.
   b. The any pressures in the area between T/P response area and T/P viewing area is prohibited.
      Please use the appropriate material as the front bezel.

2. Spacer design
   a. Please put the spacer, a cushion, on the front bezel. Do not use a double-sided adhesive tape because it adheres on the touch panel surface.
   b. Please position the spacer over the Spacer area to avoid a “short”.



Front Bezel Opening Design



Spacer Design

Unit: mm

REVISION HISTORY

The inside of latest specifications is revised to the clerical error and the major improvement of previous edition. Only a changed part such as functions, characteristic value and so on that may affect a design of customers, are described especially below.

Edition	Document number	Prepared date	Revision contents and signature
1st edition	DOD-PP-0144	Jan. 18, 2007	**Revision contents** New issue **Writer** *Approved by* T. OGAWA　*Checked by* ———　*Prepared by* N. KANO
2nd edition	DOD-PP-0182	Mar. 1, 2007	**Revision contents** P8 Absolute maximum ratings • Storage temperature: (-30 to +70°C) → (-30 to +80°C) • Storage altitude- remarks: -30°C ≤ Ta ≤ 70°C → -30°C ≤ Ta ≤ 80°C • Note1: Figure (revised) P11 Command sequence: Power On (revised) P27 Outline drawings- Adaptable connecter: FH23-51S-0.3SHW(05) → FH23-45S-0.3SHW(05) (correction) **Signature of writer** *Approved by* T. OGAWA　*Checked by* ———　*Prepared by* N. KANO