# NEC LCD Technologies, Ltd.

# TFT COLOR LCD MODULE

NL8060BC26-35C

26cm (10.4 Type) SVGA LVDS interface (1port)

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# PRELIMINARY DATA SHEET

DOD-PP-0966 (2nd edition)

This PRELIMINARY DATA SHEET is updated document from DOD-PP-0700(1).

All information is subject to change without notice. Please confirm the sales representative before starting to design your system.

#### INTRODUCTION

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The quality grade of this product is the "Standard" unless otherwise specified in this document.

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#### 1. OUTLINE

#### 1.1 STRUCTURE AND PRINCIPLE

Color LCD module NL8060BC26-35C is composed of the amorphous silicon thin film transistor liquid crystal display (a-Si TFT LCD) panel structure with driver LSIs for driving the TFT (Thin Film Transistor) array and a backlight.

The a-Si TFT LCD panel structure is injected liquid crystal material into a narrow gap between the TFT array glass substrate and a color-filter glass substrate.

Color (Red, Green, Blue) data signals from a host system (e.g. signal generator, etc.) are modulated into best form for active matrix system by a signal processing board, and sent to the driver LSIs which drive the individual TFT arrays.

The TFT array as an electro-optical switch regulates the amount of transmitted light from the backlight assembly, when it is controlled by data signals. Color images are created by regulating the amount of transmitted light through the TFT array of red, green and blue dots.

#### 1.2 APPLICATION

• For industrial use

#### 1.3 FEATURES

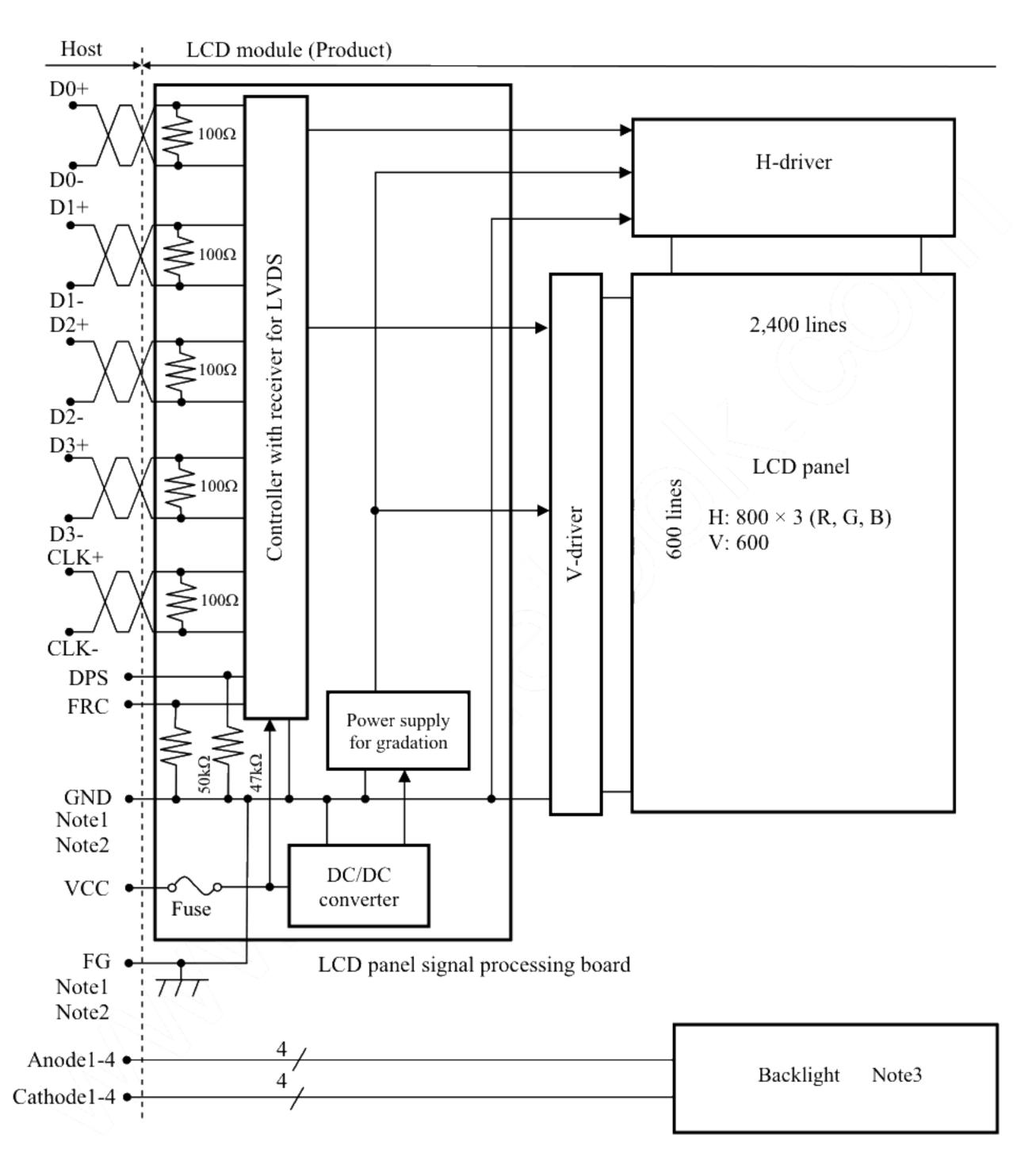
- Adoption of ST-NLT (Super-Transmissive Natural Light TFT)
- · High luminance
- High contrast
- Low reflection
- Wide viewing angle
- Wide temperature range
- LVDS interface
- · Reversible-scan direction
- Selectable 8bit or 6bit digital signals for data of RGB
- · LED backlight type
- Replaceable lamp holder for backlight

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#### 2. GENERAL SPECIFICATIONS

Display area	211.2 (H) × 158.4 (V) mm		
Diagonal size of display	26cm (10.4 inches)		
Drive system	a-Si TFT active matrix		
Display color	16,777,216 colors (At 8-bit input, FRC terminal= High) 262,144 colors (At 6-bit input, FRC terminal= Low or Open)		
Pixel	800 (H) × 600 (V) pixels		
Pixel arrangement	RGB (Red dot, Green dot, Blue dot) vertical stripe		
Dot pitch	0.088 (H) × 0.264 (V) mm		
Pixel pitch	0.264 (H) × 0.264 (V) mm		
Module size	243.0 (W) × 185.1 (H) × 10.5 (D) mm (typ.)		
Weight	(475)g (typ.)		
Contrast ratio	(900: 1) (typ.)		
Viewing angle	At the contrast ratio ≥10:1  • Horizontal: Right side 80° (typ.), Left side 80° (typ.)  • Vertical: Up side 80° (typ.), Down side 80° (typ.)		
Designed viewing direction	<ul> <li>At DPS terminal= Low or Open: Normal scan</li> <li>Viewing direction without image reversal: up side (12 o'clock)</li> <li>Viewing direction with contrast peak: down side (6 o'clock)</li> <li>Viewing angle with optimum grayscale (γ ≒ 2.2): normal axis (perpendicular)</li> </ul>		
Polarizer surface	Clear + Antireflection (AR)		
Polarizer pencil-hardness	2H (min.) [by JIS K5400]		
Color gamut	At LCD panel center 40% (typ.) [against NTSC color space]		
Response time	$Ton+Toff (10\% \longleftrightarrow 90\%)$ 18ms (typ.)		
Luminance	$At IL = 50mA/One \ circuit$ (800) cd/m <sup>2</sup> (typ.)		
Signal system	LVDS 1port (Receiver: THC63LVDF84B, THine Electronics Inc. or equivalent) [8bit/6bit digital signals for data of RGB colors, Dot clock (CLK), Data enable (DE)]		
Power supply voltage	LCD panel signal processing board: 3.3V		
Backlight	LED backlight type:  (Replaceable part  • Lamp holder set: Type No. TBD		
	Recommended LED driver board (Option)  • LED driver board :Type No. 104PW01F		

#### 3. BLOCK DIAGRAM



Note1: Relations between GND (Signal ground) and FG (Frame ground) in the LCD module are as follows.

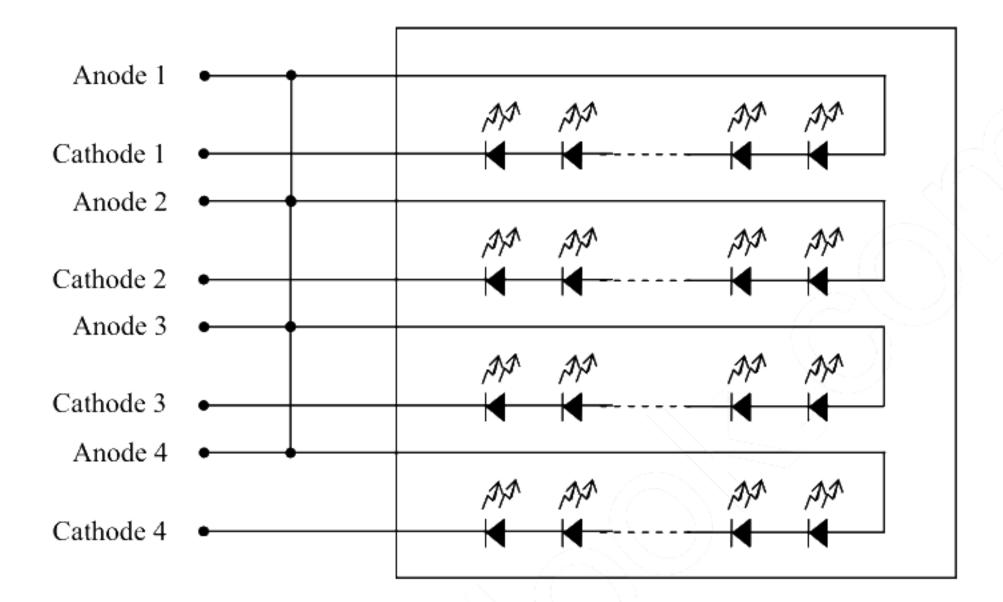
GND - FG Connected

Note2: GND and FG must be connected to customer equipment's ground, and it is recommended that these grounds be connected together in customer equipment.

Note3: Backlight in detail



#### Backlight



#### 4. DETAILED SPECIFICATIONS

#### 4.1 MECHANICAL SPECIFICATIONS

Parameter	Specification		Unit
Module size	$243.0 \pm 0.5 \text{ (W)} \times 185.1 \pm 0.5 \text{ (H)} \times 10.5 \pm 0.5 \text{ (D)}$	Note1	mm
Display area	211.2 (H) × 158.4 (V)	Note1	mm
Weight	(475) (typ.), TBD (max.)		g

Note1: See "8. OUTLINE DRAWINGS".

#### 4.2 ABSOLUTE MAXIMUM RATINGS

	Parameter		Symbol	Rating	Unit	Remarks
Power supply voltage	LCD panel signal processing board		VCC	-0.3 to +4.0	v	
Input voltage	Display No	-	VD	-0.3 to VCC+0.3	v	-
for signals	Function No	-	VF	-0.5 to VCC+0.5	V	
Backlight	Power di	ssipation	PD	TBD	W	per one circuit
Backlight	Forward	current	IL	твр	mA	per one circuit
Iı	Incident light intensity		II	150,000	lx	Note3
	Storage temperature		Tst	-30 to +80	°C	-
Operating	temperature	Front surface	TopF	-30 to +80	°C	Note4
Operating	temperature	Rear surface	TopR	-30 to +80	°C	Note5
				≤ 95	%	Ta ≤ 40°C
				≤ 85	%	40°C <ta≤ 50°c<="" td=""></ta≤>
	Relative humidity Note6		RH	≤ 55	%	50°C <ta≤60°c< td=""></ta≤60°c<>
				≤ 36	%	60°C <ta≤70°c< td=""></ta≤70°c<>
				≤ 24	%	70°C < Ta ≤ 80°C
	Absolute humidity Note6		АН	≤ 70 Note7	g/m <sup>3</sup>	-

Note1: D0+/-, D1+/-, D2+/-, D3+/- and CLK+/-

Note2: DPS and FRC

Note3: If the product surface (polarizer) is exposed to an ultraviolet ray, the polarizer may discolor (Surface treatment may be damaged.). Use a filter to protect the polarizer from the ultraviolet ray.

Note4: Measured at LCD panel surface (including self-heat)

Note5: Measured at LCD module's rear shield surface (including self-heat)

Note6: No condensation

Note7: Water amount at Ta= 80°C and RH= 24%

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#### 4.3 ELECTRICAL CHARACTERISTICS

#### 4.3.1 LCD panel signal processing board

 $(Ta=25^{\circ}C)$ 

Parameter		Symbol	min.	typ.	max.	Unit	Remarks	
Power supply voltage		VCC	3.0	3.3	3.6	v	-	
Power supply current		ICC	-	400 Note l	660 Note2	mA	at VCC= 3.3V	
Permissible ripple voltage		VRP	-	-	100	mVp-p	for VCC	
Differential input	High	VTH	-	-	+100	mV	at VCM= 1.2V	
threshold voltage	Low	VTL	-100	-	-	mV	Note3	
Terminating resistance		RT	-	100	<u>-</u>	Ω	-	
Input voltage for	High	VFH	0.7VCC	-	VCC	, v	CMOS level	
DPS and FRC signals	Low	VFL	0	-((	0.3VCC	V	CIVIOS level	
Input current for FRC	High	IFH	-		300	μΑ		
signal	Low	IFL	-300		-	μΑ	-	

Note1: Checkered flag pattern [by EIAJ ED-2522]

Note2: Pattern for maximum current

Note3: Common mode voltage for LVDS receiver

#### 4.3.2 Backlight lamp

(Ta=25°C, Note1, Note2)

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Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Forward current	IL	-	50.0	55.0	mA	-
		23.8	27.0	30.6		Ta= +25°C at IL= 50mA /One circuit
Forward Voltage	VL	21.3	-	-	v	Ta=+80°C at IL= 50mA /One circuit
Torward voltage	V L	-	-	33.6	ľ	Ta= -30°C at IL= 50mA /One circuit
		-	-	33.9		Ta= -30°C at IL= 55mA /One circuit

Note1: Please drive with constant current.

Note2: The Luminance uniformity may be changed depending on the current variation between 4 circuits. It is recommended that the current value difference among the circuits be less than 5%.

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#### 4.3.3 Power supply voltage ripple

This product works if the ripple voltage levels are over the permissible values as the following table, but there might be noise on the display image.

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Power sup	ply voltage	Ripple voltage Note1 (Measure at input terminal of power supply)	Unit
VCC	3.3V	≤ 100	mVp-p

Note1: The permissible ripple voltage includes spike noise.

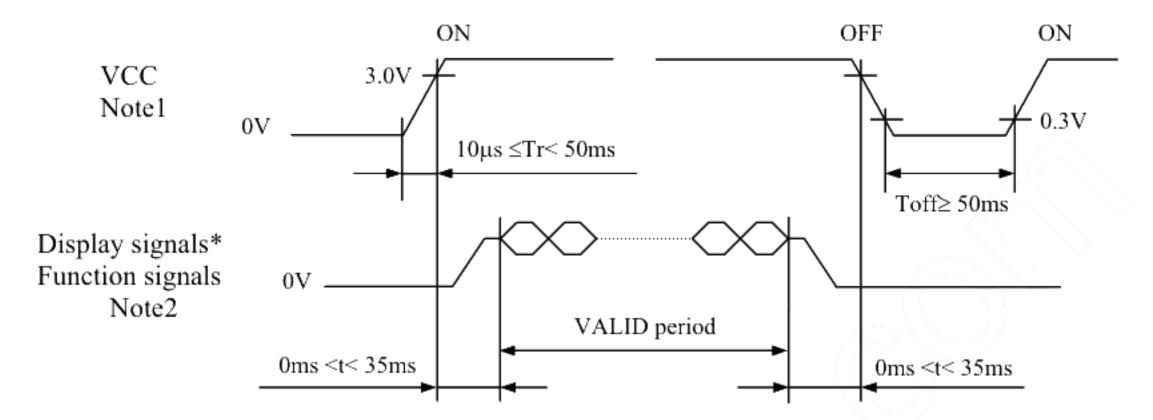
#### 4.3.4 Fuse

Parameter	F	use	Rating	Fusing current	Remarks
Parameter	Туре	Supplier	Rating	rusing current	Remarks
VCC	FCC16162AB	KAMAYA	1.6A	3.2A	Note1
l vec	rectorozab	ELECTRIC Co., Ltd	32V	3.2A	Note1

Note1: The power supply's rated current must be more than the fusing current. If it is less than the fusing current, the fuse may not blow in a short time, and then nasty smell, smoke and so on may occur.

#### 4.4 POWER SUPPLY VOLTAGE SEQUENCE

#### 4.4.1 LCD panel signal processing board



\* These signals should be measured at the terminal of  $100\Omega$  resistance.

Note1: If there is a voltage variation (voltage drop) at the rising edge of VCC below 3.0V, there is a possibility that a product does not work due to a protection circuit.

Note2: Display signals (D0+/-, D1+/-, D2+/-, D3+/- and CLK+/-) and function signals (DPS and FRC) must be set to Low or High-impedance, except the VALID period (See above sequence diagram), in order to avoid the circuitry damage.

If some of display and function signals of this product are cut while this product is working, even if the signal input to it once again, it might not work normally. If a customer stops the display and function signals, VCC also must be shut down.

#### 4.4.2 LED Driver board (Option)

Display signals, Function signals Note1

VALID period

LED backlight: ON

Note1: These are the display and function signals for LCD panel signal processing board.

Note2: The backlight should be turned on within the valid period of display and function signals, in order to avoid unstable data display.

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#### 4.5 CONNECTIONS AND FUNCTIONS FOR INTERFACE PINS

#### 4.5.1 LCD panel signal processing board

CN1 socket (LCD module side): FI-SE20P-HFE (Japan Aviation Electronics Industry Limited (JAE))
Adaptable plug: FI-S20S (Japan Aviation Electronics Industry Limited (JAE))

Pin	No.	Symbol	Signal	Remarks				
1	A	D3+	Pixel data	Note1, Note2				
1	В	GND	Ground	Note3				
	A	D3-	Pixel data		Note1, Note2			
2	В	GND	Ground		Note3			
;	3	DPS	Selection of scan direction		verse scan rmal scan	Note4		
	4	FRC	Selection of the number of colors	High: 16,	777,216 colors 2,144 colors	Note1		
:	5	GND	Ground	Note3				
	6	CLK+	D: 1 1 1	Note2				
	7	CLK-	Pixel clock					
	8	GND	Ground	Note3		Note3		
	9	D2+	(62%)					
1	10	D2-	Pixel data	Note2				
1	11	GND	Ground	Note3				
1	12	D1+						
1	13	D1-	Pixel data		Note2			
1	14	GND	Ground	Note3				
1	15	D0+						
1	16	D0-	Pixel data	Note2				
1	17	GND						
	18	GND	Ground	Note3				
1	19	VCC						
2	20	VCC	Power supply	Note3				

Note1: See "4.6 DISPLAY COLORS AND INPUT DATA SIGNALS".

Note2: Twist pair wires with  $100\Omega$  (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note3: All GND and VCC terminals should be used without any non-connected lines.

Note4: See "4.8 SCANNING DIRECTIONS".

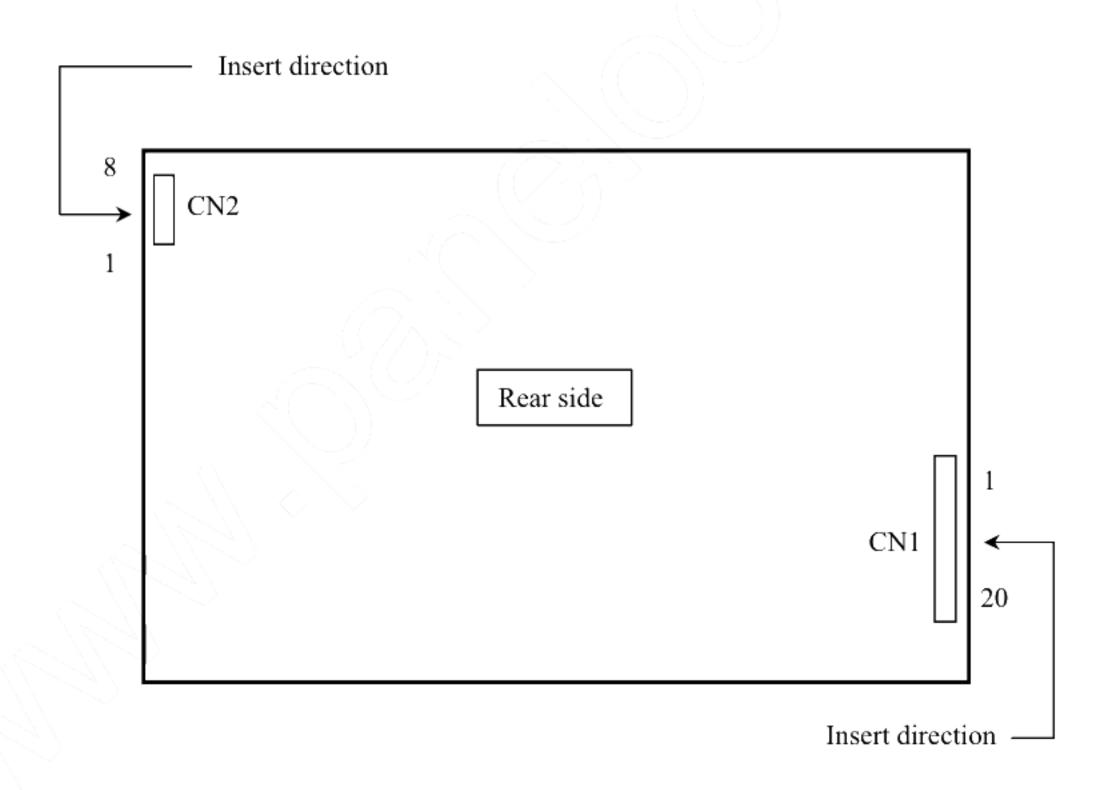
Note5: See "4.5.4 Connection between receiver and transmitter for LVDS".

#### 4.5.2 Backlight lamp

CN2 plug (LCD module side): SM08B-SRSS-TB (J.S.T. Mfg. Co., Ltd.)
Adaptable socket: SHR-08V-S (J.S.T. Mfg. Co., Ltd.)

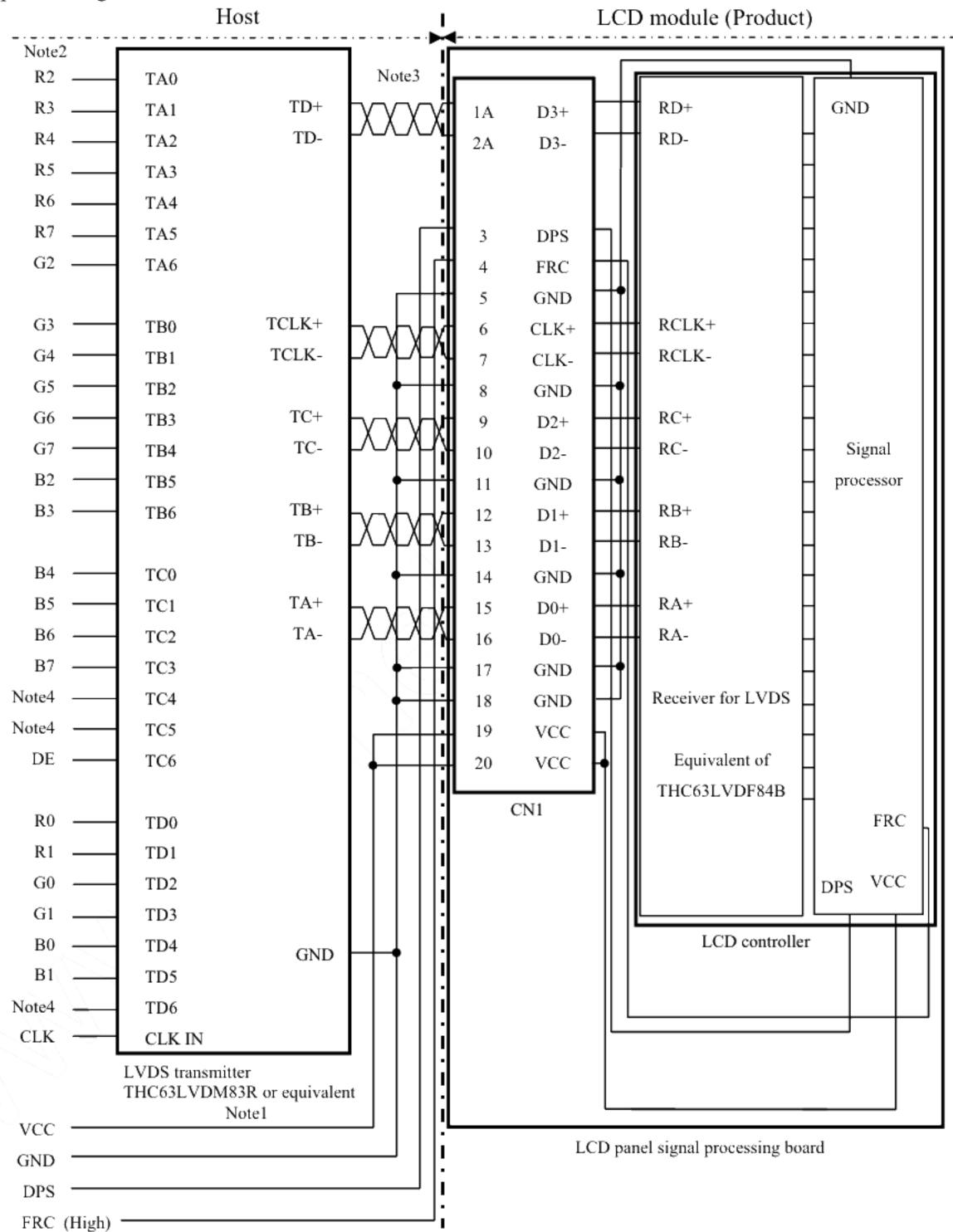
Transmit		2111 00 · 2 (0.2.11 1.11g. 00., 2.ta.)	
Pin No.	Symbol	Signal	Remarks
1	A1	Anode1	-
2	K1	Cathode1	-
3	A2	Anode2	- /^
4	K2	Cathode2	- <u>/</u>
5	A3	Anode3	<u>-</u> << \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
6	K3	Cathode3	
7	A4	Anode4	<u> </u>
8	K4	Cathode4	2-5X\ <del>-</del> 27

#### 4.5.3 Positions of plug and socket



#### 4.5.4 Connection between receiver and transmitter for LVDS

(1) Input data signal: 8bit



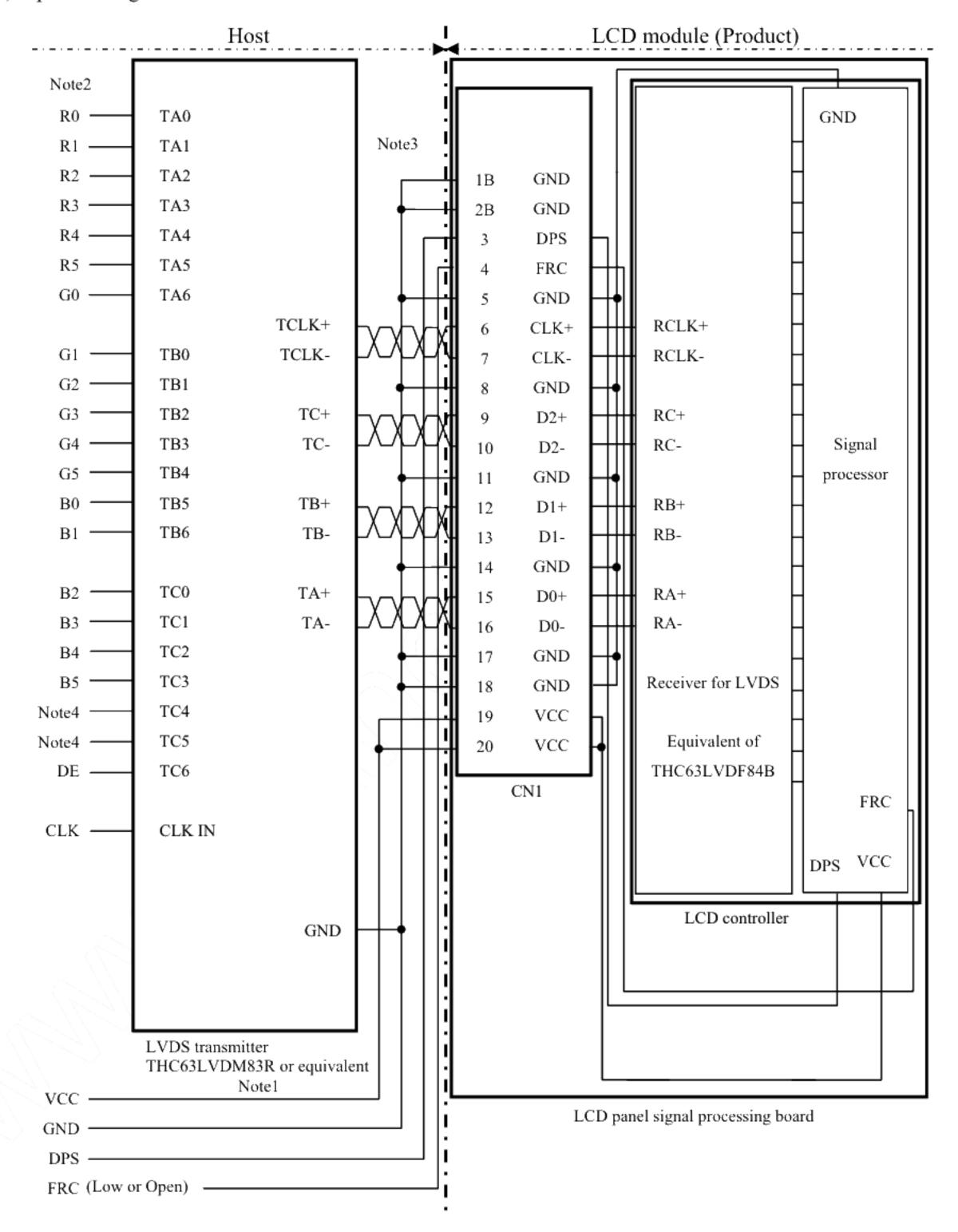
Note1: Recommended transmitter THC63LVDM83R (THine Electronics Inc.) or equivalent

Note2: LSB (Least Significant Bit) - R0, G0, B0 MSB (Most Significant Bit) - R7, G7, B7

Note3: Twist pair wires with  $100\Omega$  (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note4: Input signals to TC4, TC5 and TD6 are not used inside the product, but do not keep TC4, TC5 and TD6 open to avoid noise problem.

#### (2) Input data signal: 6bit



Note1: Recommended transmitter THC63LVDM83R (THine Electronics Inc.) or equivalent

Note2: LSB (Least Significant Bit) - R0, G0, B0 MSB (Most Significant Bit) - R5, G5, B5

Note3: Twist pair wires with  $100\Omega$  (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note4: Input signals to TC4 and TC5 are not used inside the product, but do not keep TC4 and TC5 open to avoid noise problem.

#### 4.6 DISPLAY COLORS AND INPUT DATA SIGNALS

#### 4.6.1 Combinations of input data signals and FRC signal

This product can display 16,777,216 colors equivalent with 256 gray scales and 262,144 colors with 64 gray scales by combination of input data signals and FRC signal. See the following table.

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Combination	Input data signals	CN1-Pin No.1 and 2	FRC terminal	Display colors	Remarks
1	8-bit	D3+/-	High	16,777,216	Note1
2	6-bit	GND	Low or Open	262,144	Note2

Note1: See "**4.6.2 16,777,216 colors**". Note2: See "**4.6.3 262,144 colors**".

4.6.2 16,777,216 colors

This product can display 16,777,216 colors equivalent with 256 gray scales by combination ①. (See "4.6.1 Combinations of input data signals and FRC signal".)

Also the relation between display colors and input data signals is as follows.

Display colors									Data	a sig	nal	(0: I	Low	leve	el, 1	: Hi	gh le	evel)							
Display		R7	R6	R5	R4	R3	R2	R1	R0	G	7 G6	G5	G4	G3	G2	G1	G0	В7	B6	B5	В4	В3	B2	Βl	Β0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Basic Colors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
[O]	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	,1	1	1
sic	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Ва	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	-1-	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1_	_ 1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
12		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
cal	dark	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Red gray scale						:								:								:			
gra	↓ ↓					:																:			
Sed	bright	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
<u>9</u>		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
sca	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
Green gray scale	↑					:								:								Ξ			
ın Eğ	↓					: (								:								:			
ree	bright	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
D D		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
o l		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
cal	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
ay s	1					:								:								:			
Blue gray scale	1					:								:								:			
]	bright	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
" \		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1



4.6.3 262,144 colors

This product can display 262,144 colors with 64 gray scales by combination ②. (See "4.6.1 Combinations of input data signals and FRC signal".)

Also the relation between display colors and input data signals is as follows.

Display colors							Data	ı signa	al (0:	Low	level	, 1: H	Iigh le	evel)					
Display		R5	R 4	R 3	R 2	R 1	R 0	G5	G4	G3	G2	G1	G0	В5	B4	В3	В2	B 1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	/1-	1	1
ors	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0 <	0	0	0
col	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	. 1	1
Basic colors	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Ba	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	- 1	1	/ /1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1 )	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ပ		0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
scal	dark	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red gray scale	1 ↑			:	:					/	:/-\						:		
l gr	↓			:	:								$\sim$				:		
Red	bright	1	1	1	1	0	1	0	0	0	0_	0	0	0	0	0	0	0	0
'		1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	/ 0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	-0	0	0	0	0	0	0	0	0	0
ale		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
SCS	dark	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Green gray scale	<b>↑</b>			:	: /						:						:		
ໄ ແ	↓			:							:						:		
ìrеє	bright	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
~		0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
<u>e</u>		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
sca	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
ray	1 1	. <		$\bigcirc$ :	:						:						:		
e 23	↓			:							:						:		
Blue gray scale	bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
		0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

#### 4.7 DISPLAY POSITIONS

The following table is the coordinates per pixel (See "4.8 SCANNING DIRECTIONS".).

C (0,	0) B					
C(0, 0)	C( 1, 0)		C( X, 0)		C(798, 0)	C(799, 0)
C(0, 1)	C( 1, 1)		C( X, 1)		C(798, 1)	C(799, 1)
•	•	•	•	•	•	· · · · ·
•	•	• • •			• ( ( )	\ · · ·
•	•	•	•	•	• ( \	)) •
C( 0, Y)	C( 1, Y)		C( X, Y)	• • •	C(798, Y)	C(799, Y)
•	•	•	•	•	(( • ))	•
· ·	•			• • •	\ <u>.</u> •//	·
•	•	•	•	<b>○•</b>	/ . •	•
C( 0, 598)	C( 1, 598)	• • •	C( X, 598)	• / •	C(798, 598)	C(799, 598)
C( 0, 599)	C( 1, 599)		C( X, 599)		C(798, 599)	C(799, 599)

#### 4.8 SCANNING DIRECTIONS

The following figures are seen from a front view. Also the arrow shows the direction of scan.

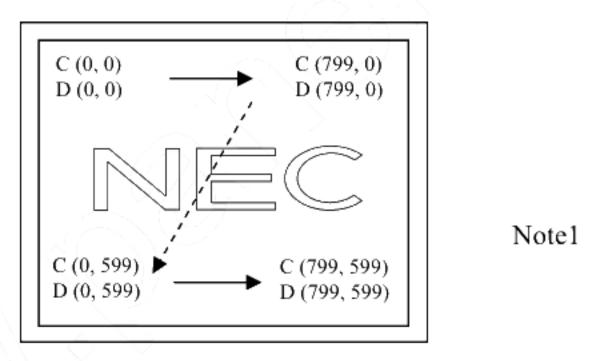


Figure 1. Normal scan (DPS: Low or Open)

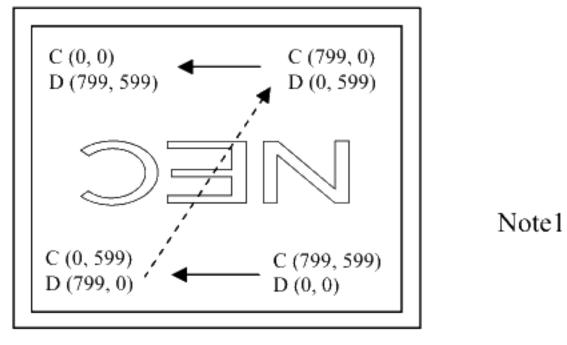


Figure 2. Reverse scan (DPS: High)

Note1: Meaning of C (X, Y) and D (X, Y)

C (X, Y): The coordinates of the display position (See "4.7 DISPLAY POSITIONS".)

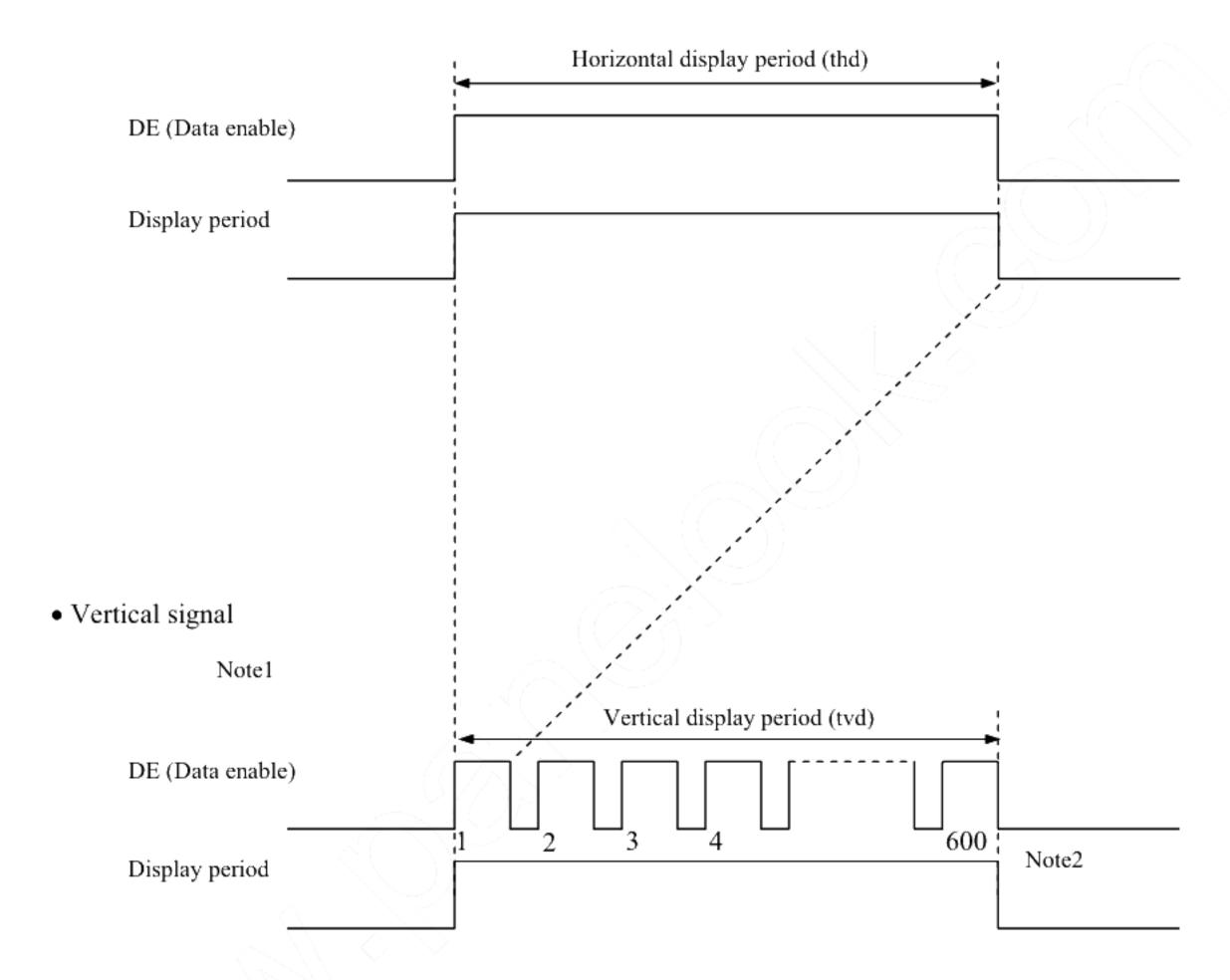
D (X, Y): The data number of input signal for LCD panel signal processing board

#### 4.9 INPUT SIGNAL TIMINGS

#### 4.9.1 Outline of input signal timings

#### • Horizontal signal

Note1



Note1: This diagram indicates virtual signal for set up to timing.

Note2: See "4.9.3 Input signal timing chart" for the pulse number.

7

#### 4.9.2 Timing characteristics

(Note1, Note2, Note3)

								21, 110102, 110103)		
	Parameter	-	Symbol	min.	typ.	max.	Unit	Remarks		
	Fre	quency	1/tc	34.0	38.362	40.0	MHz	26.067ns (typ.)		
CLK	]	Duty	-				-			
	Rise time, Fall time		-		-		ns	_		
	CLK-DATA Setup time		-				ns	/~		
DATA	CLK-DATA	Hold time	-	]	-		ns	_(-\\		
	Rise time, Fall time		-				ns			
		Cycle	th	24.0	26.693	30.1	μs			
	Horizontal	Cycle	un	-	1,024	-	CLK	37.463kHz (typ.)		
		Display period	thd		800		CLK	))		
	37	Cycle	<b>.</b>	16.1	16.683	17.2 /	ms_			
DE	Vertical (One frame)	Cycle	tv	-	625	  -	∖ н 🗍	59.94Hz (typ.)		
	(one name)	Display period	tvd		600	4	Н			
	CLK-DE	Setup time	-				ns			
	CLK-DE	Hold time	-				ns	-		
	Rise time, Fall time		-				ns			

Note1: Definition of parameters is as follows.

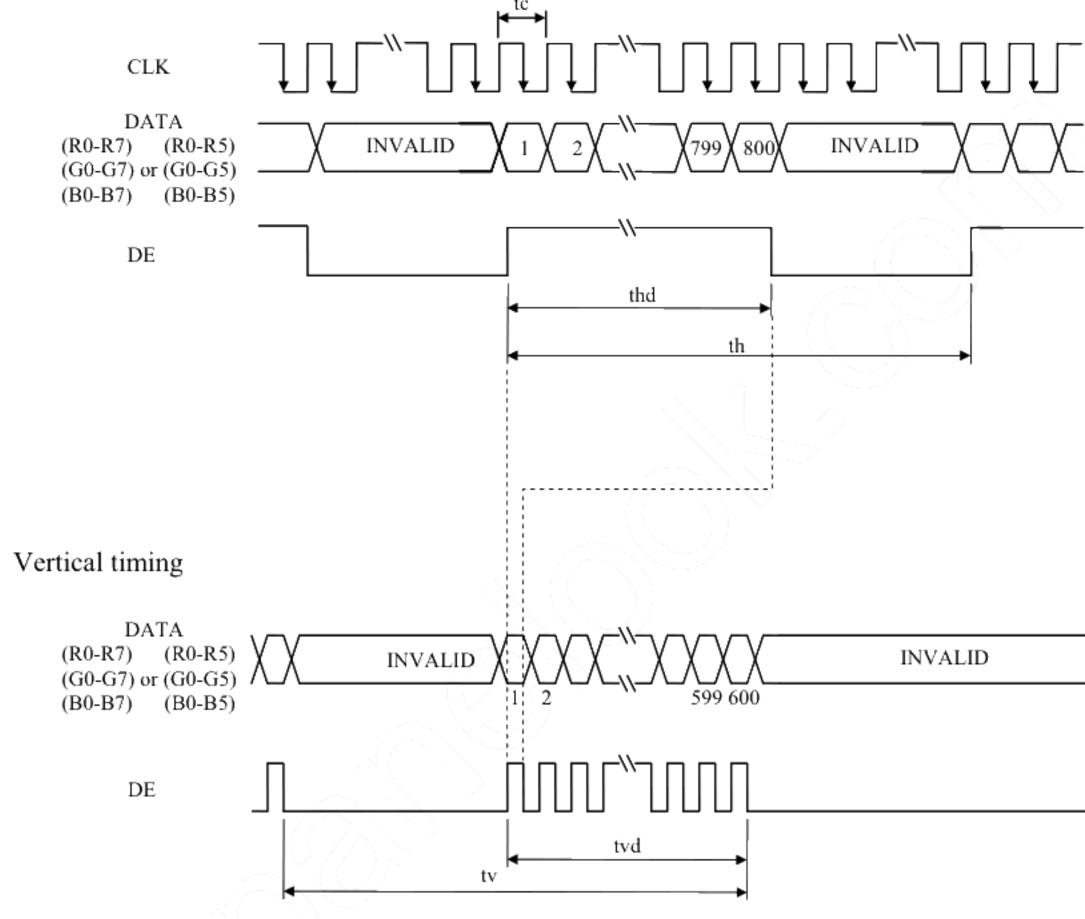
tc=1CLK, th=1H

Note2: See the data sheet of LVDS transmitter.

Note3: Vertical cycle (tv) should be specified in integral multiple of Horizontal cycle (th).

#### 4.9.3 Input signal timing chart

## Horizontal timing



#### 4.10 OPTICS

#### 4.10.1 Optical characteristics

(Note1, Note2)

								(Hotel,	
Paramete	r	Condition	Symbol	min.	typ.	max.	Unit	Measuring instrument	Remarks
Luminance		White at center $\theta R = 0^{\circ}$ , $\theta L = 0^{\circ}$ , $\theta U = 0^{\circ}$ , $\theta D = 0^{\circ}$	L	TBD	(800)	-	cd/m <sup>2</sup>	BM-5A	-
Contrast ratio		White/Black at center $\theta R = 0^{\circ}$ , $\theta L = 0^{\circ}$ , $\theta U = 0^{\circ}$ , $\theta D = 0^{\circ}$	CR	TBD	(900)	-	-	BM-5A	Note3
Luminance unit	formity	White $\theta R = 0^{\circ}$ , $\theta L = 0^{\circ}$ , $\theta U = 0^{\circ}$ , $\theta D = 0^{\circ}$	LU	-	1.25	1.4	-	BM-5A	Note4
	White	x coordinate	Wx	TBD	TBD	TBD	-6		
	White	y coordinate	Wy	TBD	TBD	TBD	(=(		
	Red	x coordinate	Rx	-	TBD	- /	> <del>-</del> >	2)	
Chromotioity	Reu	y coordinate	Ry	-	TBD	- 47	-3		
Chromaticity	Graan	x coordinate	Gx	-	TBD	-//	-17	CD 2	Nota5
	Green	y coordinate	Gy	-	TBD	- \		SR-3	Note5
	Blue	x coordinate	Bx	-	TBD		-		
	Blue	y coordinate	Ву	-	TBD	~~ <u>.</u> ,▽	-		
Color gam	ut	$\theta$ R= 0°, $\theta$ L= 0°, $\theta$ U= 0°, $\theta$ D= 0° at center, against NTSC color space	С	TBD	40	} '	%		
Dasnonsa ti	ma	White to Black	Ton	-//	3	5	ms	BM-5A	Note6
Response time		Black to White	Toff	~- <u>-</u> -\	15	21	ms	DWI-JA	Note7
Right Viewing angle Left		θU= 0°, θD= 0°, CR≥ 10	θR	70	80	-	0		
		θU= 0°, θD= 0°, CR≥ 10	θL	70	80	-	0	EZ	Nota 0
Viewing angle	Up	$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}, \ CR \ge 10$	θU	70	80	-	٥	Contrast	Note8
	Down	$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}, \ CR \ge 10$	θD	<sup>2</sup> 70	80	-	٥		

Note1: These are initial characteristics.

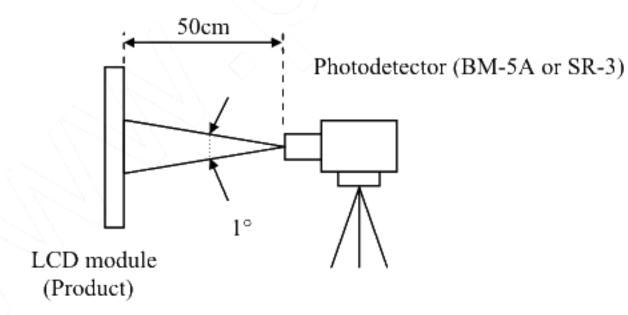
Note2: Measurement conditions are as follows.

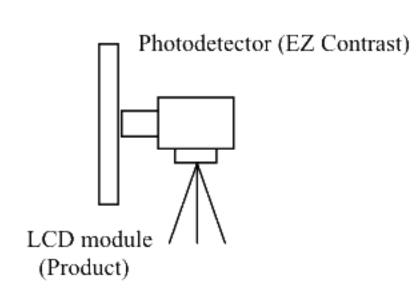
Ta= 25°C, VCC= 3.3V, IL= 50mA/One circuit, Display mode: SVGA, Horizontal cycle= 1/37.463kHz, Vertical cycle= 1/59.94Hz, DPS= Low or Open: Normal scan

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Optical characteristics are measured at luminance saturation 20minutes after the product works, in the dark room. Also measurement methods are as follows.





Note3: See "4.10.2 Definition of contrast ratio".

Note4: See "4.10.3 Definition of luminance uniformity".

Note5: These coordinates are found on CIE 1931 chromaticity diagram.

Note6: Product surface temperature: TopF= TBD °C

Note7: See "4.10.4 Definition of response times".

Note8: See "4.10.5 Definition of viewing angles".

#### 4.10.2 Definition of contrast ratio

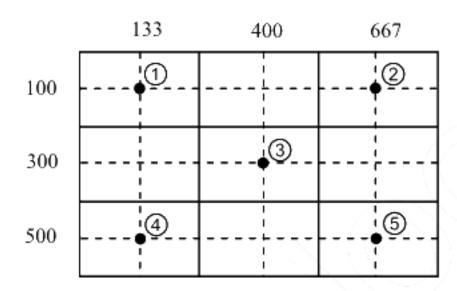
The contrast ratio is calculated by using the following formula.

#### 4.10.3 Definition of luminance uniformity

The luminance uniformity is calculated by using following formula.

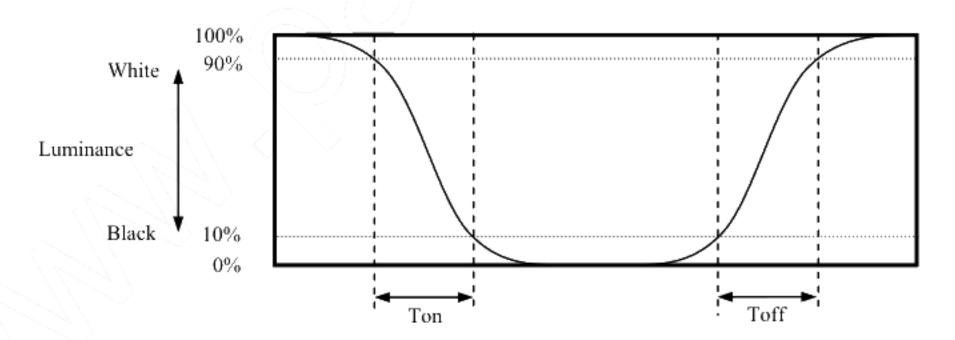
Luminance uniformity (LU) = 
$$\frac{\text{Maximum luminance from } \textcircled{1} \text{ to } \textcircled{5}}{\text{Minimum luminance from } \textcircled{1} \text{ to } \textcircled{5}}$$

The luminance is measured at near the 5 points shown below.

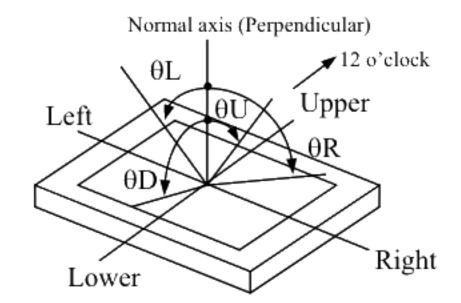


#### 4.10.4 Definition of response times

Response time is measured at the time when the luminance changes from "white" to "black", or "black" to "white" on the same screen point, by photo-detector. Ton is the time when the luminance changes from 90% down to 10%. Also Toff is the time when the luminance changes from 10% up to 90% (See the following diagram.).



#### 4.10.5 Definition of viewing angles



#### 5. ESTIMATED LUMINANCE LIFETIME

The luminance lifetime is the time from initial luminance to half-luminance.

This lifetime is the estimated value, and is not guarantee value.

	, ,		
	Condition	Estimated luminance lifetime (Life time expectancy)	Unit
		Note1, Note2, Note3	
LED	25°C (Ambient temperature of LED) Continuous operation, IL=50mA/one circuit	70,000	h
elementary substance	70°C (Ambient temperature of LED) Continuous operation, IL=50mA/one circuit	60,000	h

Note1: Life time expectancy is mean time to half-luminance.

Note2: Estimated luminance lifetime is not the value for an LCD module but the value for LED elementary substance.

Note3: By ambient temperature, the lifetime changes particularly. Especially, in case the product works under high temperature environment, the lifetime becomes short.





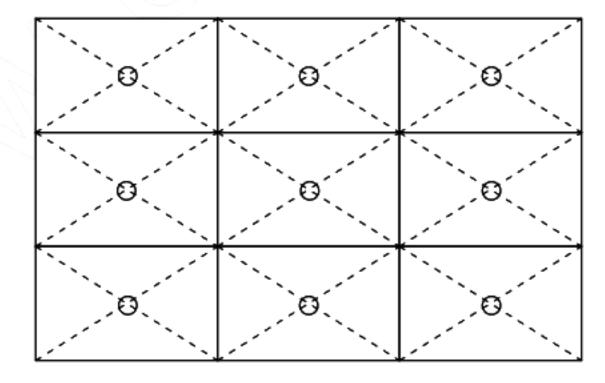


#### 6. RELIABILITY TESTS

Test item	Condition	Judgment		
High temperature and humidity (Operation)	① 60 ± 2°C, RH= 90%, 240hours ② Display data is black.			
High temperature (Operation)	<ul> <li>1 80 ± 3°C, 240hours</li> <li>2 Display data is black.</li> </ul>			
Heat cycle (Operation)	<ul> <li>30 ± 3°C1hour</li> <li>80 ± 3°C1hour</li> <li>50cycles, 4 hours/cycle</li> <li>Display data is black.</li> </ul>			
Thermal shock (Non operation)	<ul> <li>30 ± 3°C30minutes</li> <li>80 ± 3°C30minutes</li> <li>100cycles, 1hour/cycle</li> <li>Temperature transition time is within 5 minutes.</li> </ul>	No display malfunctions Note1		
ESD (Operation)	<ul> <li>① 150pF, 150Ω, ±10kV</li> <li>② 9 places on a panel surface Note2</li> <li>③ 10 times each places at 1 sec interval</li> </ul>			
Dust (Operation)	<ul> <li>Sample dust: No. 15 (by JIS-Z8901))</li> <li>15 seconds stir</li> <li>8 times repeat at 1 hour interval</li> </ul>			
Vibration (Non operation)	<ul> <li>5 to 100Hz, 19.6m/s²</li> <li>1 minute/cycle</li> <li>X, Y, Z directions</li> <li>120 times each directions</li> </ul>	No display malfunctions No physical damages		
Mechanical shock (Non operation)	<ul> <li>539m/ s², 11ms</li> <li>±X, ±Y, ±Z directions</li> <li>5 times each directions</li> </ul>	Note1		

Note1: Display and appearance are checked under environmental conditions equivalent to the inspection conditions of defect criteria.

Note2: See the following figure for discharge points.



#### 7. PRECAUTIONS

#### 7.1 MEANING OF CAUTION SIGNS

The following caution signs have very important meaning. Be sure to read "7.2 CAUTIONS" and "7.3 ATTENTIONS"!



This sign has the meaning that a customer will be injured or the product will sustain damage if the customer practices wrong operations.



This sign has the meaning that a customer will be injured if the customer practices wrong operations.

#### 7.2 CAUTIONS



\* Do not shock and press the LCD panel and the backlight! There is a danger of breaking, because they are made of glass. (Shock: Equal to or no greater than 539m/s<sup>2</sup> and equal to or no greater than 11ms, Pressure: Equal to or no greater than 19.6 N (\$16mm jig))

### 7.3 ATTENTIONS



#### 7.3.1 Handling of the product

- 1 Take hold of both ends without touching the circuit board when the product (LCD module) is picked up from inner packing box to avoid broken down or misadjustment, because of stress to mounting parts on the circuit board.
- ② When the product is put on the table temporarily, display surface must be placed downward.

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- 3 When handling the product, take the measures of electrostatic discharge with such as earth band, ionic shower and so on, because the product may be damaged by electrostatic.
- ④ The torque for product mounting screws must never exceed 0.294N·m. Higher torque might result in distortion of the bezel.
- ⑤ The product must be installed using mounting holes without undue stress such as bends or twist (See outline drawings). And do not add undue stress to any portion (such as bezel flat area). Bends or twist described above and undue stress to any portion may cause display mura.
- ⑤ Do not press or rub on the sensitive product surface. When cleaning the product surface, wipe it with a soft dry cloth.
- ② Do not push or pull the interface connectors while the product is working.
- ® When handling the product, use of an original protection sheet on the product surface (polarizer) is recommended for protection of product surface. Adhesive type protection sheet may change color or characteristics of the polarizer.
- Usually liquid crystals don't leak through the breakage of glasses because of the surface tension of thin layer and the construction of LCD panel. But, if you contact with liquid crystal by any chance, please wash it away with soap and water.

#### 7.3.2 Environment

- ① Do not operate or store in high temperature, high humidity, dewdrop atmosphere or corrosive gases. Keep the product in packing box with antistatic pouch in room temperature to avoid dusts and sunlight, when storing the product.
- ② In order to prevent dew condensation occurred by temperature difference, the product packing box must be opened after enough time being left under the environment of an unpacking room. Evaluate the storage time sufficiently because dew condensation is affected by the environmental temperature and humidity. (Recommended leaving time: 6 hours or more with the original packing state after a customer receives the package)
- 3 Do not operate in high magnetic field. If not, circuit boards may be broken down by it.
- This product is not designed as radiation hardened.

#### 7.3.3 Characteristics

#### The following items are neither defects nor failures.

- ① Characteristics of the LCD (such as response time, luminance, color uniformity and so on) may be changed depending on ambient temperature. If the product is stored under condition of low temperature for a long time, it may cause display mura. In this case, the product should be operated after enough time being left under condition of operating temperature.
- ② Display mura, flickering, vertical streams or tiny spots may be observed depending on display patterns.
- 3 Do not display the fixed pattern for a long time because it may cause image sticking. Use a screen saver, if the fixed pattern is displayed on the screen.
- The display color may be changed depending on viewing angle because of the use of condenser sheet in the backlight.
- ⑤ Optical characteristics may be changed depending on input signal timings.
- The product gives AR (antireflection) coating of the polarizer surface. Though AR (antireflection) coating actualizes the low reflection with the multilayer structure, the color of reflection may differ between products and the color change of reflection may occur in the same product by fluctuation of AR (antireflection) coating.

#### 7.3.4 Others

- ① All VCC and GND terminals should be used without any non-connected lines.
- ② Do not disassemble a product or adjust variable resistors.
- ③ See "REPLACEMENT MANUAL FOR LAMP HOLDER SET", when replacing lamp holder set.
- Pack the product with the original shipping package, in order to avoid any damages during transportation, when returning the product to NEC for repairing and so on.

2

2

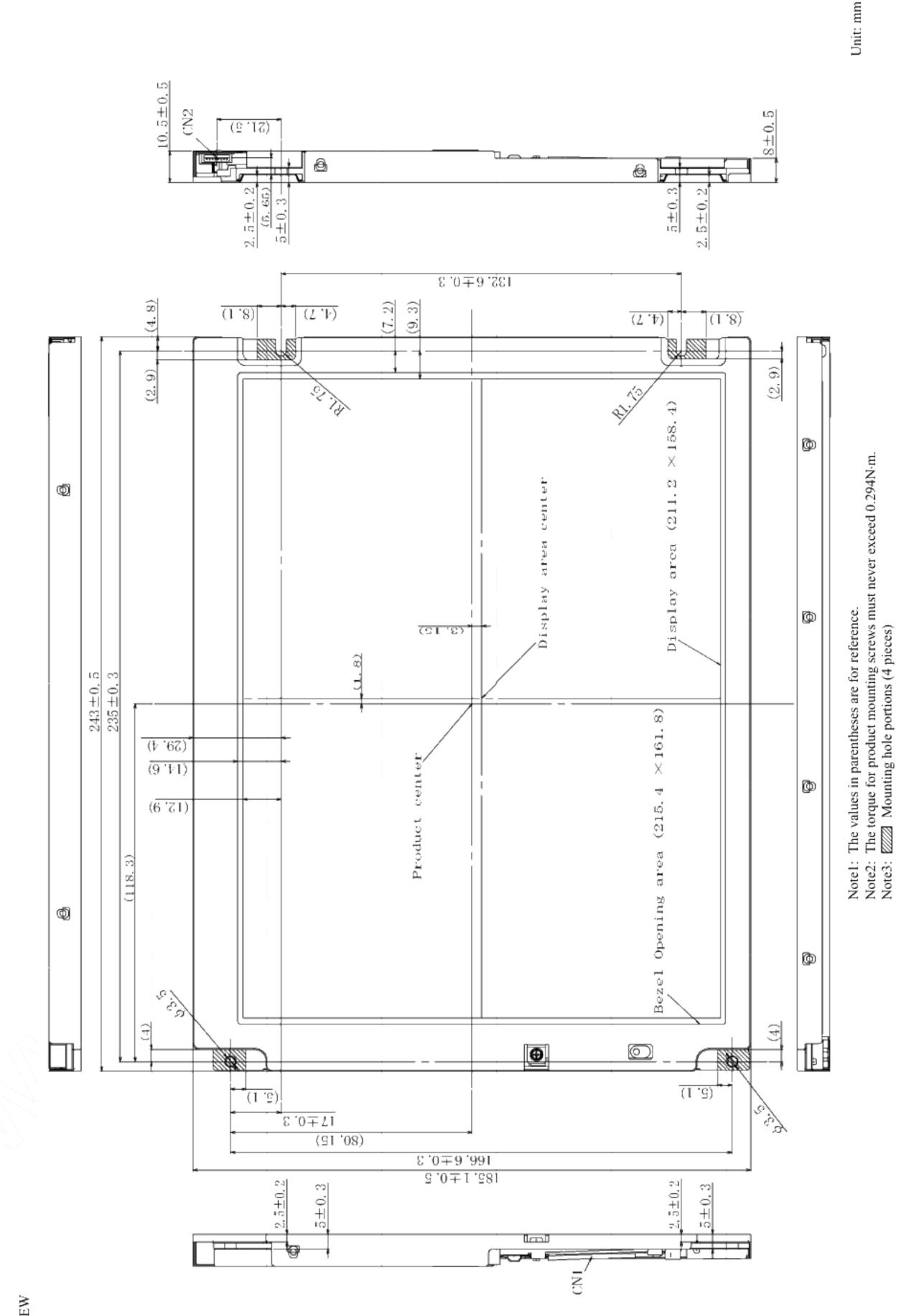
7

PRELIMINARY DATA SHEET DOD-PP-0966 (2nd edition)

# NEC NEC LCD Technologies, Ltd.

# 8. OUTLINE DRAWINGS

8.1 FRONT VIEW



£4

NEC LCD Technologies, Ltd.

8.2 REAR VIEW

P CN1 (150) (6.8) (2) (9) 9 0 Φ 0 (1) 80 (5.6)(6.6) (4) Maid : NI SORDELZG - 355C SERIAL - Control of the Control NEC NEC LCO Technologies, Ltd. CNI FI-SE20P-HFE (11) CN2 (SM08B-SRSS-TB) (4.5)蜡 3,14 (1) بالعال (4.5) œ

(8'9)

(8)

Ecoll.

(8,8)

(8)

Note1: The values in parentheses are for reference.

Note2: The torque for product mounting screws must never exceed 0.294N·m. Note3: ZZZZ Mounting hole portions (4 pieces)

#### REVISION HISTORY

The inside of latest specifications is revised to the clerical error and the major improvement of previous edition. Only a changed part such as functions, characteristic value and so on that may affect a design of customers, are described especially below.

Edition	Document	Prepared	Revision contents and si	onature
Eartion	number	date	Revision contents and si	gnature
1st edition	DOD-PP -0700	Dec. 25, 2008	Revision contents  New issue	
			Writer	
			Approved by Checked by H. FUKUYOSHI	Prepared by E. KATAYAMA
2nd edition	DOD-PP -0966	Apr. 6, 2010	Revision contents	
			<ul> <li>P1 TOP SHEET <ul> <li>LVDS interface (1port) (addition)</li> </ul> </li> <li>P4 Features <ul> <li>Replaceable LED holder for backlight →Replacea</li> </ul> </li> <li>P5 General specifications <ul> <li>Contrast ratio: (1,000: 1) (typ.) → (900: 1) (typ.)</li> <li>Luminance: At IL=50mA → At IL=50mA/One cit</li> <li>Backlight <ul> <li>Recommended LED Driver Board (option) (addition)</li> <li>Power consumption: At IL=50mA, (7.3)W (typ.) → A</li> </ul> </li> <li>Power dissipation of the inverse power of the inverse power dissipation of the inverse power disperse power dissipation of the inverse power disperse power</li></ul></li></ul>	reuit dition) At IL=50mA/One circuit, TBD (typ.) erter is not included. (elimination) $C \rightarrow -30 \text{ to } +80 ^{\circ}\text{C}$
			<ul> <li>Note3, Note4: " at center of LCD " → " at L0"</li> <li>Note6: Ta= 70°C and RH= 36% → Ta= 80°C and P10 Backlight lamp</li> <li>Chart (revised)</li> <li>Note2 (change of expression)</li> <li>Note3 (elimination)</li> <li>P10 Power supply voltage ripple (change of expression)</li> <li>P10 Fuse</li> <li>Note1 (change of expression)</li> <li>P11 LCD panel signal processing board</li> <li>Note1, Note2 (change of expression)</li> <li>P11 LED Driver board (Option) (title is changed)</li> <li>P16 Combinations of input data signals and FRC signal</li> <li>P17 16,777,216 colors (change of expression)</li> <li>P18 262,144 colors (change of expression)</li> <li>P20 Outline of input signal timings</li> <li>Note2 (change of expression)</li> <li>P23 Optical characteristics</li> <li>Contrast ratio: (1,000) (typ.) → (900) (typ.)</li> <li>Note2: IL=50mA → IL=50mA/One circuit, (change)</li> <li>P24 Definition of response times (change of expression)</li> </ul>	RH= 24%  (change of expression)  ge of expression)

#### REVISION HISTORY

Edition	Document number	Prepared date	Revision contents and signature
2nd	DOD-PP	Apr. 6,	Revision contents
edition	-0966	2010	P25 Estimated luminance lifetime  • Expected luminance lifetime → Estimated luminance lifetime (Life time expectancy)  • IL=50mA → IL=50mA/One circuit  • Note1 (addition)  • Note2 (change of expression)  P26 Reliability tests
			• High temperature- $\textcircled{1}$ : $70 \pm 3^{\circ}\text{C} \rightarrow 80 \pm 3^{\circ}\text{C}$
			<ul> <li>Heat cycle-①: -20 ± 3°C, 70 ± 3°C → -30 ± 3°C, 80 ± 3°C</li> <li>P27-28 PRECAUTIONS</li> <li>Meaning of caution signs (change of expression)</li> <li>Cautions (change of expression)</li> <li>Handling of product: Do not hook " any damage. (elimination) <ul> <li>⑥, ⑦, ⑨ (change of expression)</li> </ul> </li> <li>Environment:②, ③ (change of expression)</li> <li>Characteristics:② (change of expression)</li> <li>The interference " does not appear. (elimination)</li> <li>Others: Pay attention " tapping screws. (elimination)</li> <li>③, ④ (change of expression)</li> </ul> <li>P29-30 Outline drawings <ul> <li>Front view (revised)</li> <li>Rear view (revised)</li> </ul> </li> <li>Signature of writer</li>
			Approved by  Checked by  Prepared by  T. OGAWA  Checked by  Prepared by  T. OGAWA  T. OGAWA