SPEC

Spec No.	TQ3C-8EAF0-E1DDE81-02
Date	April 7, 2008

TYPE : TCG057QVLBA-G00

< 5.7 inch QVGA transmissive color TFT with LED backlight>

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Issued
Date:Apr.9,2008
KYOCERA
Hayato LCD Division

KYOCERA CORPORATION
KAGOSHIMA HAYATO PLANT
LCD DIVISION

This specification is subject to change without notice.
Consult Kyocera before ordering.

Original Issue Date	Designed by: Engineering dept.			Confirmed by: QA dept.	
	Prepared	Checked	Approved	Checked	Approved
September 8, 2007	S. Kojima		Y. Matsumoto	J. Sakaguchi	

Warning

1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.

2. Customer agrees to indemnity, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision record

Date	Designed by : Engineering dept.			Confirmed by : QA dept.	
	Prepared	Checked	Approved	Checked	Approved
Apr. 7, 2008	S. Kojima		Y. Matsumoto	J. Sakaguchi	

Rev.No.	Date	Page	Descriptions
01	Dec. 12, 2007	1	2. Construction and outline ～Add comment "Additional circuit"
			3. Mechanical specifications ～Change "Mass"
		2	4. Absolute maximum ratings ～Delete ( )
		3	5. Electrical characteristics ～Delete ( ) ～Add "Current consumption value"
		4	6. Optical characteristics ～Delete ( ) ～Change all
		9	9. LED backlight characteristics ～Delete ( ) ～Change "Operating life time"
		13	14. Outline Drawing ～Change "121A5062800" → "121A5062800-1"
02	Apr. 7, 2008	14	14. Outline Drawing ～Add Outline Drawing "121A5065700"

1. Application

This document defines the specification of TCG057QVLBA-G00. (RoHS Compliant)

2. Construction and outline

LCD	: Transmissive color dot matrix type TFT
Backlight system	: LED
Polarizer	: Glare treatment
Additional circuit	: Timing controller, Power supply (3.3V input) (without constant current circuit for LED Backlight)

3. Mechanical specifications

Item	Specification	Unit
Outline dimensions	127.2 (W)× 100.4 (H) × 5.7 (D)	mm
Active area	115.2 (W) × 86.4 (H) (14.4cm / 5.7 inch (Diagonal))	mm
Dot format	320×(B,G,R) (W) × 240 (H)	dot
Dot pitch	0.12 (W) × 0.36 (H)	mm
Base color *1	Normally White	-
Mass	110	g

*1 Due to the characteristics of the LCD material, the color varies with environmental temperature.

4. Absolute maximum ratings

4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	V_{DD}	0	4.0	V
Input signal voltage *1	V_{IN}	-0.3	6.0	V
LED forward current *2	IF	-	30	mA
Reversed voltage *2	VR	-	5	V

*1 Input signal : CK, R0～R5, G0～G5, B0～B5, H_{SYNC}, V_{SYNC}, ENAB, R/L, U/D
*2 For each "AN1-CA1", "AN2-CA2" and "AN3-CA3" Temp. = 25°C

4-2. Environmental absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Operating temperature *1	T_{OP}	-20	70	°C
Storage temperature *2	T_{STO}	-30	80	°C
Operating humidity *3	H_{OP}	10	*4	%RH
Storage humidity *3	H_{STO}	10	*4	%RH
Vibration	-	*5	*5	-
Shock	-	*6	*6	-

*1 Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

*2 Temp. = -30°C＜48h , Temp. = 80°C＜168h
Store LCD panels at normal temperature/humidity. Keep them free from vibration and shock.
An LCD panel that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.
(Please refer to "Precautions for Use" for details.)

*3 Non-condensing

*4 Temp.≦40°C, 85%RH Max.
Temp.＞40°C, Absolute humidity shall be less than 85%RH at 40°C.

*5

Frequency	10～55 Hz	Acceleration value (0.3～9 m/s^2)
Vibration width	0.15mm	
Interval	10-55-10 Hz 1 minutes	

2 hours in each direction X, Y, Z (6 hours total)
EIAJ ED-2531

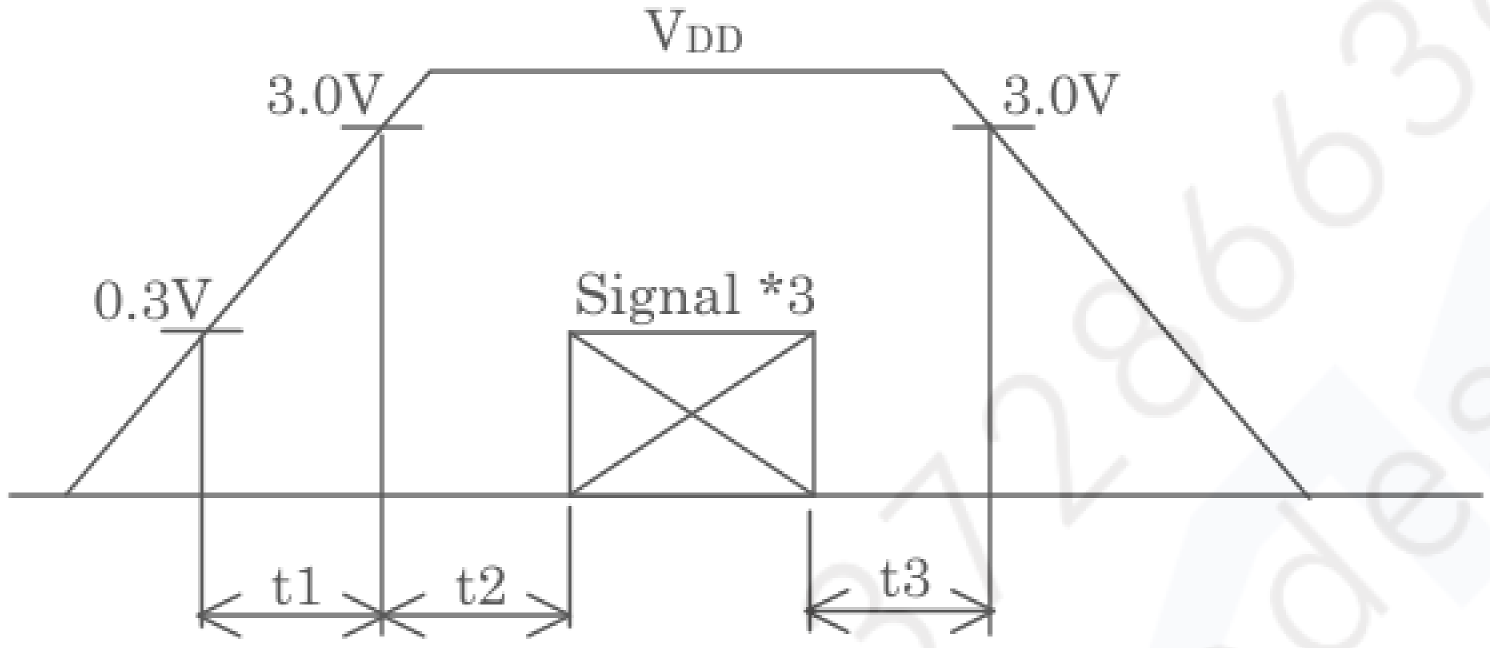
*6 Acceleration: 490 m/s^2, Pulse width: 11 ms
3 times in each direction: ±X, ±Y, ±Z
EIAJ ED-2531

5. Electrical characteristics

Temp. = -20～70°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage for logic *1	V_{DD}	-	3.0	3.3	3.6	V
Current consumption for logic	I_{DD}	*2	-	60	80	mA
Permissive input ripple voltage	V_{RP}	-	-	-	100	mVp-p
Input signal voltage *3	V_{IL}	"Low" level	0	-	$0.3V_{DD}$	V
	V_{IH}	"High" level	$0.7V_{DD}$	-	V_{DD}	V

*1 V_{DD}-turn-on conditions


VDD
3.0V
3.0V
0.3V
Signal *3
t1
t2
t3

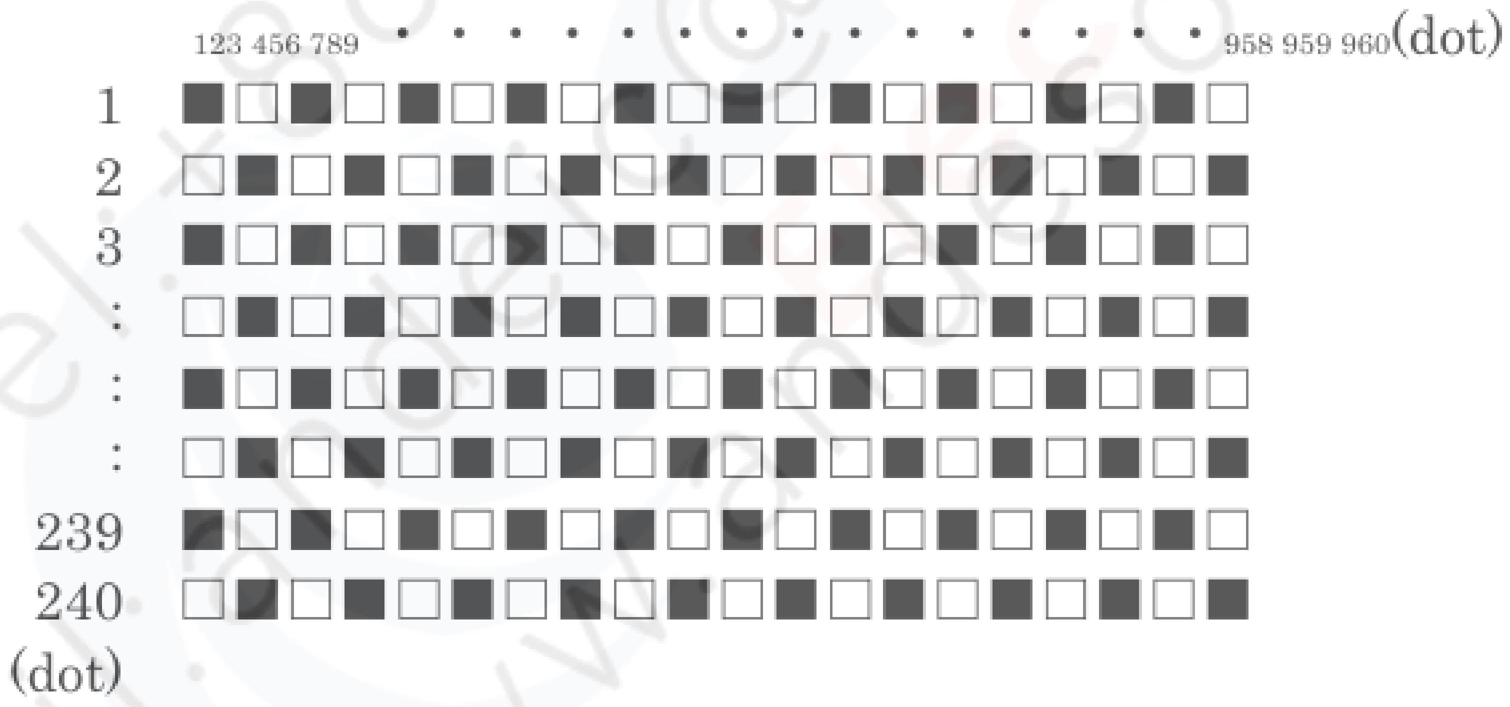
0＜t1≦20ms

0＜t2≦50ms

0＜t3≦1s

*2 Display pattern:

V_{DD} = 3.3V, Temp. = 25°C


123 456 789
958 959 960(dot)
1
2
3
:
:
:
239
240
(dot)

*3 Input signal : CK, R0～R5, G0～G5, B0～B5, H_{SYNC}, V_{SYNC}, ENAB, R/L, U/D

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6. Optical characteristics

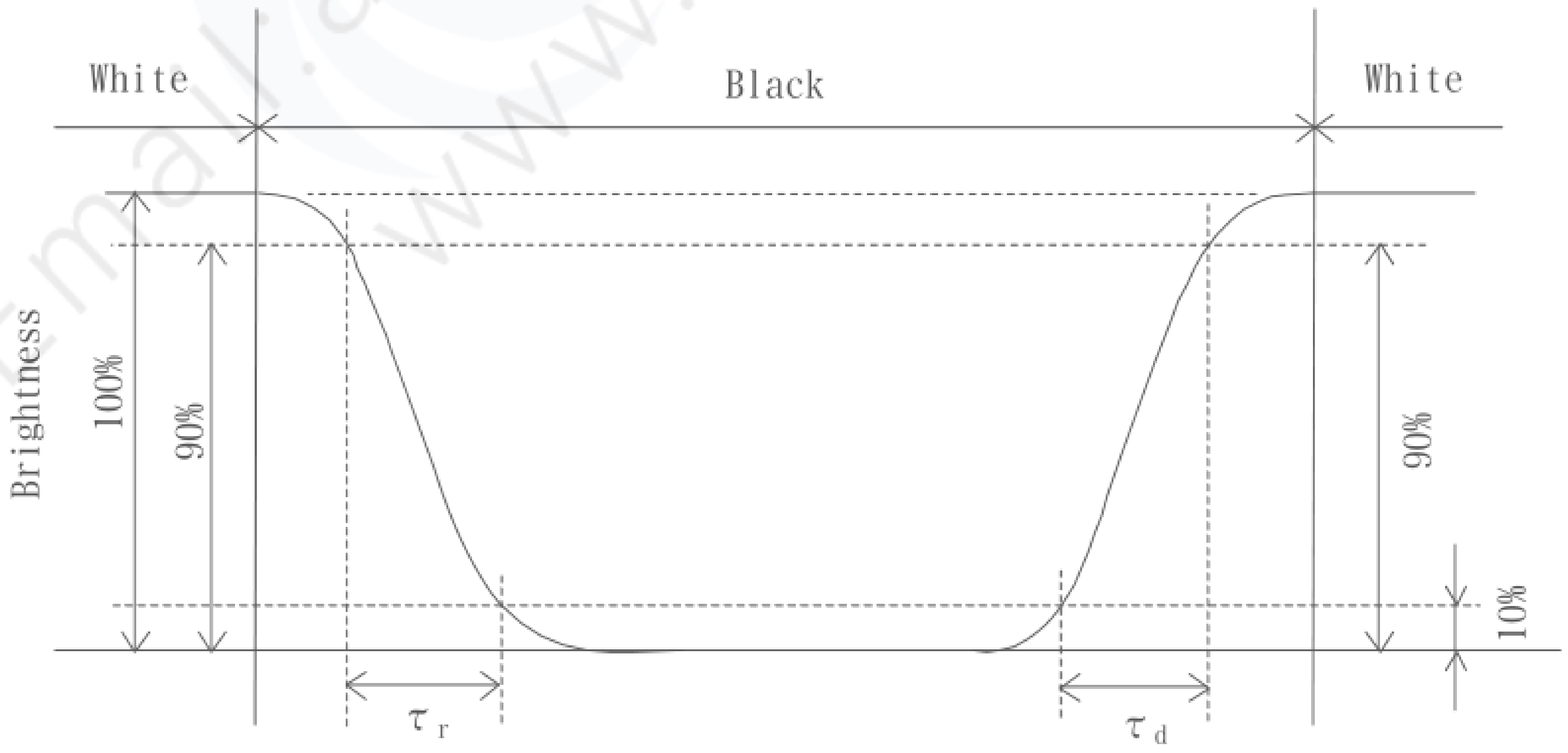
Measuring spot = ϕ 6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Typ.	Max.	Unit
Response time	Rise	τ_r	$\theta=\phi=0°$	-	10	-	ms
	Down	τ_d	$\theta=\phi=0°$	-	25	-	ms
Viewing angle range View deriction : 12 o'clock (Gray inversion)		θ_{UPPER}	CR≧5	-	80	-	deg.
		θ_{LOWER}		-	80	-	
		ϕ_{LEFT}		-	80	-	deg.
		ϕ_{RIGHT}		-	80	-	
Contrast ratio		CR	$\theta=\phi=0°$	300	500	-	-
Brightness		L	IF=15mA/Line	210	300	-	cd/m^2
Chromaticity coordinates	Red	x	$\theta=\phi=0°$	0.57	0.62	0.67	-
		y		0.32	0.37	0.42	
	Green	x	$\theta=\phi=0°$	0.28	0.33	0.38	
		y		0.54	0.59	0.64	
	Blue	x	$\theta=\phi=0°$	0.09	0.14	0.19	
		y		0.04	0.09	0.14	
	White	x	$\theta=\phi=0°$	0.27	0.32	0.37	
		y		0.29	0.34	0.39	

6-1. Definition of contrast ratio

$$\text{CR(Contrast ratio)} = \frac{\text{Brightness with all pixels "White"}}{\text{Brightness with all pixels "Black"}}$$

6-2. Definition of response time



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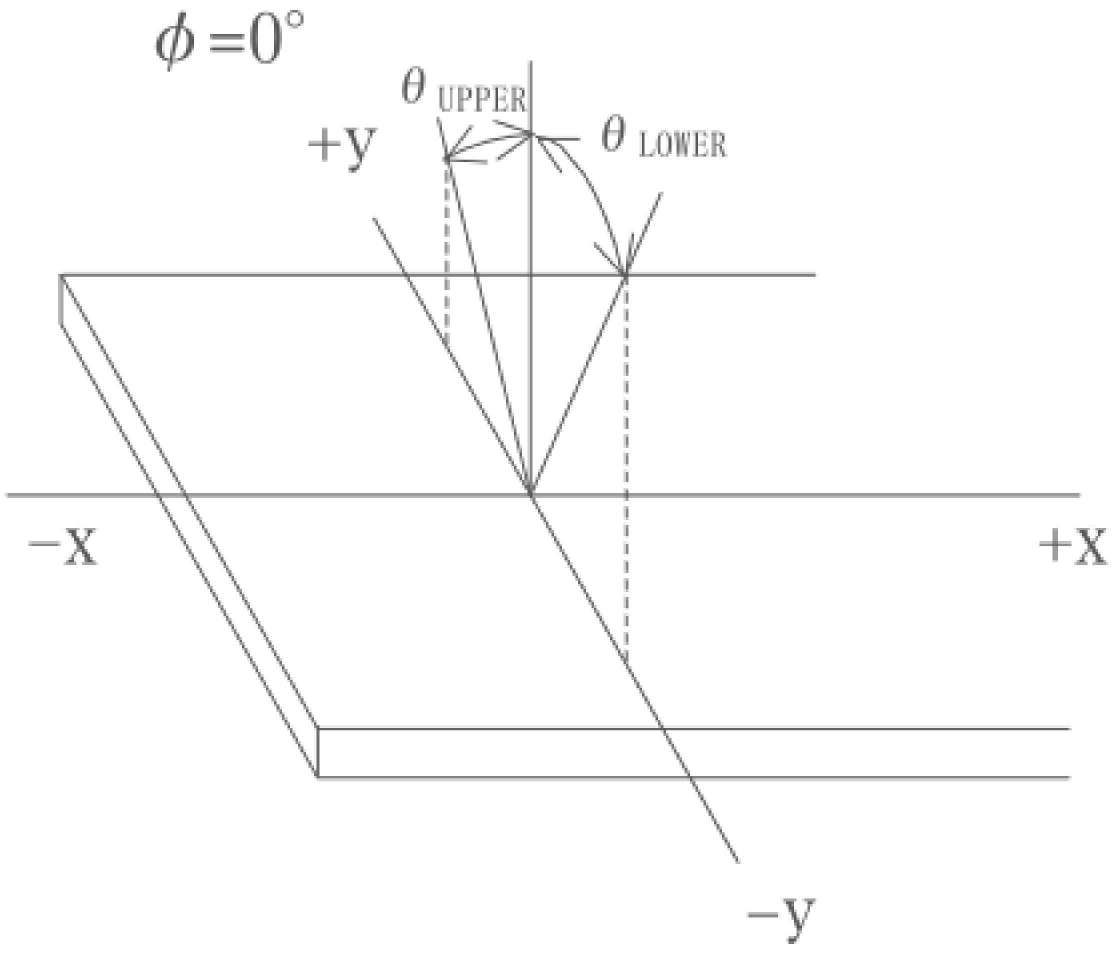
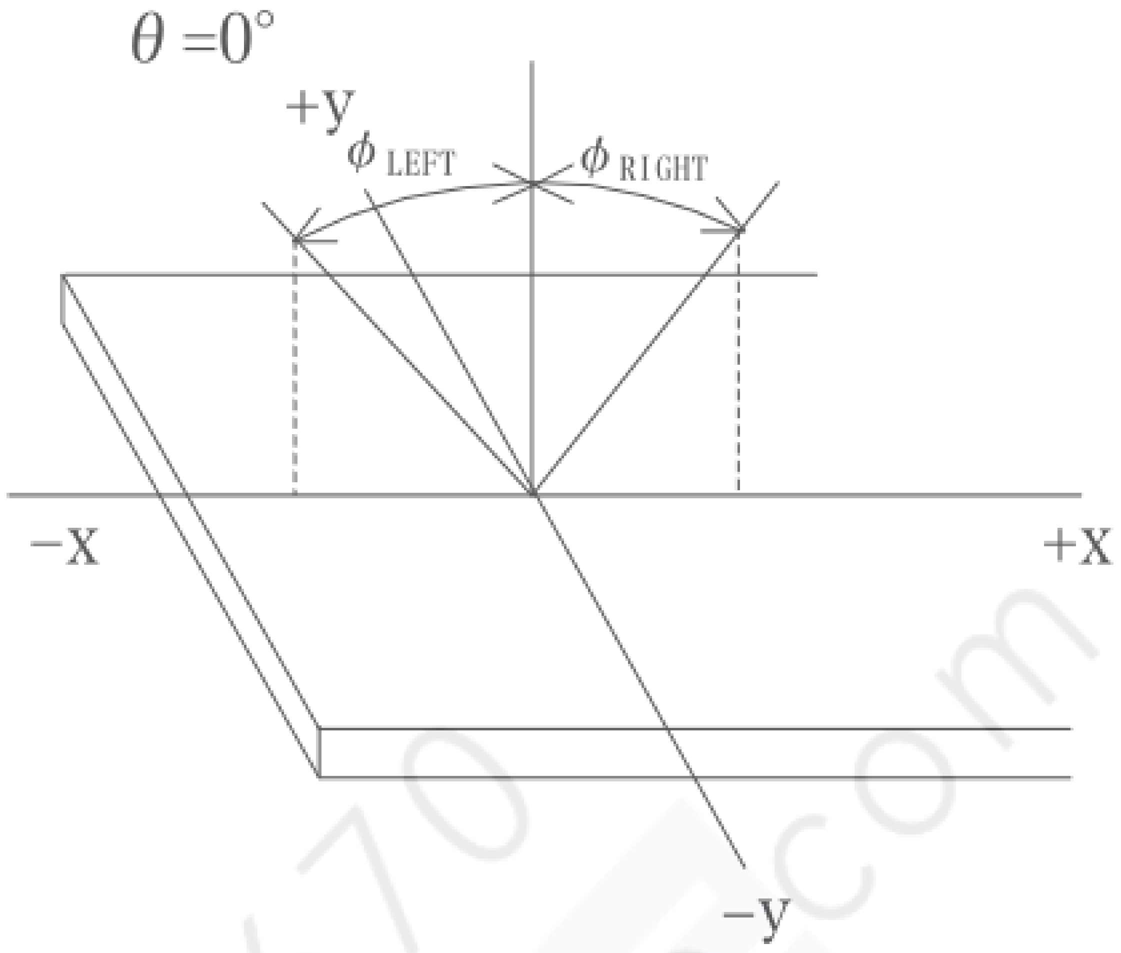
6-3. Definition of viewing angle

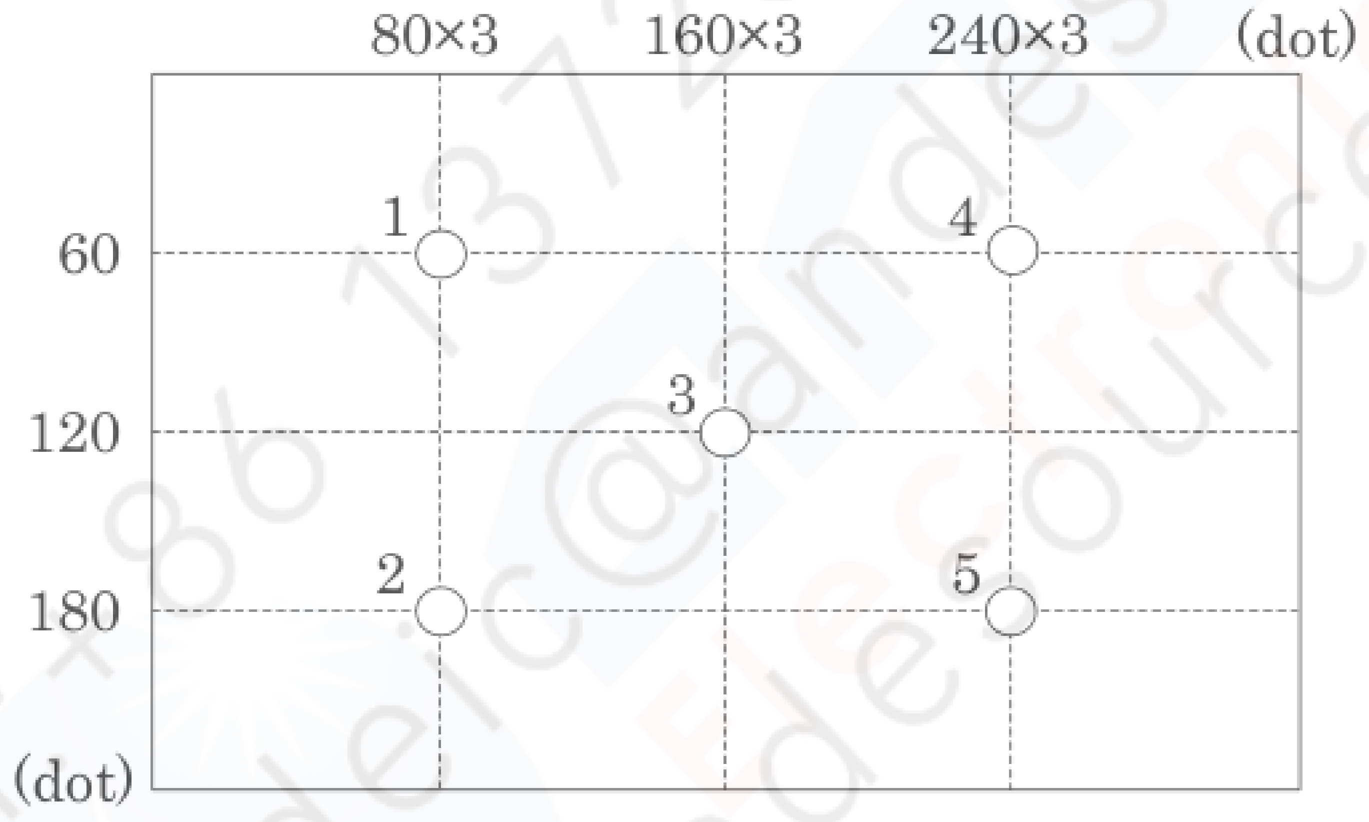

ϕ =0°
θ UPPER
θ LOWER
+y
−X
+X
−y

θ direction


θ =0°
+y
ϕ LEFT
ϕ RIGHT
−X
+X
−y

φ direction

6-4. Brightness measuring points


80×3
160×3
240×3
(dot)
60
120
180
(dot)
1
2
3
4
5

1) Rating is defined on the average in the viewing area.
2) Measured 30 minutes after the LED is powered on. (Ambient temp. = 25°C)

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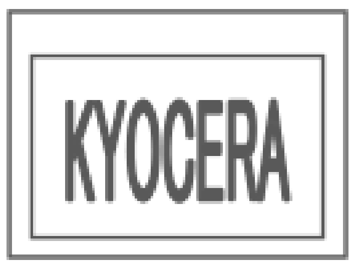
7. Interface signals

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	CK	Clock signal for sampling each data signal	I	
3	H_{SYNC}	Horizontal synchronous signal (negative)	I	
4	V_{SYNC}	Vertical synchronous signal (negative)	I	
5	GND	GND	-	
6	R0	RED data signal (LSB)	I	
7	R1	RED data signal	I	
8	R2	RED data signal	I	
9	R3	RED data signal	I	
10	R4	RED data signal	I	
11	R5	RED data signal (MSB)	I	
12	GND	GND	-	
13	G0	GREEN data signal (LSB)	I	
14	G1	GREEN data signal	I	
15	G2	GREEN data signal	I	
16	G3	GREEN data signal	I	
17	G4	GREEN data signal	I	
18	G5	GREEN data signal (MSB)	I	
19	GND	GND	-	
20	B0	BLUE data signal (LSB)	I	
21	B1	BLUE data signal	I	
22	B2	BLUE data signal	I	
23	B3	BLUE data signal	I	
24	B4	BLUE data signal	I	
25	B5	BLUE data signal (MSB)	I	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	I	*1
28	V_{DD}	3.3V power supply	-	
29	V_{DD}	3.3V power supply	-	
30	R/L	Horizontal display mode select signal L : Normal , H : Left / Right reverse mode	I	*2
31	U/D	Vertical display mode select signal H : Normal , L : Up / Down reverse mode	I	
32	NC	No connect	-	
33	CA1	Cathode 1	-	
34	CA2	Cathode 2	-	
35	CA3	Cathode 3	-	
36	NC	No connect	-	
37	AN1	Anode 1	-	
38	AN2	Anode 2	-	
39	AN3	Anode 3	-	
40	NC	No connect	-	

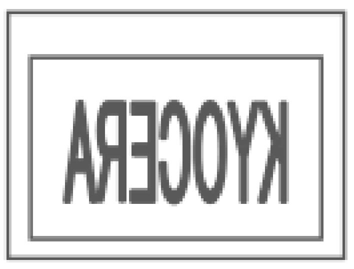
LCD connector	: IMSA-9681S-40A-GF (IRISO)
Recommended matching FFC or FPC	: 0.5mm pitch

*1 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal.
In case ENAB is fixed "Low", the horizontal start timing is determined.
Don't keep ENAB "High" during operation.

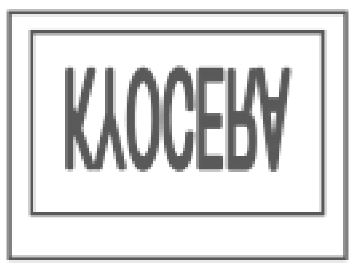
*2



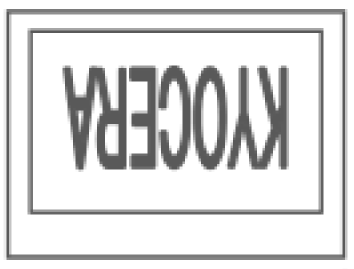
R/L = L
U/D = H



R/L = H
U/D = H



R/L = L
U/D = L



R/L = H
U/D = L

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8. Input timing characteristics

8-1. Timing characteristics

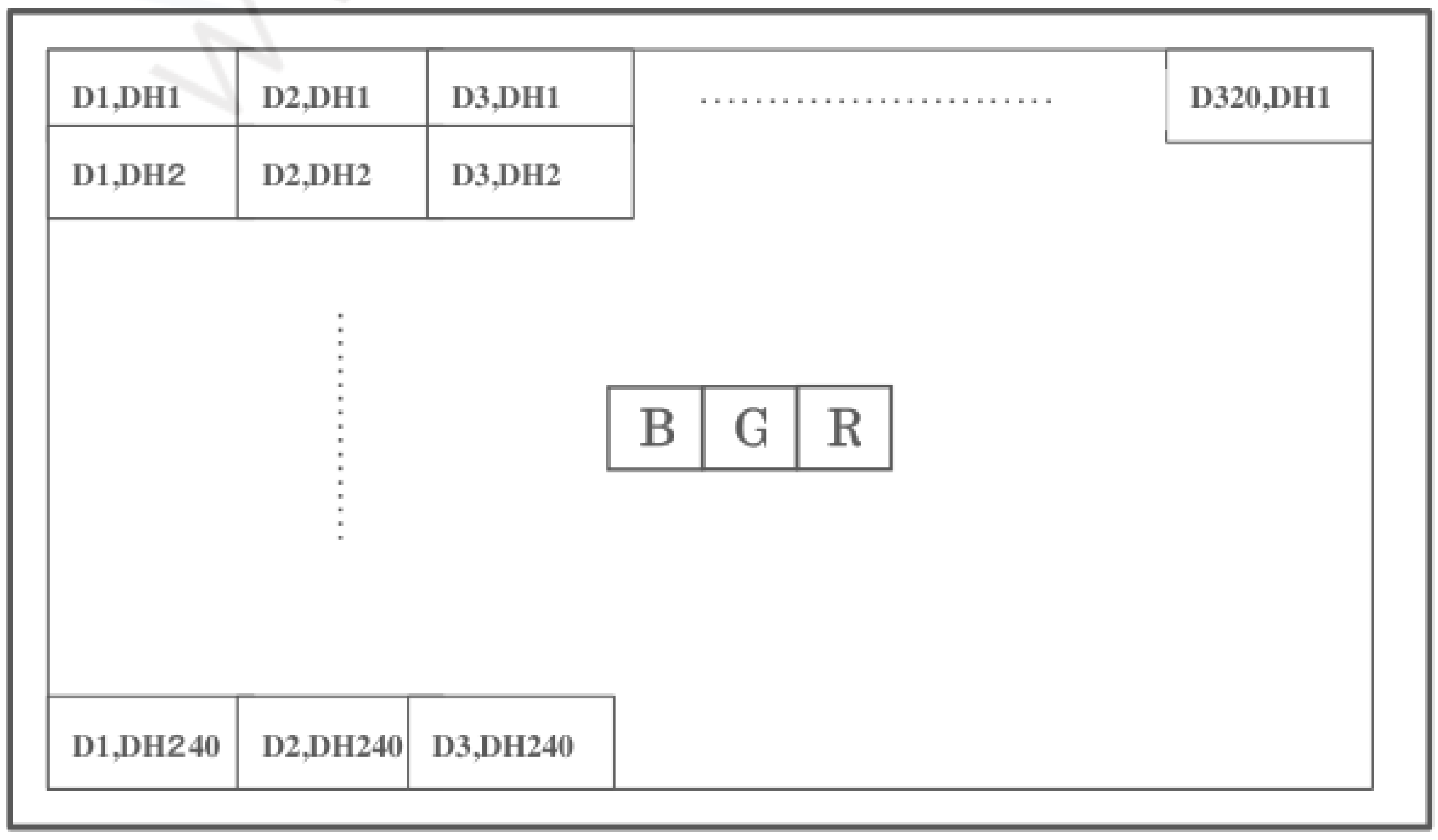
Item		Symbol	Min.	Typ.	Max.	Unit	Note
Clock	Frequency	1/Tc	–	6.3	7.0	MHz	
	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	12	–	–	ns	
	Hold time	Tdh	12	–	–	ns	
Horizontal sync. signal	Cycle	TH	50.0	63.6	–	μs	
			360	400	450	clock	
	Pulse width	THp	5	30	–	clock	
	Set up time	THs	12	–	–	ns	
	Hold time	THh	12	–	–	ns	
Vertical sync. signal	Cycle	TV	251	262	280	line	
	Pulse width	TVp	1	3	5	line	
	Set up time	TVs	12	–	–	ns	
	Hold time	TVh	12	–	–	ns	
Enable signal (ENAB)	Pulse width	TEp	320			clock	
	Set up time	TEs	12	–	–	ns	
	Hold time	TEh	12	–	–	ns	
HSYNC - Enable signal phase difference		THE	36	68	88	clock	
Vertical sync. signal start position		TVE	2	18	38	line	
Horizontal display period		THd	320			clock	
Vertical display period		TVd	240			line	

*When ENAB is fixed at "Low", the horizontal display starts from the data of C68 (clock) as shown in 8-3.

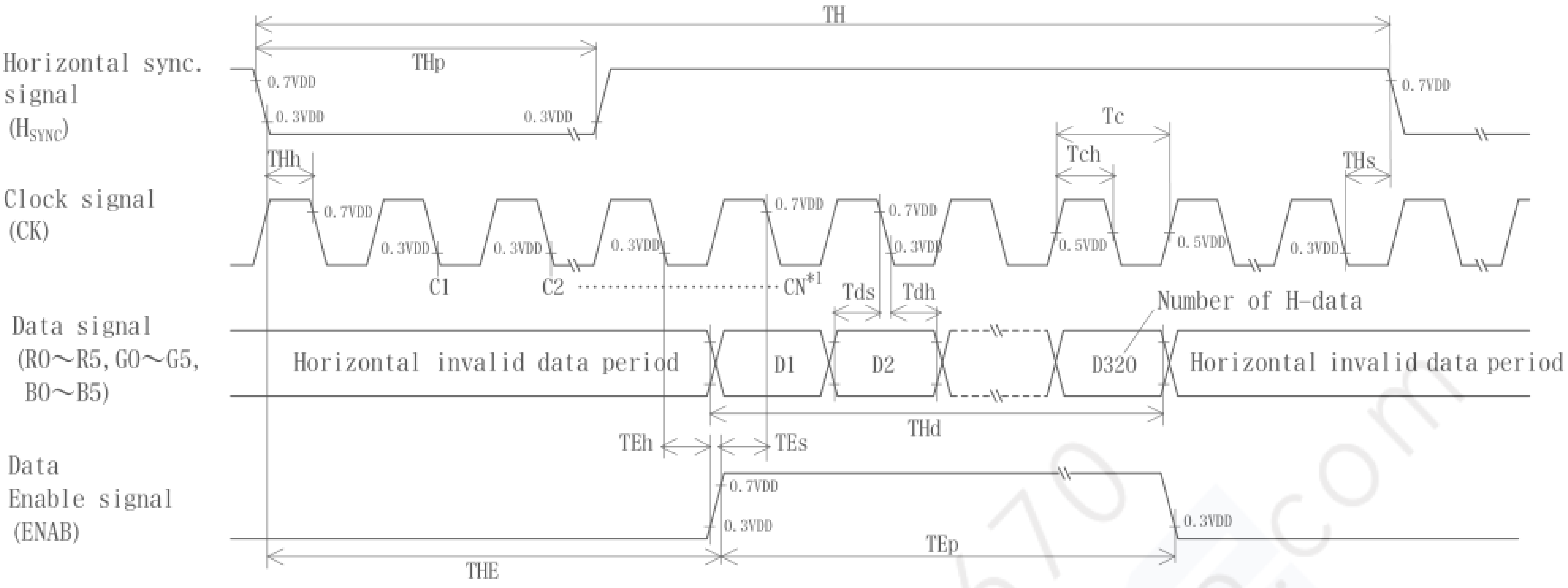
*When ENAB is fixed at "Low", the vertical sync. signal start position is 18 (line) as shown in 8-3.

*In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

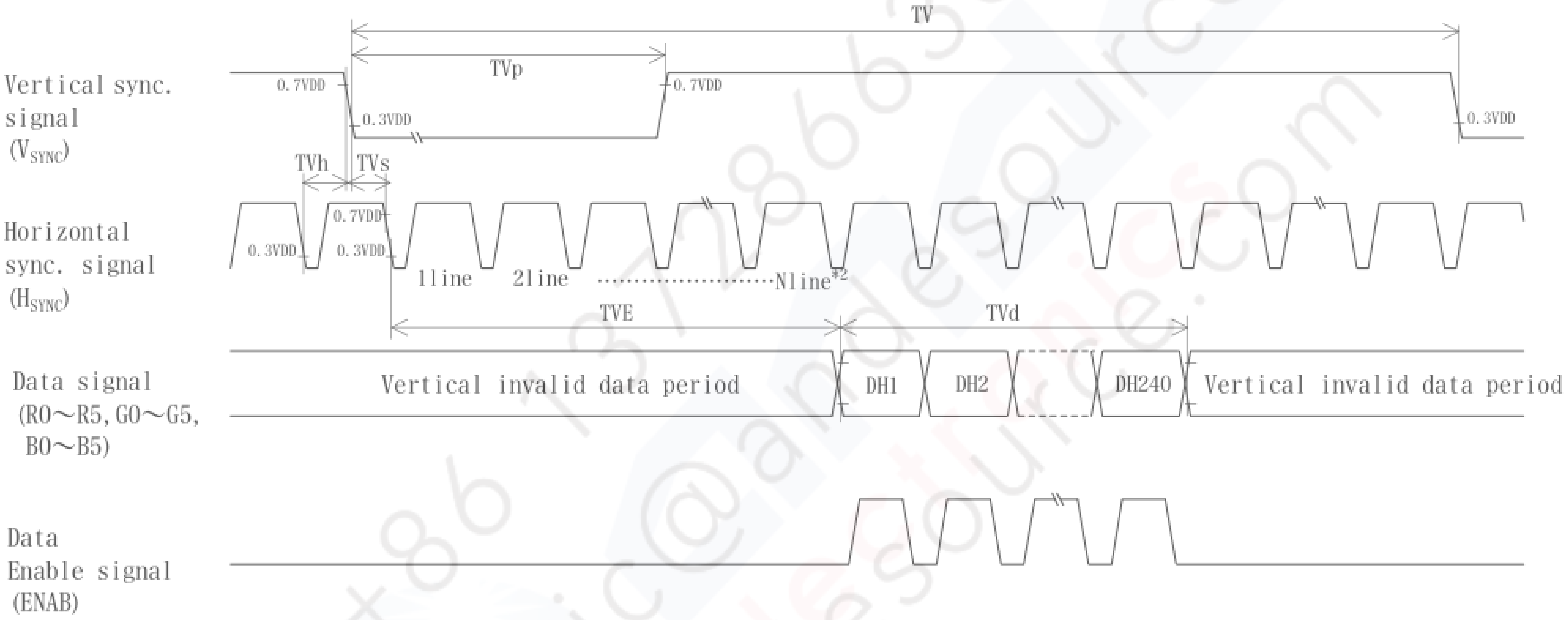
8-2. Input Data Signals and Display position on the screen



8-3. Input timing characteristics



TH
THp
Horizontal sync. signal (H_{SYNC})
0.7VDD
0.3VDD
Tc
Tch
THh
THs
Clock signal (CK)
0.5VDD
C1
C2
CN*1
Tds
Tdh
Number of H-data
Data signal (R0～R5, G0～G5, B0～B5)
Horizontal invalid data period
D1
D2
D320
Horizontal invalid data period
THd
TEh
TEs
Data Enable signal (ENAB)
TEp
THE



TV
TVp
Vertical sync. signal (V_{SYNC})
0.7VDD
0.3VDD
TVh
TVs
Horizontal sync. signal (H_{SYNC})
1line
2line
Nline*2
TVE
TVd
Data signal (R0～R5, G0～G5, B0～B5)
Vertical invalid data period
DH1
DH2
DH240
Vertical invalid data period
Data Enable signal (ENAB)

*1 When ENAB is fixed at "Low", the horizontal display starts from the data of C68 (clock).

*2 When ENAB is fixed at "Low", the vertical sync. signal start position is 18 (line).

9. LED backlight characteristics

LED ratings

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Forward current	*1	IF	-	15	-	mA	Ta=-20～70°C
Forward voltage	*1	VF	-	22.1	25.0	V	IF=15mA, Ta=-20℃
			-	21.7	24.5	V	IF=15mA, Ta=25℃
			-	21.3	24.1	V	IF=15mA, Ta=70℃
Operating life time	*2, *3	T	-	40,000	-	h	IF=15mA, Ta=25℃

*1 For each “AN1-CA1”, “AN2-CA2” and “AN3-CA3”

*2 When brightness decrease 50% of initial brightness.

*3 Life time is estimated data.(Condition : IF=15mA, Ta=25℃ in chamber).

* An input current below 5.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.

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10. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

TCG057QVLBA-G00 - □□ - □□ - □   MADE IN   □□□□□
↓↓ ↓ ↓ ↓
1 2 3 4 5

No1. - No5. above indicate
- 1. Year code
- 2. Month code
- 3. Date
- 4. Version Number
- 5. Country of origin (Japan or China)

Year	2008	2009	2010	2011	2012	2013
Code	8	9	0	1	2	3

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

11. Warranty

11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

11-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.

12. Precautions for use

12-1. Installation of the LCD

1) A transparent protection plate shall be added to protect the LCD and its polarizer.
2) The LCD shall be installed so that there is no pressure on the LSI chips.
3) The LCD shall be installed flat, without twisting or bending.
4) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

12-2. Static electricity

1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
2) Workers should use body grounding. Operator should wear ground straps.

12-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

12-4. Storage

1) The LCD shall be stored within the temperature and humidity limits specified.
   Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
2) Always store the LCD so that it is free from external pressure onto it.

12-5. Usage

1) DO NOT store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
6) Do not disassemble LCD module because it will result in damage.
7) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
9) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.

13. Reliability test data

Test item	Test condition	Test time	Judgement
High temp. atmosphere	80°C	240h	Display function : No defect Display quality : No defect Current consumption : No defect
Low temp. atmosphere	-30°C	240h	Display function : No defect Display quality : No defect Current consumption : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function : No defect Display quality : No defect Current consumption : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function : No defect Display quality : No defect Current consumption : No defect
High temp. operation	70°C	500h	Display function : No defect Display quality : No defect Current consumption : No defect

* Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

* The LCD is tested in circumstances in which there is no condensation.

* The reliability test is not an out-going inspection.

* The result of the reliability test is for your reference purpose only.
The reliability test is conducted only to examine the LCD's capability.

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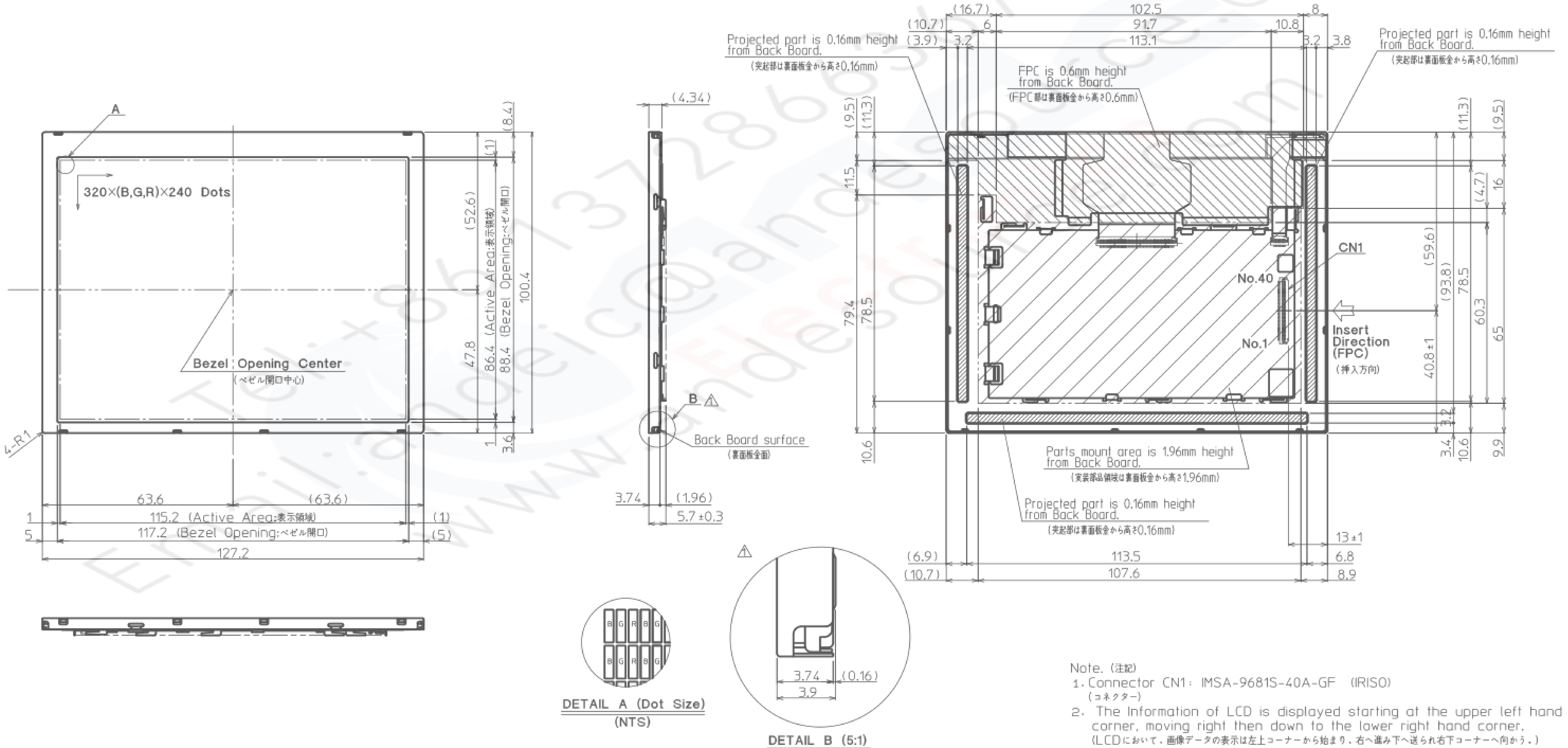
No	Description	Drawn	Checked	Checked	Approved
⚠	·Add DETAIL B.	'07.12.01 倉元	'07.12.02 鶴崎		'07.12.03 南



A
320×(B,G,R)×240 Dots
Bezel Opening Center
（ベゼル開口中心）
4-R1
63.6
(63.6)
1
115.2 (Active Area:表示領域)
(1)
5
117.2 (Bezel Opening:ベゼル開口)
(5)
127.2
(8.4)
(1)
(52.6)
47.8
86.4 (Active Area:表示領域)
88.4 (Bezel Opening:ベゼル開口)
100.4
1
3.6
(4.34)
B ⚠
Back Board surface
（裏面板全面）
3.74
(1.96)
5.7±0.3
B G R B G
DETAIL A (Dot Size)
(NTS)
⚠
3.74
(0.16)
3.9
DETAIL B (5:1)
(16.7)
102.5
8
(10.7)
6
91.7
10.8
(3.9)
3.2
113.1
3.2
3.8
Projected part is 0.16mm height from Back Board.
（突起部は裏面板金から高さ0.16mm）
FPC is 0.6mm height from Back Board.
（FPC部は裏面板金から高さ0.6mm）
Projected part is 0.16mm height from Back Board.
（突起部は裏面板金から高さ0.16mm）
(9.5)
(11.3)
11.5
79.4
78.5
10.6
CN1
No.40
No.1
Insert Direction (FPC)
（挿入方向）
(11.3)
(9.5)
(4.7)
16
(59.6)
(93.8)
78.5
60.3
65
40.8±1
3.2
3.4
10.6
9.9
Parts mount area is 1.96mm height from Back Board.
（実装部品領域は裏面板金から高さ1.96mm）
Projected part is 0.16mm height from Back Board.
（突起部は裏面板金から高さ0.16mm）
13±1
(6.9)
113.5
6.8
(10.7)
107.6
8.9

Note.（注記）

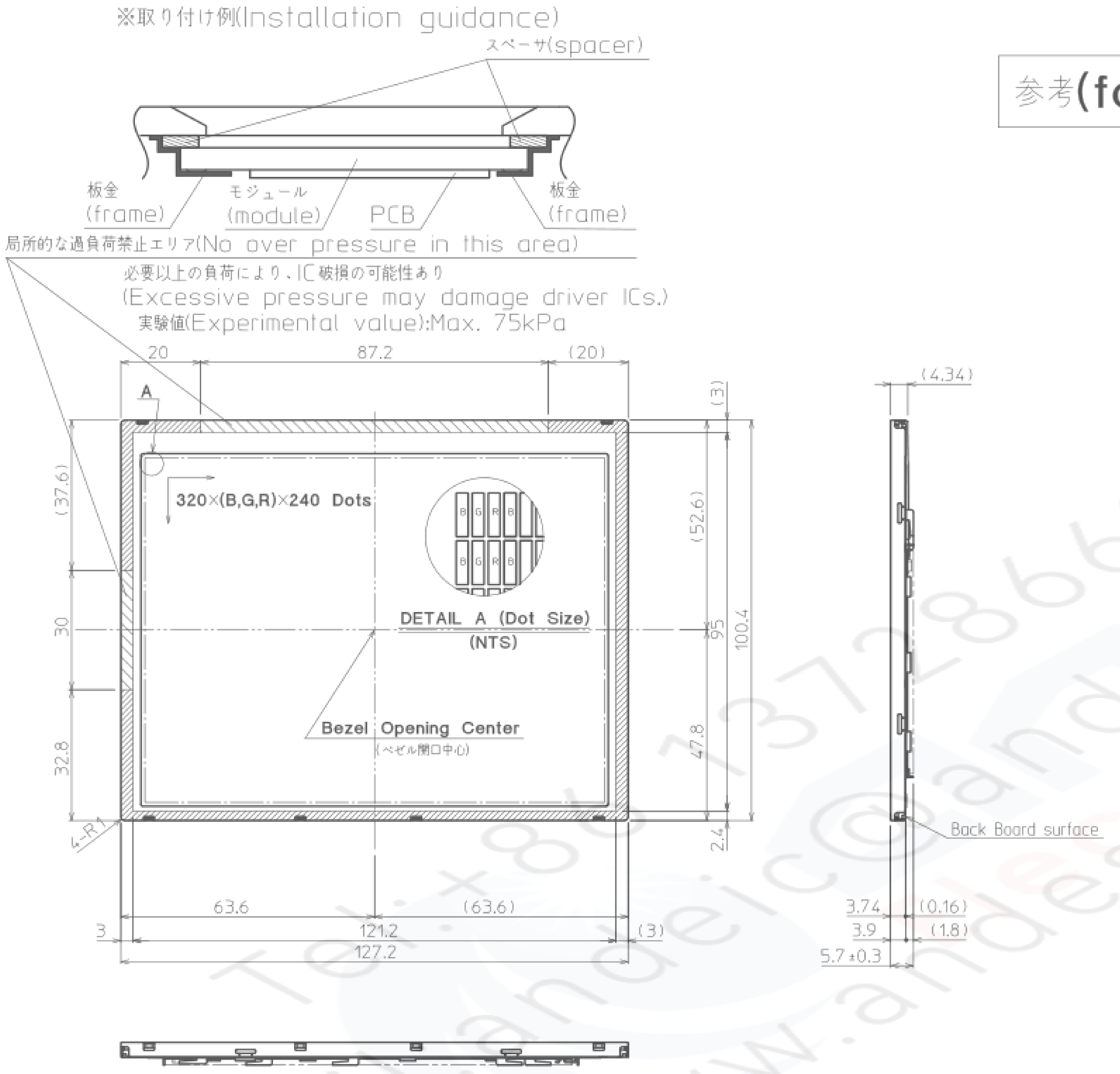
1. Connector CN1: IMSA-9681S-40A-GF (IRISO)
   （コネクター）
2. The Information of LCD is displayed starting at the upper left hand corner, moving right then down to the lower right hand corner.
   (LCDにおいて，画像データの表示は左上コーナーから始まり，右へ進み下へ送られ右下コーナーへ向かう。)
3. Tolerance without indication: ±0.5
   （指示無き公差）

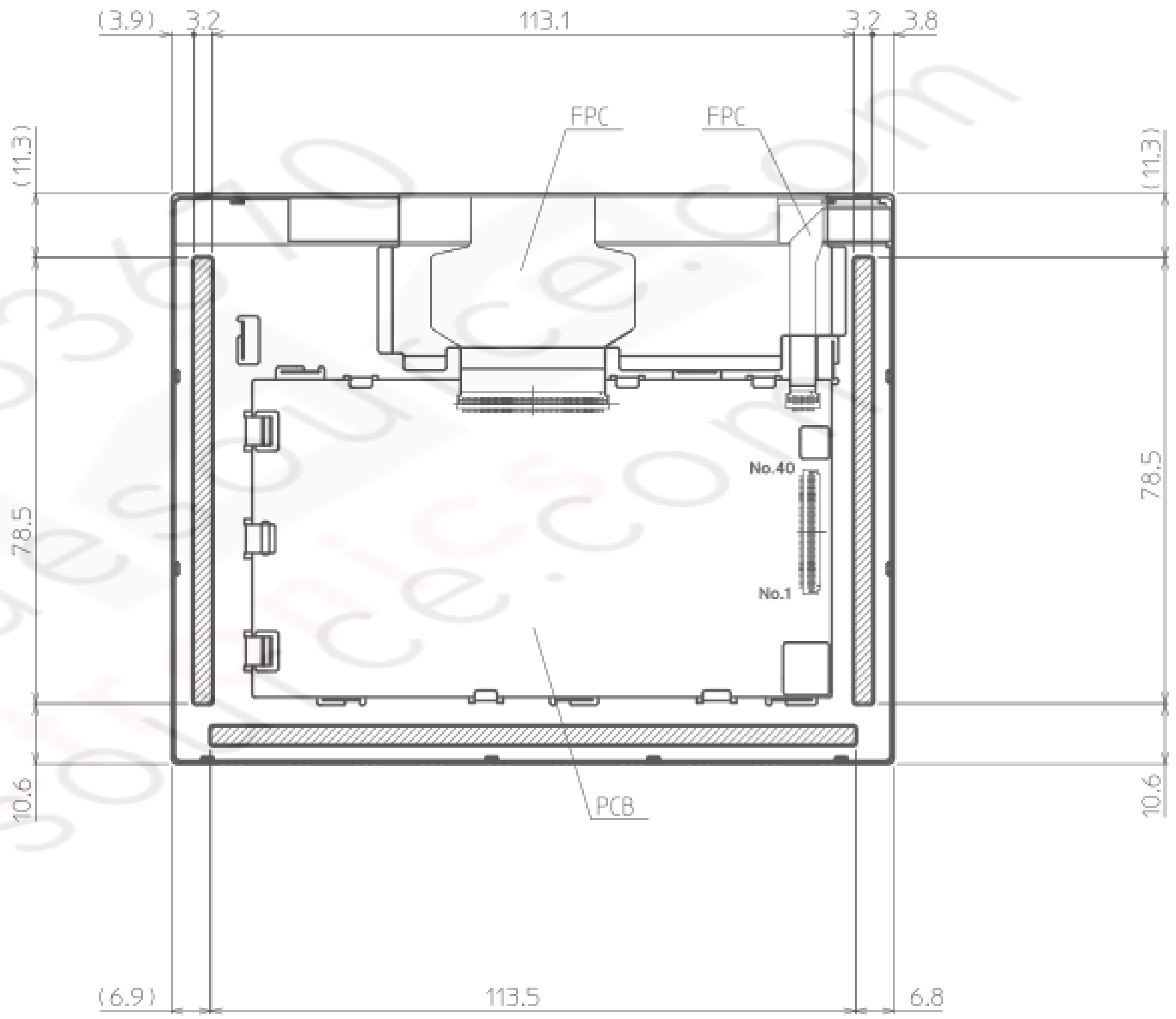
Material 材質	Treatment 処理	Approved	Checked	Checked	Drawn	Scale	Title		KYOCERA	Year-Month-Day	Size
		'07.09.04	'07.09.04	'07.09.04		1:1(5:1,NTS)	TCG057QVLBA			'07.09.02	2
Quantity 製作数	Description; 備考 RoHS	南	阿部	茶圓	倉元		Outline Dimensions		Drawing No. 121A5062800-1		

Tel:+8613728665170
Email:andeic@ande[illegible].com
www.ande[illegible].com

No	Description	Drawn	Checked	Checked	Approved

参考(for Reference)


※取り付け例(Installation guidance)
スペーサ(spacer)
板金 (frame)
モジュール (module)
PCB
板金 (frame)
局所的な過負荷禁止エリア(No over pressure in this area)
必要以上の負荷により、IC破損の可能性あり
(Excessive pressure may damage driver ICs.)
実験値(Experimental value):Max. 75kPa
20
87.2
(20)
A
320×(B,G,R)×240 Dots
DETAIL A (Dot Size)
(NTS)
Bezel Opening Center
(ベゼル開口中心)
(37.6)
30
32.8
4-R1
(3)
(52.6)
95
100.4
47.8
2.4
63.6
(63.6)
3
121.2
(3)
127.2
(4.34)
Back Board surface
3.74
(0.16)
3.9
(1.8)
5.7±0.3


(3.9)
3.2
113.1
3.2
3.8
FPC
FPC
(11.3)
78.5
10.6
No.40
No.1
PCB
(11.3)
78.5
10.6
(6.9)
113.5
6.8

取り付け方法
(Installation of the LCD)

ケースにモジュールをはめ込み(x,y固定)、裏面からZ方向を固定する事が可能。
(The LCD module shall be held in the X/Y direction by the housing, and in the Z direction using a backboard.)

斜線部の領域にて、下記の条件を満たし押さえることが望ましい。
(To hold the LCD module in place, it shall be supported with pressure applied to the hatched areas indicated by the descriptions below.)

▨ 押さえつけ可能領域
(Pressure may be applied in this area.)

▧ 押さえつけ可能領域。但し、局所的な過負荷禁止。
(No "point" over pressure is allowed.)

表面取り付け条件
(Installation conditions (Front side))

1.上辺長辺側、左右短辺中央部に局所的な負荷がかからないように取り付のこと
(1. The LCD shall be installed so that there is no point pressure applied in the middle of the left, right, and upper border around the viewing area.)

2.広い範囲で均一に押さえることが望ましい
(2. The LCD shall be uniformly supported over as wide an area as possible.)

注記(Note)
指定外公差(Torelance without indication) :±0.5

裏面取り付け条件
(Installation conditions (Back side))

1.PCBへの接触無きこと
(1. Do not allow any foreign material to contact the PCB.)

2.FPCエリア押さえ不可
(2. Do not use any part of the FPC area to hold the LCD module in place)

3.PCB周りの板金突起部押さえ不可
(3. Do not apply pressure on the projected metal part of the PCB.)

4.モジュールがたわむこと無きよう押さえつけのこと
(4. Do not allow the LCD module to bend or twist.)

5.広い範囲で均一に押さえることが望ましい
(5. Support the LCD with uniform pressure over as wide an area as possible.)

Material 材 質	Treatment 処 理	Approved '07.12.14	Checked	Checked '07.12.14	Drawn	Scale 1:1(NTS)	Title TCG057QVLBA		KYOCERA	Year-Month-Day '07.12.12	Size 2
Quantity 製作数	Description; 備 考 RoHS	朝倉		鶴崎	倉元		モジュール取り付け方法		Drawing No. 121A5065700		

Spec No.	TQ3C-8EAF0-E2DDE78-00
Date	September 8, 2007

KYOCERA INSPECTION STANDARD

TYPE : TCG057QVLBA-G00

Tel:+86 13728663670
Electronics

KYOCERA CORPORATION
KAGOSHIMA HAYATO PLANT
LCD DIVISION

Original Issue Date	Designed by : Engineering dept.			Confirmed by : QA dept.	
	Prepared	Checked	Approved	Checked	Approved
September 8, 2007	H. Tokumori	Y. Yamazaki	Y. Matsumoto	J. Sakaguchi	

Revision record

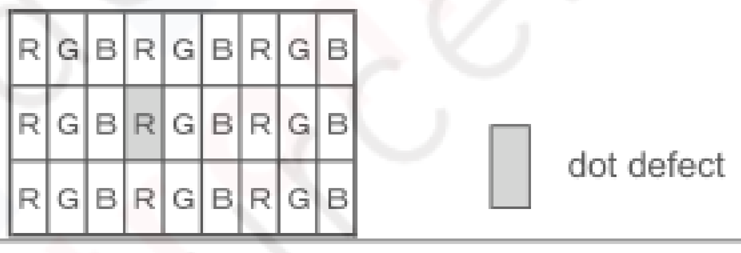
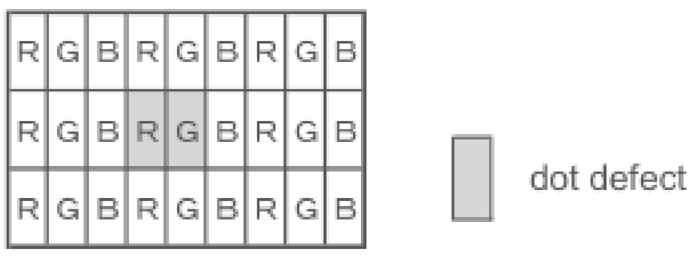
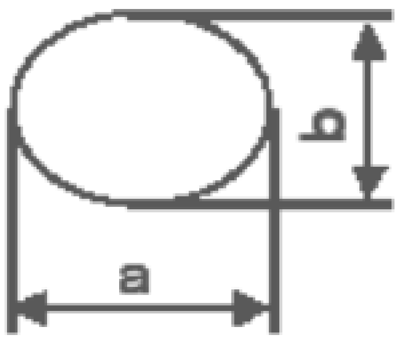
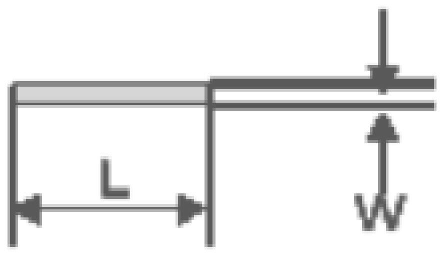
Date	Designed by : Engineering dept.			Confirmed by : QA dept.	
	Prepared	Checked	Approved	Checked	Approved

Rev.No.	Date	Page	Descriptions

Tel:+86 13728663670
Electronics

Visuals specification

1) Note

	Note		
General	1. Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent. 2. This inspection standard about the image quality shall be applied to any defect within the active area and shall not be applicable to outside of the area. 3. Inspection conditions Luminance : 500 Lux min. Inspection distance : 300 mm. Temperature : 25 ± 5°C Direction : Directly above		
Definition of inspection item	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen. Inspection tool: 5% Transparency neutral density filter. Count dot: If the dot is visible through the filter Don't count dot: If the dot is not visible through the filter  R G B R G B R G B R G B R G B R G B R G B R G B R G B dot defect
		Black dot defect	The dot is constantly "off" when power applied to the LCD, even when all "White" data sent to the screen.
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot defects or black dot defects.  R G B R G B R G B R G B R G B R G B R G B R G B R G B dot defect
	External inspection	Bubble, Scratch, Foreign particle (Polarizer, Cell, Backlight)	Visible operating (all pixels "Black" or "White") and non operating.
		Appearance inspection	Does not satisfy the value at the spec.
	Others	LED wire	Damaged to the LED wire, connector, pin, functional failure or appearance failure.
	Definition of size	Definition of circle size  d =(a + b)/2	Definition of linear size 

Email:andeic@andesource.com www.andesource.com

2) Standard

<table>
<tr><th colspan="2">Classification</th><th colspan="2">Inspection item</th><th>Judgement standard</th></tr>
<tr><td rowspan="7">Defect (in LCD glass)</td><td rowspan="6">Dot defect</td><td colspan="2">Bright dot defect</td><td>Acceptable number : 4
Bright dot spacing : 5 mm or more</td></tr>
<tr><td colspan="2">Black dot defect</td><td>Acceptable number : 5
Bright dot spacing : 5 mm or more</td></tr>
<tr><td rowspan="2">2 dot join</td><td>Bright dot defect</td><td>Acceptable number : 2</td></tr>
<tr><td>Black dot defect</td><td>Acceptable number : 3</td></tr>
<tr><td colspan="2">3 or more dots join</td><td>Acceptable number : 0</td></tr>
<tr><td colspan="2">Total dot defects</td><td>Acceptable number : 5 Max</td></tr>
<tr><td>Others</td><td colspan="2">White dot, Dark dot (Circle)</td><td>
<table>
<tr><th>Size (mm)</th><th>Acceptable number</th></tr>
<tr><td>d ≦ 0.2</td><td>(Neglected)</td></tr>
<tr><td>0.2 < d ≦ 0.4</td><td>5</td></tr>
<tr><td>0.4 < d ≦ 0.5</td><td>3</td></tr>
<tr><td>0.5 < d</td><td>0</td></tr>
</table>
</td></tr>
<tr><td colspan="2" rowspan="4">External inspection (Defect on Polarizer or between Polarizer and LCD glass)</td><td colspan="2">Polarizer (Scratch)</td><td>
<table>
<tr><th>Width (mm)</th><th>Length (mm)</th><th>Acceptable number</th></tr>
<tr><td>W ≦ 0.1</td><td>—</td><td>(Neglected)</td></tr>
<tr><td rowspan="2">0.1 < W ≦ 0.3</td><td>L ≦ 5.0</td><td>(Neglected)</td></tr>
<tr><td>5.0 < L</td><td>0</td></tr>
<tr><td>0.3 < W</td><td>—</td><td>0</td></tr>
</table>
</td></tr>
<tr><td colspan="2">Polarizer (Bubble)</td><td>
<table>
<tr><th>Size (mm)</th><th>Acceptable number</th></tr>
<tr><td>d ≦ 0.2</td><td>(Neglected)</td></tr>
<tr><td>0.2 < d ≦ 0.3</td><td>5</td></tr>
<tr><td>0.3 < d ≦ 0.5</td><td>3</td></tr>
<tr><td>0.5 < d</td><td>0</td></tr>
</table>
</td></tr>
<tr><td colspan="2">Foreign particle (Circle shape)</td><td>
<table>
<tr><th>Size (mm)</th><th>Acceptable number</th></tr>
<tr><td>d ≦ 0.2</td><td>(Neglected)</td></tr>
<tr><td>0.2 < d ≦ 0.4</td><td>5</td></tr>
<tr><td>0.4 < d ≦ 0.5</td><td>3</td></tr>
<tr><td>0.5 < d</td><td>0</td></tr>
</table>
</td></tr>
<tr><td colspan="2">Foreign particle (Linear shape)
Scratch</td><td>
<table>
<tr><th>Width (mm)</th><th>Length (mm)</th><th>Acceptable number</th></tr>
<tr><td>W ≦ 0.03</td><td>—</td><td>(Neglected)</td></tr>
<tr><td rowspan="3">0.03 < W ≦ 0.1</td><td>L ≦ 2.0</td><td>(Neglected)</td></tr>
<tr><td>2.0 < L ≦ 4.0</td><td>3</td></tr>
<tr><td>4.0 < L</td><td>0</td></tr>
<tr><td>0.1 < W</td><td>—</td><td>(According to circular shape)</td></tr>
</table>
</td></tr>
</table>