

TENTATIVE

Kaohsiung Opto-Electronics Inc.

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TECHNICAL DATA

TX13D200VM5BAA

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ACCEPTED BY: _____ PROPOSED BY: ____

2. RECORD OF REVISION

DATE	SHEET No.	SUMMARY

3. GENERAL DATA

3.1 DISPLAY FEATURES

This module is a 5" VGA of 4:3 format of amorphous silicon TFT. The pixel format is vertical stripe and sub pixels are arranged as R(red), G(green), B(blue) sequentially .This display is RoHS compliant , and COG (chip on glass) technology and LED backlight are applied on this display.

Part Name	TX13D200VM5BAA
Module Dimensions	119.4(W)mm x 89.1(H)mm x 9.3(D)mm typ
LCD Active Area	101.76(W)mm x 76.32(H)mm
Pixel Pitch	0.053(W)mm x 3(R,G,B)(W) x 0.159(H)mm
Resolution	640x3(R,G,B))(W)x480(H) dots
Color Pixel Arrangement	R, G, B Vertical stripe
LCD Type	Transmissive Color TFT; Normally White
Display Type	Active Matrix
Top Polarizer Type	Anti-glare Polarizer Film
Number of Colors	262k Colors (R,G,B 6bit digital each)
Backlight	Light Emitting Diode (LED)
Weight	110 g (typ.)
Interface	40pin C-MOS
Power Supply Voltage	3.3V for LCD driving 12 V for B / L driving
Power Consumption	0.5 W for LCD; 3 W for B/L
Viewing Direction	Super Wide Version

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4. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Remarks
Supply Voltage	V _{DD}	-0.3	5.0	٧	-
Input Voltage of Logic	Vı	-0.3	V _{DD} +0.3	V	Note 1
Operating Temperature	T _{op}	-30	80	°C	Note 2
Storage Temperature	T _{st}	-30	80	°C	Note 2
Backlight Input Voltage	V _{LED}	-	15	V	-

- Note 1: The rating is defined for the signal voltages of the interface such as DCLK, DE, and RGB data bus.
- Note 2: The maximum rating is defined as above based on the chamber temperature, which might be different from ambient temperature after assembling the panel into the application. Moreover, some temperature-related phenomenon as below needed to be noticed:
 - Background color, contrast and response time would be different in temperatures other than 25°C.
 - Operating under high temperature will shorten LED lifetime.

5. ELECTRICAL CHARACTERISTICS

5.1 LCD CHARACTERISTICS

 $T_a = 25 \, ^{\circ}C, \text{ Vss} = 0V$

Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	-	3.0	3.3	3.6	V	-
Input Voltage of Logic	.,	"H" level	0.7V _{DD}	-	V _{DD} +0.3	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	Niete 4
Input Voltage of Logic	Vı	"L" level	-0.3	-	0.25V _{DD}	V	Note 1
Power Supply Current	I _{DD}	V _{DD} =3.3V	- 135 160 mA		mA	Note 2,3	
Vsync Frequency	f_{v}	-	50	60	70	Hz	-
Hsync Frequency	Hsync Frequency f_{II}		26.25	31.50	36.75	KHz	
DCLK Frequency	$f_{\scriptscriptstyle CLK}$	_	21.0	25.2	29.4	MHz	

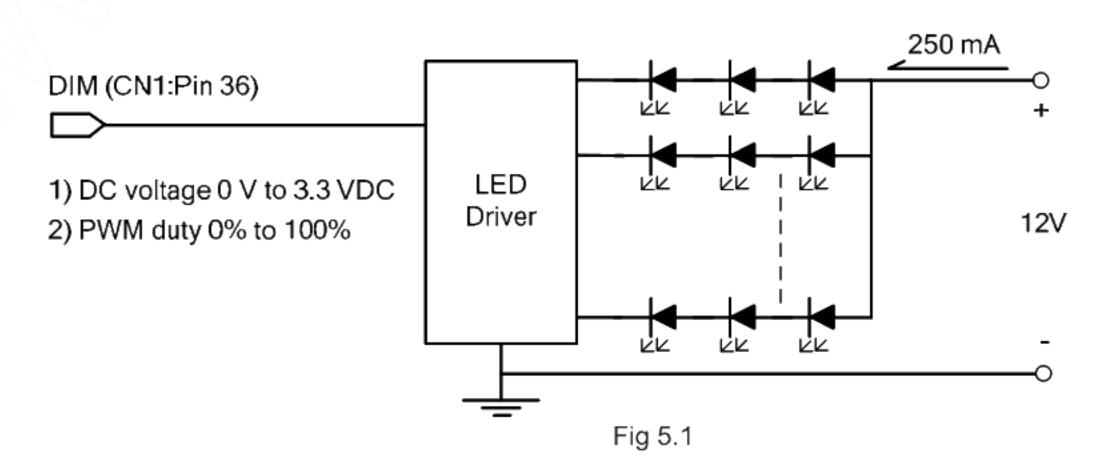
- Note1: The rating is defined for the signal voltages of the interface such as DE, DCLK and RGB data bus.
- Note 2: An all black check pattern is used when measuring I_{DD} . f_v is set to 60 Hz.
- Note 3: 1.0A fuse is applied in the module for IDD. For display activation and protection purpose, power supply is recommended larger than 2.5A to start the display and break fuse once any short circuit occurred.

5.2 BACKLIGHT CHARACTERISTICS

 $T_a = 25 \, ^{\circ}C$

Item	Symbol	Symbol Condition		Тур.	Max.	Unit	Remarks
LED Input Voltage	V _{LED}	- (2)	11.5	12.0	12.5	V	Note1
LED Forward Current (Dim Control)		0V; 0% duty	220	250	280	_	
	I _{LED}	3.3VDC; 100% duty	-	30	36	mA	Note 2
LED lifetime	-	250 mA	-	70K	-	hrs	Note 3

- Note 1: As Fig. 5.1 shown, LED current is constant, 250 mA, controlled by the LED driver when applying 12V V_{LED}.
- Note 2: Dimming function can be obtained by applying DC voltage or PWM signal from the display interface CN1. The recommended PWM signal is 1kHz ~ 10kHz with 3.3V amplitude.
- Note 3: The estimated lifetime is specified as the time to reduce 50% brightness by applying 250 mA at 25°C.



6. OPTICAL CHARACTERISTICS

The optical characteristics are measured based on the conditions as below:

- Supplying the signals and voltages defined in the section of electrical characteristics.
- The backlight unit needs to be turned on for 30 minutes.
- The ambient temperature is 25°C.
- In the dark room around 500~1000 lx, the equipment has been set for the measurements as shown in Fig 6.1.

 $T_a = 25 \, ^{\circ}C, f_v = 60 \, \text{Hz}, \text{Vdd} = 3.3 \text{V}$

Item		Symbol	Condition	Min.	Typ	Max.	Unit /	Remarks
		Symbol	Condition		Тур.	IVIAX.		
Brightness of	f White	-	$\phi = 0^{\circ}, \theta = 0^{\circ},$	500	600	-	cd/m ²	Note 1
Brightness U	niformity	-	$V = 0$, $V = 0$, $I_{LED} = 250 \text{mA}$	70	-	-	%	Note 2
Contrast F	Ratio	CR	ILED- 250MA	200	370	-		Note 3
Response (Rising + Fa		$T_r + T_f$	$\phi = 0^{\circ}, \theta = 0^{\circ}$	-	-	45	ms	Note 4
		θ x	φ = 0°, CR ≥ 10	-	80	1))	
\ Carries a A	Viowing Anglo		φ = 180°, CR ≥ 10	-	80	-	Danna	Note 5
Viewing Angle		θ y	φ = 90°, CR ≥ 10	-	80	<u>-</u>	Degree	Note 5
		θ y'	φ = 270°, CR ≥ 10	- /	80	-		
	Dod	Х		0.58	0.63	0.68		
	Red	Υ		0.27	0.32	0.37		
	0	Х		0.29	0.34	0.39		
Color	Green	Υ		0.51	0.56	0.61		
Chromaticity	Dhia	Х	$\phi = 0^\circ, \theta = 0^\circ$	0.10	0.15	0.20	-	Note 6
	Blue	Y		0.05	0.10	0.15		
	\A/bita	X		0.25	0.30	0.35		
	White	Υ		0.27	0.32	0.37		

Note 1: The brightness is measured from the panel center point, P5 in Fig. 6.2, for the typical value.

Note 2: The brightness uniformity is calculated by the equation as below:

$$Brightness\ uniformity = \frac{Min.\ Brightness}{Max.\ Brightness} \times 100\%$$

, which is based on the brightness values of the 9 points measured by BM-5 as shown in Fig. 6.2.

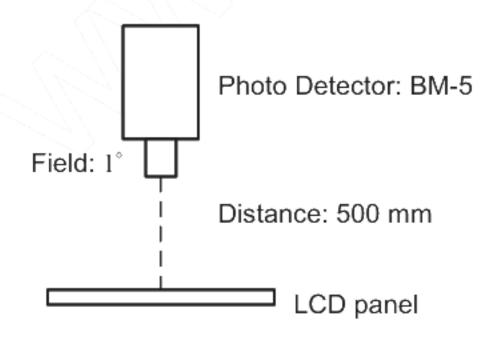


Fig 6.1

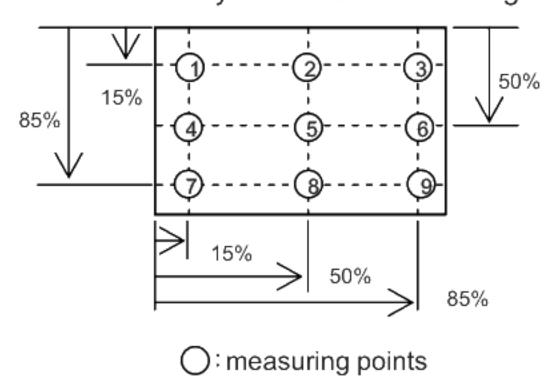
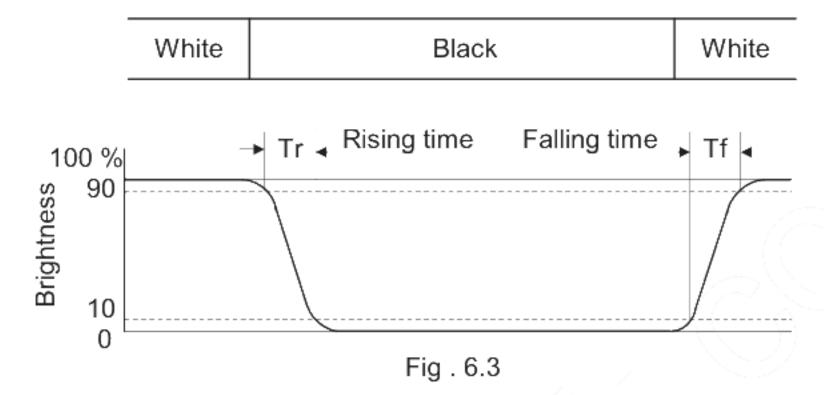


Fig 6.2

Note 3: The Contrast Ratio is measured from the center point of the panel, P5, and defined as the following equation:

$$CR = \frac{Brightness of White}{Brightness of Black}$$

Note 4: The definition of response time is shown in Fig. 6.3. The rising time is the period from 90% brightness to 10% brightness when the data is from black to white. Oppositely, Falling time is the period from 10% brightness rising to 90% brightness.



Note 5: The definition of viewing angle is shown in Fig. 6.4. Angle ϕ is used to represent viewing directions, for instance, $\phi = 270^{\circ}$ means 6 o'clock, and $\phi = 0^{\circ}$ means 3 o'clock. Moreover, angle θ is used to represent viewing angles from axis Z toward plane XY.

The display is super wide viewing angle version, so that the best optical performance can be obtained from every viewing direction.

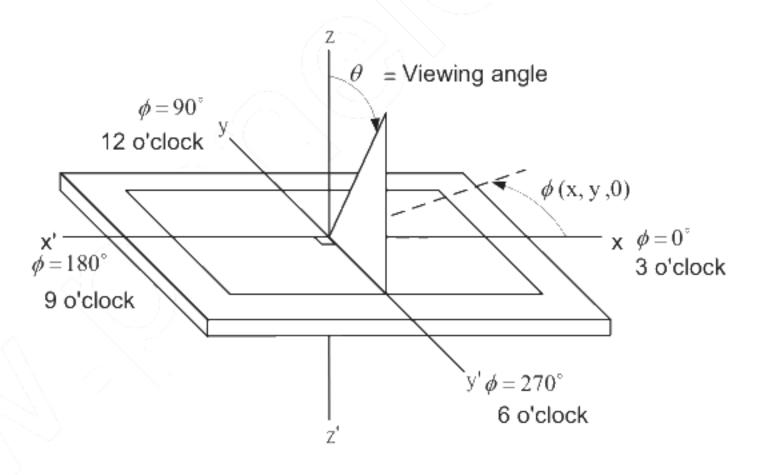
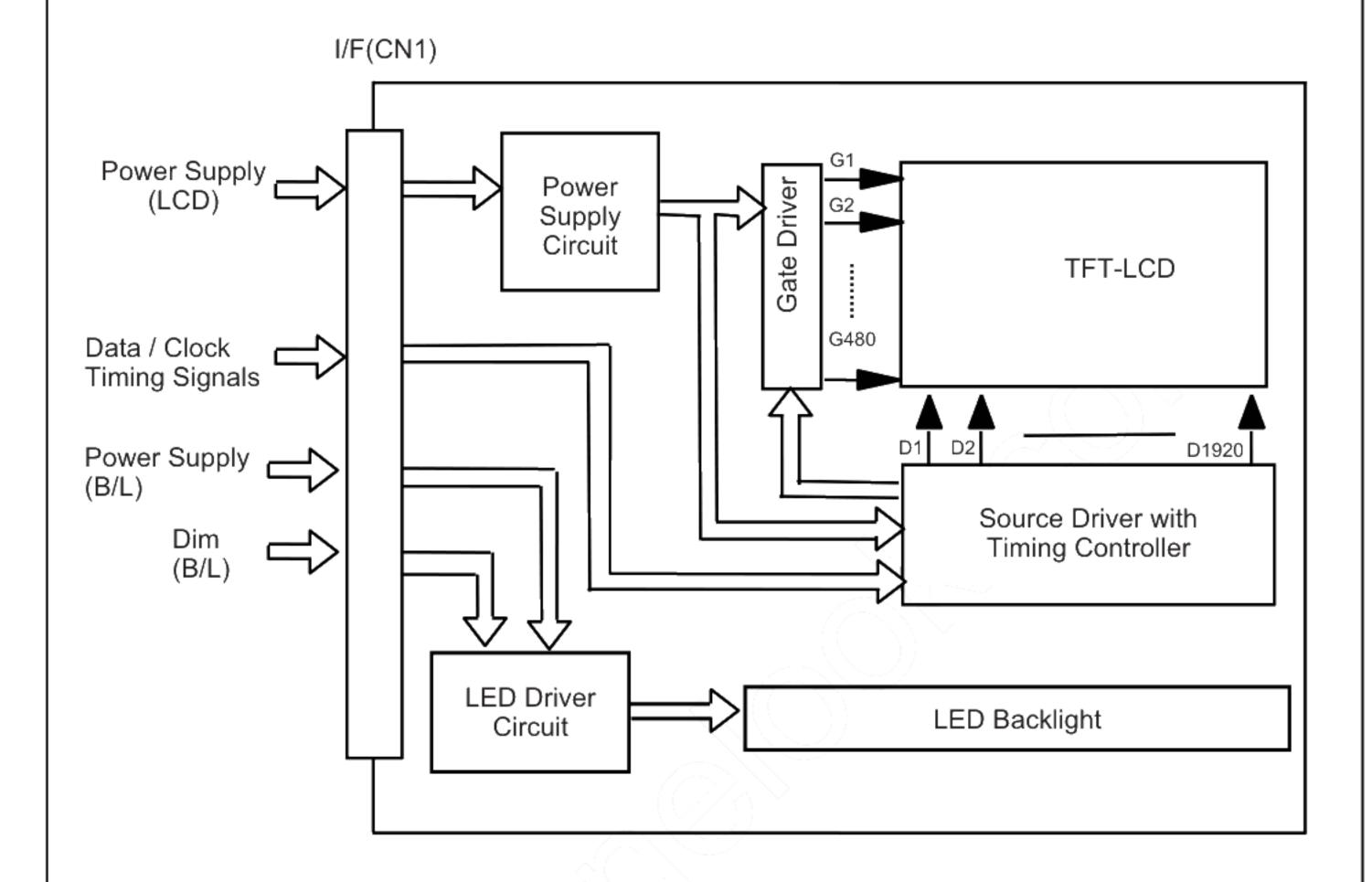


Fig 6.4

Note 6: The color chromaticity is measured from the center point of the panel, P5, as shown in Fig. 6.2.

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7. BLOCK DIAGRAM



Note 1: Signals are DCLK, DE, and RGB data bus.

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7B64LTD-2421-1

8. LCD INTERFACE

8.1 INTERFACE PIN CONNECTIONS

The display interface connector (CN1) is FA5S040HP1R3000 (JAE), and Pin assignment is as below:

Pin No.	Symbol	Signal	Pin No.	Symbol	Signal
1	V	Cumply Voltage	21	G4	Green data
2	V_{DD}	Supply Voltage	22	G3	Green data
3	L/R	Horizontal Display mode Control	23	V _{SS}	Ground
4	U/D	Vertical Display mode Control	24	G2	Green data
5	NC	No Connection	25	G1	Green data
6	DE	Data Enable	26	G0	Green data (LSB)
7	V _{SS}	Ground	27	V _{SS}	Ground
8	DCLK	Dot clock	28	R5	Red data (MSB)
9	V _{SS}	Ground	29	R4	Red data
10	NC	No Connection	30	R3	Red data
11	V _{SS}	Ground	31	V _{SS}	Ground
12	B5	Blue data (MSB)	32	R2	Red data
13	B4	Blue data	33	R1	Red data
14	В3	Blue data	34	R0	Red data (LSB)
15	V _{SS}	Ground	35	NC	No Connection
16	B2	Blue data	36	DIM	Note 1
17	B1	Blue data	37		
18	В0	Blue data (LSB)	38		Davis Cumple for D/I
19	V _{SS}	Ground	39	V _{LED}	Power Supply for B/L
20	G5	Green data (MSB)	40		

Note 1: Normal brightness: 0V or 0% PWM duty; Brightness control: 0V to 3.3V DC or 0% to 100% PWM duty.

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8.2 TIMING CHART

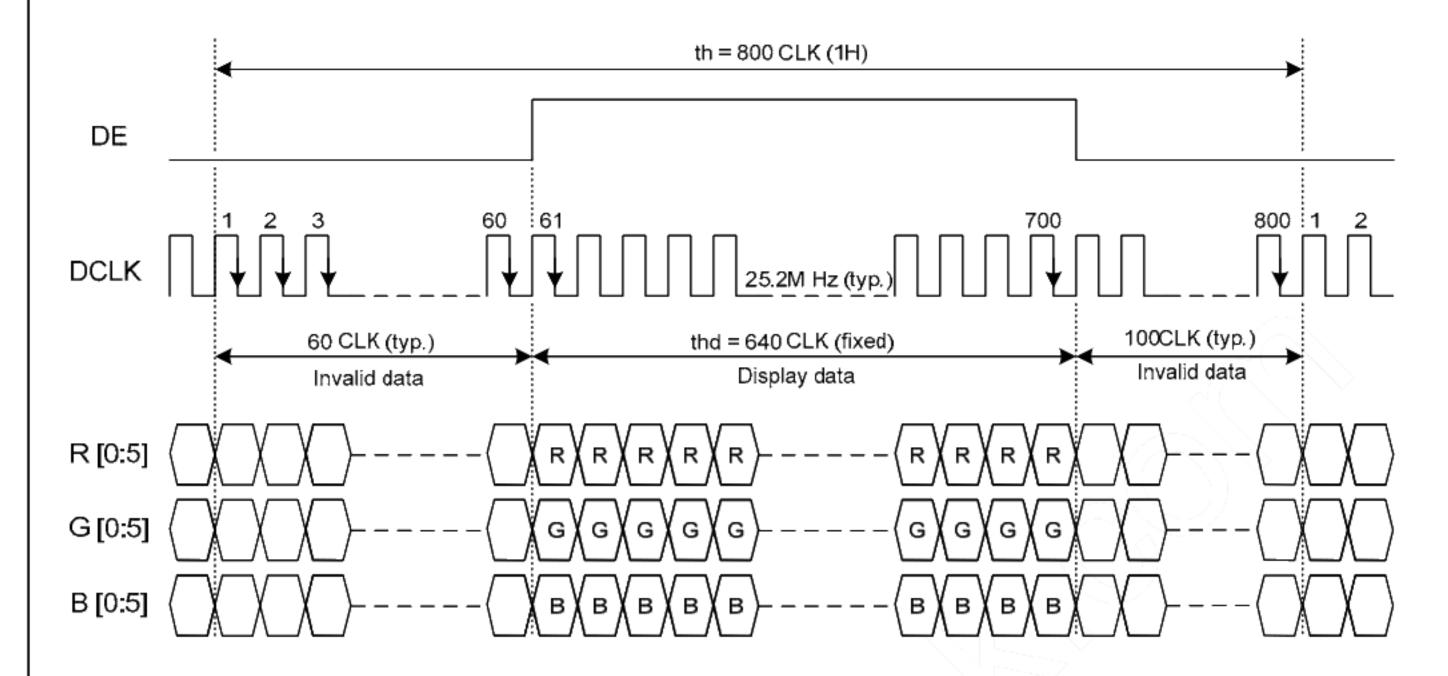


Fig. 8.1 Horizontal Timing

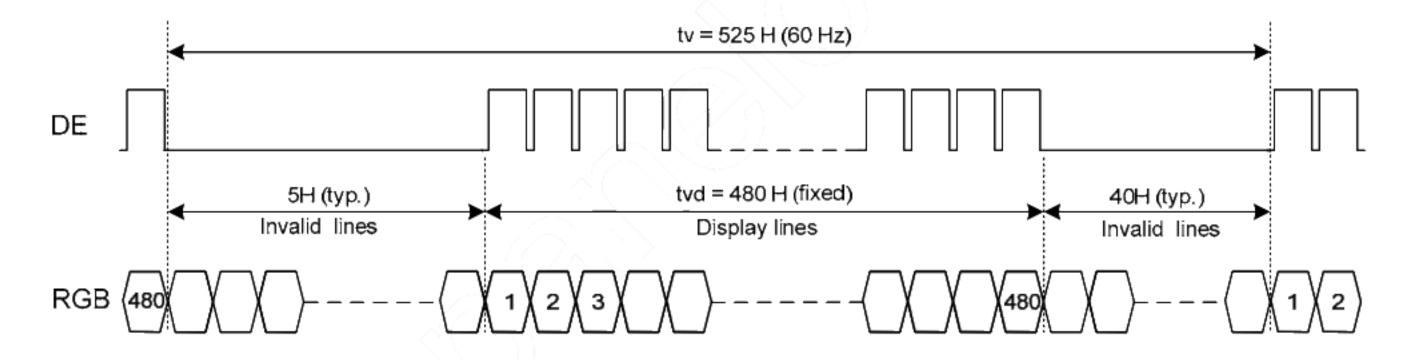


Fig. 8.2 Vertical Timing

8.3 CLOCK AND DATA INPUT TIMING

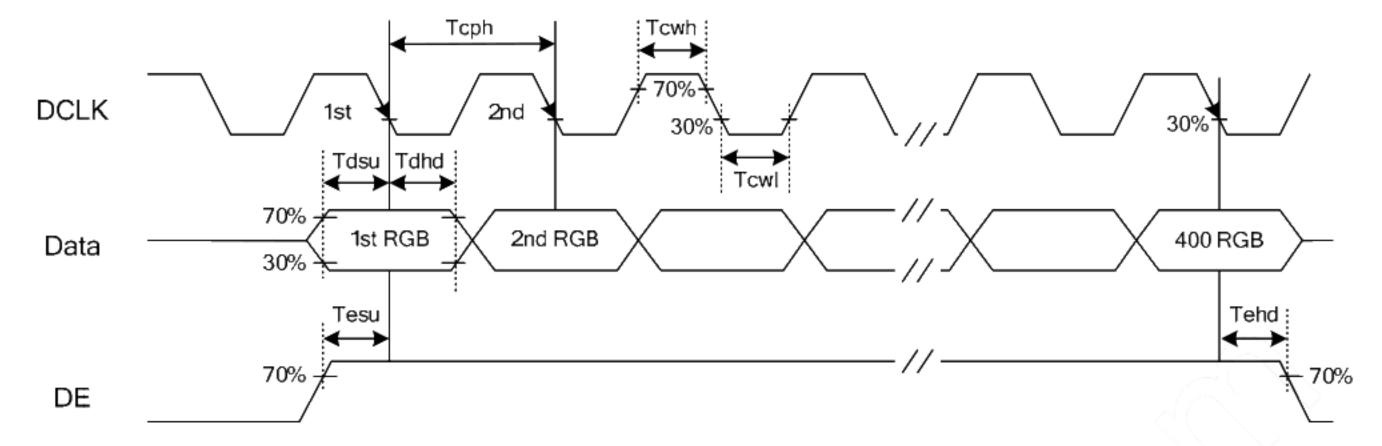


Fig. 8.3 Setup & Hold Time of Data and DE signal.

8.4 TIME TABLE

The column of timing sets including minimum, typical, and maximum as below are based on the best optical performance, frame frequency (Vsync) = 60 Hz to define. If 60 Hz is not the aim to set, 50~70 Hz for Vsync is recommended to apply for better performance by other parameter combination as the definitions in section 5.1.

FOR TIMING CHART

	Item		Min.	Тур.	Max.	Unit
Horizontal	CLK Frequency	fclk	21.0	25.2	29.7	M Hz
	Display Data	thd	640	640	640	CLK
	Cycle Time	th	700	800	900	CLK
Vention	Display Data	tvd	480	480	480	11
Vertical	Cycle Time	tv	500	525	550	> H

FOR CLOCK AND DATA INPUT TIMING

	Item	Symbol	Min.	Тур.	Max.	Unit
DOLK	Duty	Tcwh	45	50	55	%
DCLK	Cycle Time	Tcph	34	40	-	
Dete	Setup Time	Tdsu	12	-	-	
Data	Hold Time	Tdhd	12	-	-	ns
DE	Setup Time	Tesu	12	-	-	
DE	Hold Time	Tehd	12	-	-	

8.5 POWER SEQUENCE

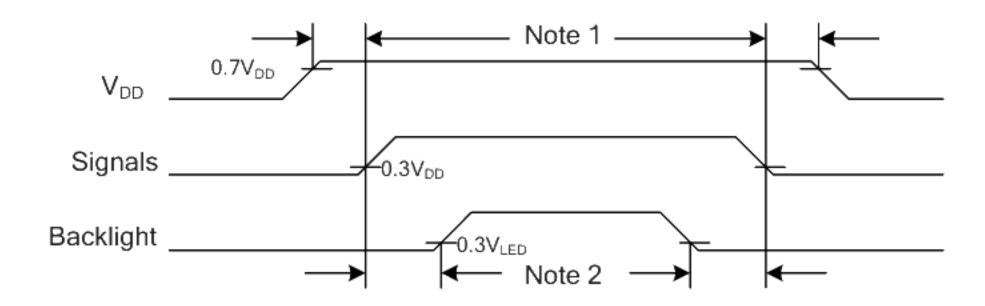


Fig. 8.4 Power Sequence Timing

- Note 1: In order to avoid any damages, V_{DD} has to be applied before all other signals. The opposite is true for power Off where V_{DD} has to be remained on until all other signals have been switch off. The recommended time period is 1 second. Hot plugging might cause display damage due to incorrect power sequence, please pay attention on interface connecting before power on.
- Note 2: In order to avoid showing uncompleted patterns in transient state. It is recommended that switching the backlight on is delayed for 1 second after the signals have been applied. The opposite is true for power Off where the backlight has to be switched off 1 second before the signals are removed.

8.6 SCAN DIRECTION

Scan direction is available to be switched as below by setting CN1's LR & UD pin.



L/R : L, U/D : L(default)



L/R:H,U/D:L



L/R:L, U/D:H



L/R:H,U/D:H

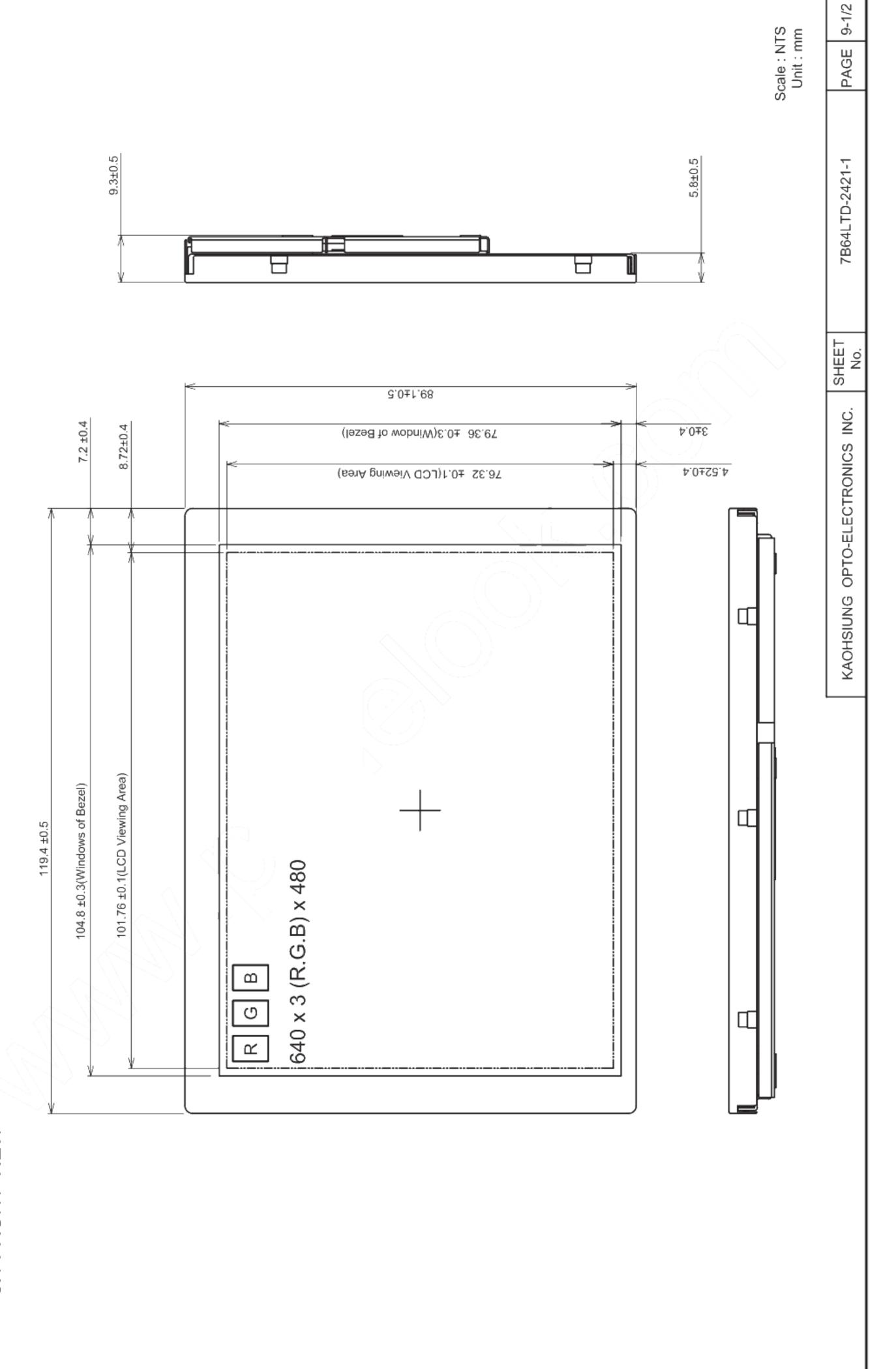
8.7 DATA INPUT for DISPLAY COLOR

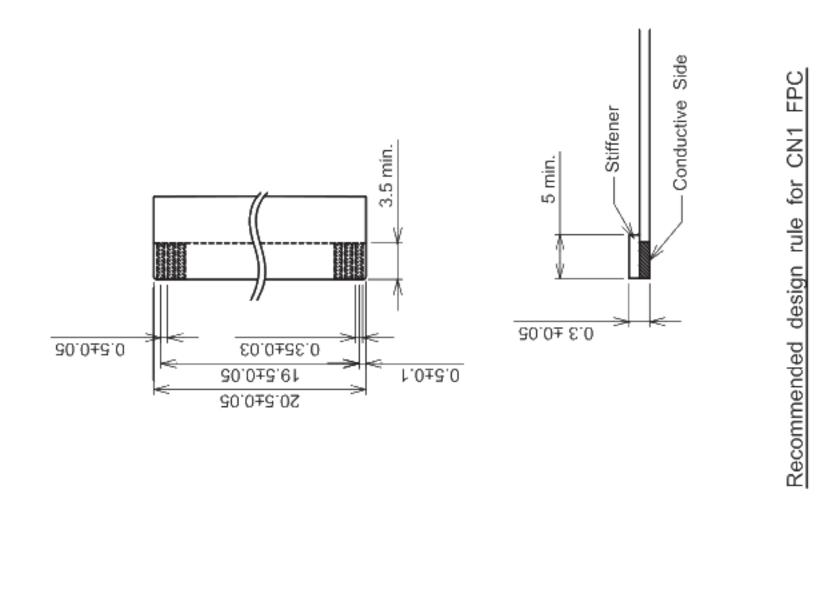
	COLOR &		Data Signal																
	Gray Scale	R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	В4	ВЗ	B2	В1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green (0)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue (0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1,	-(1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (62)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red (61)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	:	:	:	:	:	:	:	:	:	:	•	ς;	:	ः	:	:	:	:	:
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	Red (1)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (62)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green (61)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Green	:	:	:	:	:	:/	:	À.	<u> </u>	:	:	:	:	:	:	:	:	:	:
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	Green (1)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green (0)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue (62)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue (61)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue	: (7:	$\stackrel{\sim}{\cdot}$:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
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	Blue (1)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue (0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

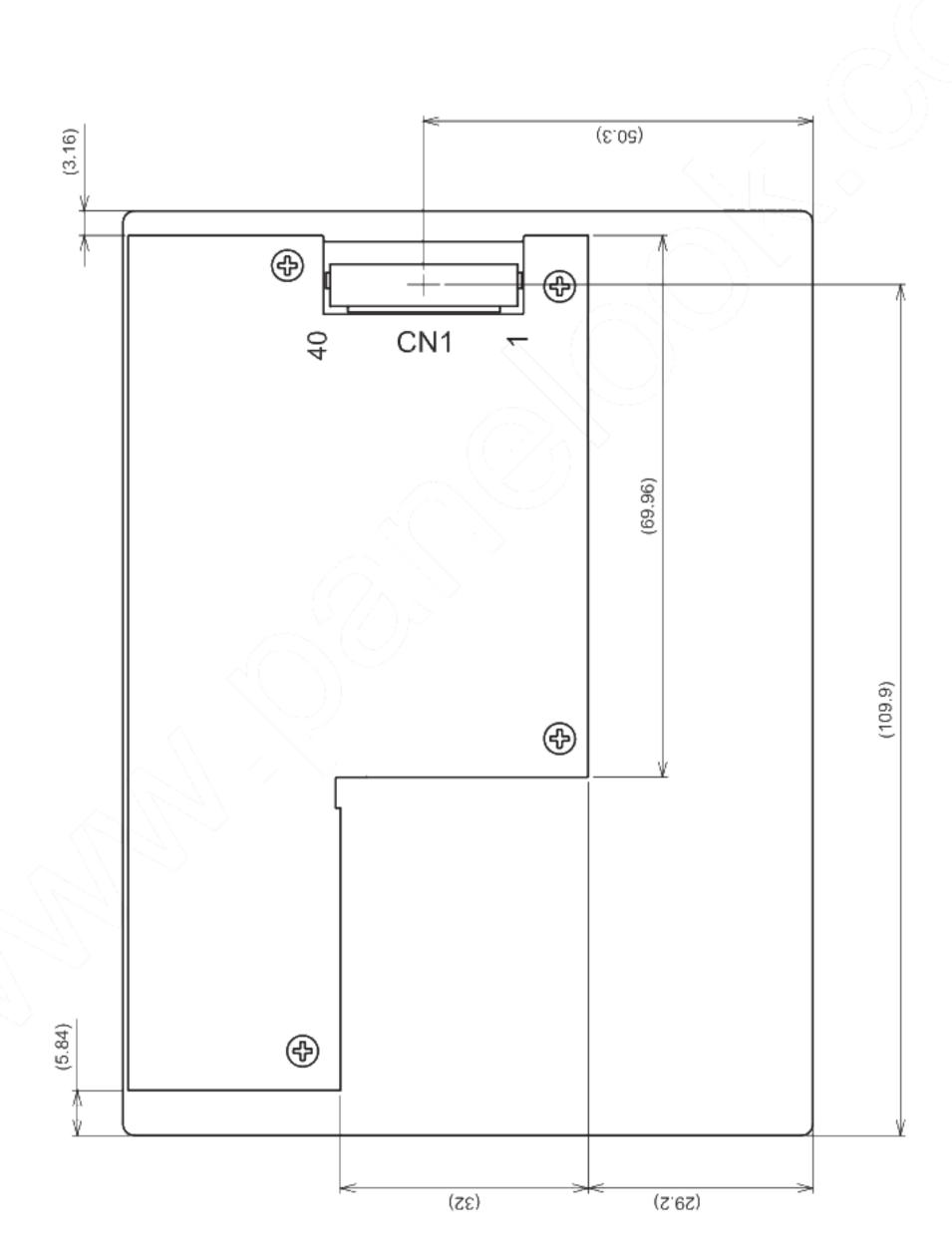
SHEET NO.

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9. OUTLINE DIMENSIONS 9.1 FRONT VIEW







Scale: NTS Unit: mm PAGE 7B64LTD-2421-1

SHEET

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No.

9-2/2